

Lab3

EECS4312:
Software
Engineering
Requirements

To do
top.pvs
Submit

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EECS4312: Software Engineering Requirements

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Table of contents

Lab3

EECS4312:
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To do
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1 To do

2 top.pvs

3 Submit

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There are three problems you must attempt in the Lab.

- The date problem – discussed in detail in slides 06-SimpleTables (see svn)
- The pressure problem discussed in detail in slides 07-TimedASMs. You must use the Time theory that comes with this Lab.
- The Car Interlock problem discussed in detail in car-interlock-spec.pdf. You must also submit a requirements document **car-interlock.pdf**.

You must specify and prove three theories as shown in the `top.pvs` file below:

```
% proveit --importchain --clean top.pvs
top : THEORY
BEGIN
    IMPORTING Time
    IMPORTING date
    IMPORTING pressure
    IMPORTING car_interlock
END top
```

See the `top.summary` file listing all the specification proofs you must complete.

Submit your work 1

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- Remove all files from your `4312-lab3` directory other than ***.pvs** and ***.prf**. files and `car-interlock.pdf`
- Run the following command in the directory:
- `proveit --importchain --clean top.pvs`
(see previous slide for summary file)
- All theorems must be proven.

Submit your work 2

Lab3

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- Now submit your 4312-lab3 directory:

```
> submit 4312 lab3 4312-lab3
```
- You will get confirmation of your submission.
- Ensure that you follow the instructions (and naming conventions) carefully and precisely to ensure that your submission can be checked.
- You must also print out your requirements document `car-interlock.pdf` and submit a printed copy in the course drop box. Place your name and Prism login on the document. The document must be professionally prepared.
- To obtain a grade on your quiz, you must complete and submit this Lab according to the instructions.