

UE18EC201

Network Analysis and Synthesis

Assignment IV: Two Ports

Deadline: 13 November 2019, 1300 Hours

1. Problems from [1]:

(a) Chapter 11: 11-1, 11-2, 11-4, 11-5, 11-9, 11-11, 11-13, 11-15, 11-17, 11-18, 11-21, 11-29, 11-38.

2. Practice problems from [2] (These problems are available in the accompanying documents.):

(a) Chapter 17: 6, 11, 12, 15, 17, 36, 37, 41, 42, 45, 49, 51, 53.

References

- [1] M. E. Van Valkenburg, *Network Analysis*, 3rd Edition, Prentice Hall, 1975 (PHI/Pearson Education (Indian Edition), 2015).
- [2] W. H. Hayt, Jr., J. E. Kemmerly, and S. M. Durbin, *Engineering Circuit Analysis*, 8th Edition, McGraw-Hill, 2012.