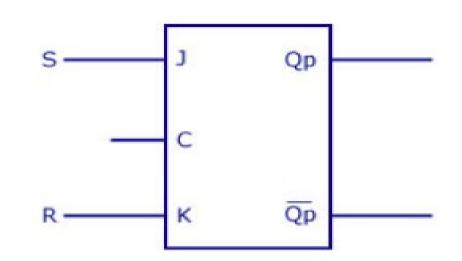


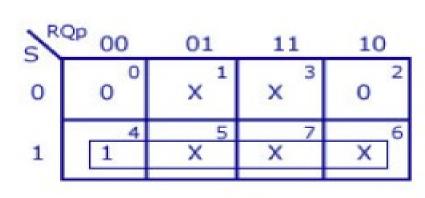
J-K Flip Flop to S-R Flip Flop

Conversion Table

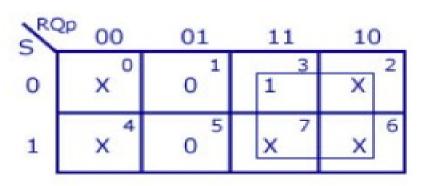
Logic Diagram

S-R Inputs S R		Outputs Qp Qp+1		J-K Inputs J K	
0	0	0	0	0	X
0	0	1	1	X	0
0	1	0	0	0	X
0	1	1	0	X	1
1	0	0	1	1	X
1	0	1	1	X	0
1	1	Invalid		Dor	nt care
1	1	Invalid		Dor	nt care





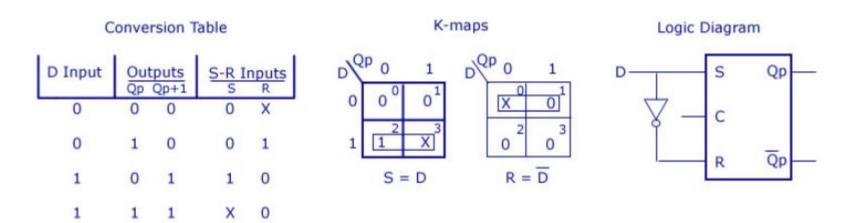
1=5



K-maps

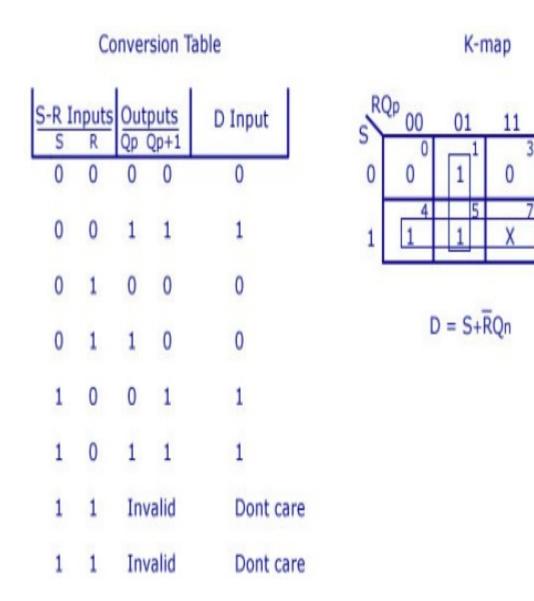
K=R

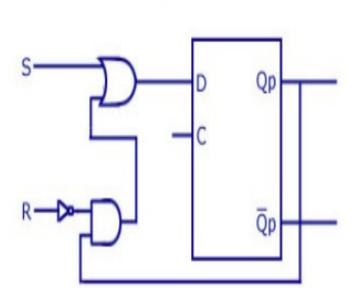
S-R Flip Flop to D Flip Flop



D Flip Flop to S-R Flip Flop

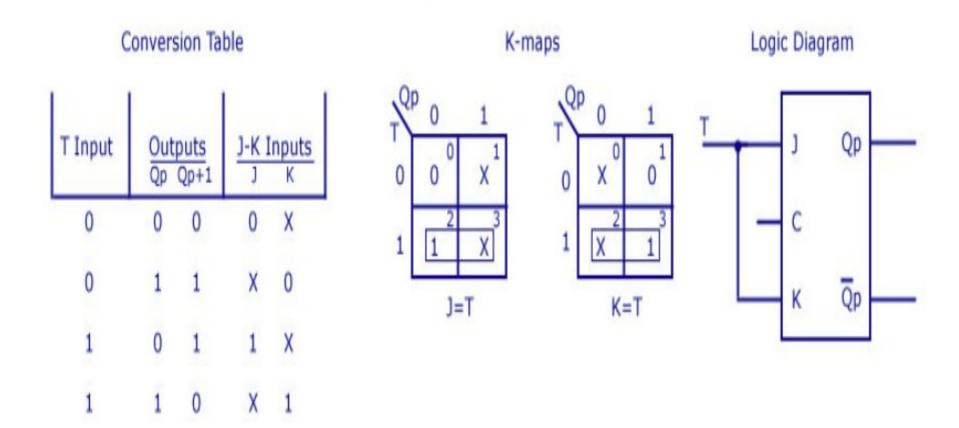
10



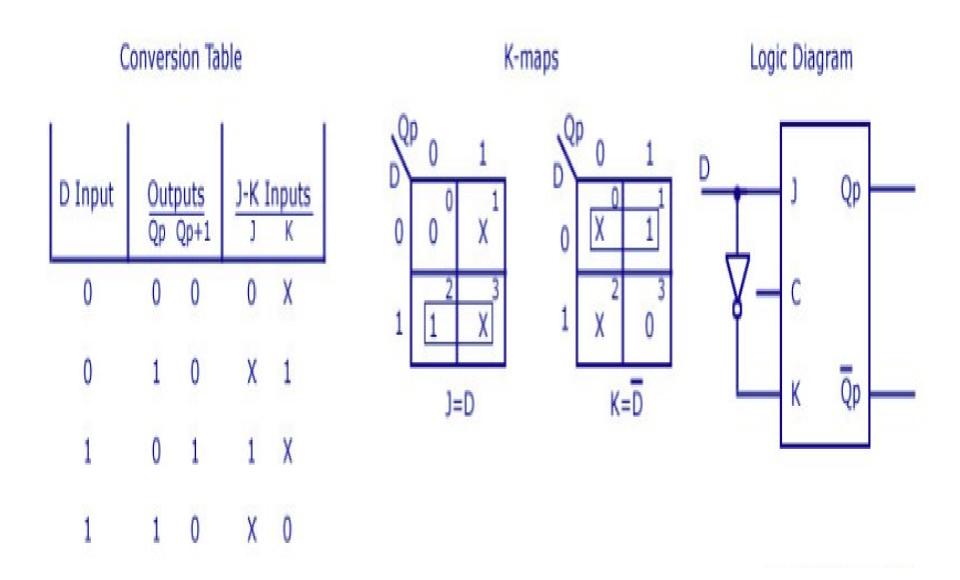


Logic Diagram

J-K Flip Flop to T Flip Flop



J-K Flip Flop to D Flip Flop

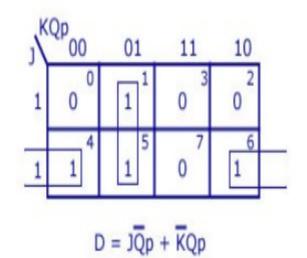


D Flip Flop to J-K Flip Flop

Conversion Table

J-K Input		Ou Qp	tputs Qp+1	D Input	
0	0	0	0	0	
0	0	1	1	1	
0	1	0	0	0	
0	1	1	0	0	
1	0	0	1	1	
1	0	1	1	1	
1	1	0	1	1	
1	1	1	0	0	

K-map



Logic Diagram

