

COMPUTATIONAL PHYSICS: INCLUDES PARALLEL COMPUTING/PARALLEL PROGRAMMING

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ABSTRACT. Everything about Computational Physics, including Parallel computing/ Parallel programming.

Part 1. Introduction

1. PARALLEL COMPUTING

1.1. **Udacity Intro to Parallel Programming : Lesson 1 - The GPU Programming Model.** Owens and Luebki pound fists at the end of this video. =)))) [Intro to the class](#).

1.1.1. *Running CUDA locally.* Also, [Intro to the class](#), in Lesson 1 - The GPU Programming Model, has links to documentation for running CUDA locally; in particular, for Linux: <http://docs.nvidia.com/cuda/cuda-getting-started-guide-for-linux/index.html>. That guide told me to go download the NVIDIA CUDA Toolkit, which is the <https://developer.nvidia.com/cuda-downloads>.

For *Fedora*, I chose Installer Type `runfile` (`local`).

Afterwards, installation of CUDA on Fedora 23 workstation had been nontrivial. Go see either my github repository [ML-grabbag](#) (which will be updated) or my [wordpress blog](#) (which may not be upgraded frequently).

$P = VI = I^2R$ heating.

1.1.2. *Definitions of Latency and throughput (or bandwidth).* cf. [Building a Power Efficient Processor](#)

[Latency vs Bandwidth](#)

latency [sec]. From the title “Latency vs. bandwidth”, I’m thinking that throughput = bandwidth (???). throughput = job/time (of job).

Given total task, velocity v ,

total task / v = latency. throughput = latency/(jobs per total task).

Also, in [Building a Power Efficient Processor](#). Owens recommends the article David Patterson, “Latency...”

cf. [GPU from the Point of View of the Developer](#)

$n_{\text{core}} \equiv$ number of cores

$n_{\text{vecop}} \equiv (n_{\text{vecop}}\text{--wide axial vector operations}/\textit{core core})$

$n_{\text{thread}} \equiv$ threads/core (hyperthreading)

$n_{\text{core}} \cdot n_{\text{vecop}} \cdot n_{\text{thread}}$ parallelism

There were various websites that I looked up to try to find out the capabilities of my video card, but so far, I’ve only found these commands (and I’ll print out the resulting output):

```
$ lspci -vnn | grep VGA -A 12
03:00.0 VGA compatible controller [0300]: NVIDIA Corporation GM200 [GeForce GTX 980 Ti] [10de:17c8] (rev a1) (prog-if 00 [VGA controller])
Subsystem: eVga.com. Corp. Device [3842:3994]
Physical Slot: 4
Flags: bus master, fast devsel, latency 0, IRQ 50
Memory at fa000000 (32-bit, non-prefetchable) [size=16M]
Memory at e0000000 (64-bit, prefetchable) [size=256M]
Memory at f0000000 (64-bit, prefetchable) [size=32M]
I/O ports at e000 [size=128]
```

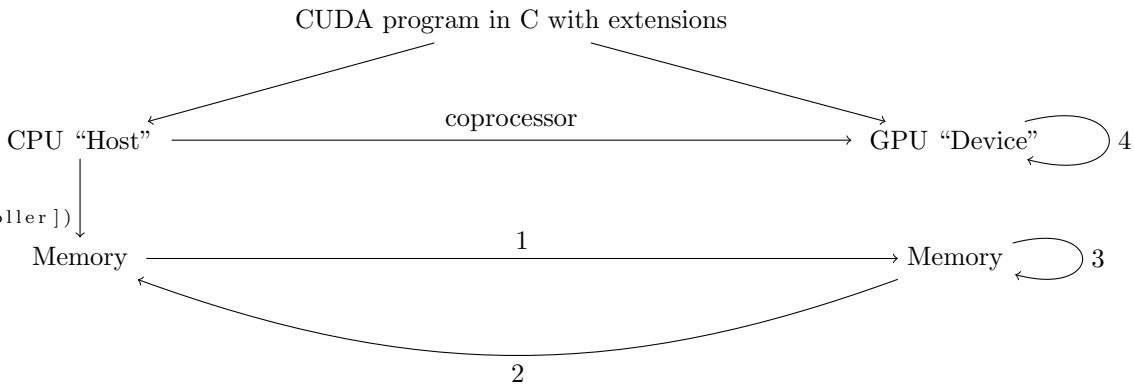
```
[virtual] Expansion ROM at fb000000 [disabled] [size=512K]
Capabilities: <access denied>
Kernel driver in use: nvidia
Kernel modules: nouveau, nvidia

$ lspci | grep VGA -E
03:00.0 VGA compatible controller: NVIDIA Corporation GM200 [GeForce GTX 980 Ti] (rev a1)

$ grep driver /var/log/Xorg.0.log
[ 18.074] Kernel command line: BOOT_IMAGE=/vmlinuz-4.2.3-300.fc23.x86_64 root=/dev/mapper/fedora-root ro rd.lvm.lv=fedora
[ 18.087] (WW) Hotplugging is on, devices using drivers 'kbd', 'mouse' or 'vmmouse' will be disabled.
[ 18.087] X.Org XInput driver : 22.1
[ 18.192] (II) Loading /usr/lib64/xorg/modules/drivers/nvidia_drv.so
[ 19.088] (II) NVIDIA(GPU-0): Found DRM driver nvidia-drm (20150116)
[ 19.102] (II) NVIDIA(0): ACPI event daemon is available, the NVIDIA X driver will
[ 19.174] (II) NVIDIA(0): [DRI2] VDPAU driver: nvidia
[ 19.284] ABI class: X.Org XInput driver, version 22.1
...

$ lspci -k | grep -A 8 VGA
03:00.0 VGA compatible controller: NVIDIA Corporation GM200 [GeForce GTX 980 Ti] (rev a1)
Subsystem: eVga.com. Corp. Device 3994
Kernel driver in use: nvidia
Kernel modules: nouveau, nvidia
03:00.1 Audio device: NVIDIA Corporation GM200 High Definition Audio (rev a1)
Subsystem: eVga.com. Corp. Device 3994
Kernel driver in use: snd_hda_intel
Kernel modules: snd_hda_intel
05:00.0 USB controller: VIA Technologies, Inc. VL805 USB 3.0 Host Controller (rev 01)
```

CUDA Program Diagram



CPU “host” is the boss (and issues commands) -Owen.

Coprocessor : CPU “host” → GPU “device”

Coprocessor : CPU process ↦ (co)-process out to GPU

With

- 1 data cpu → gpu
- 2 data gpu → cpu (initiated by cpu host)

- 1., 2., uses `cudaMemcpy`
- 3 allocate GPU memory: `cudaMalloc`
- 4 launch kernel on GPU

Remember that for 4., this launching of the kernel, while it’s acting on GPU “device” onto itself, it’s initiated by the boss, the CPU “host”.

Hence, cf. [Quiz: What Can GPU Do in CUDA](#), GPUs can respond to CPU request to receive and send Data CPU → GPU and Data GPU → CPU, respectively (1,2, respectively), and compute a kernel launched by the CPU (3).

[A CUDA Program](#) A typical GPU program

- `cudaMalloc` - CPU allocates storage on GPU
- `cudaMemcpy` - CPU copies input data from CPU → GPU
- *kernel launch* - CPU launches kernel(s) on GPU to process the data
- `cudaMemcpy` - CPU copies results back to CPU from GPU

Owens advises minimizing “communication” as much as possible (e.g. the `cudaMemcpy` between CPU and GPU), and do a lot of computation in the CPU and GPU, each separately.

[Defining the GPU Computation](#)

Owens circled this

BIG IDEA

This is Important

Kernels look like serial programs

Write your program as if it will run on **one** thread

The GPU will run that program on **many** threads

[Squaring A Number on the CPU](#)

Note

- (1) Only 1 thread of execution: (“thread” := one independent path of execution through the code) e.g. the `for` loop
- (2) no explicit parallelism; it’s serial code e.g. the `for` loop through 64 elements in an array

[GPU Code A High Level View](#)

CPU:

- Allocate Memory
- Copy Data to/from GPU
- Launch Kernel - species degree of parallelism

GPU:

- Express Out = In · In - says *nothing* about the degree of parallelism

Owens reiterates that in the GPU, everything looks serial, but it’s only in the CPU that anything parallel is specified. pseudocode: CPU code: square kernel <<< 64 >>> (outArray,inArray)

[Squaring Numbers Using CUDA Part 3](#)

From the example

```
// launch the kernel
square<<<1, ARRAY_SIZE>>>(d_out , d_in)
```

we’re introduced to the “CUDA launch operator”, initiating a kernel of 1 block of 64 elements (ARRAY_SIZE is 64) on the GPU. Remember that `d_` prefix (this is naming convention) tells us it’s on the device, the GPU, solely.

With CUDA launch operator $\equiv <<< >>>$, then also looking at this explanation on [stackexchange](#) (so surely others are confused as well, of those who are learning this (cf. [CUDA kernel launch parameters explained right?](#)). From [Eric](#)’s answer,

threads are grouped into blocks. all the threads will execute the invoked kernel function.

Certainly,

$$<<< >>>: (n_{\text{block}}, n_{\text{threads}}) \times \text{kernelfunctions} \mapsto \text{kernelfunction} <<< n_{\text{block}}, n_{\text{threads}} >>> \in \text{End} : \text{Dat}_{\text{GPU}}$$
$$<<< >>>: \mathbb{N}^+ \times \mathbb{N}^+ \times \text{Mor}_{\text{GPU}} \rightarrow \text{EndDat}_{\text{GPU}}$$

where I propose that GPU can be modeled as a category containing objects Dat_{GPU} , the collection of all possible data inputs and outputs into the GPU, and Mor_{GPU} , the collection of all kernel functions that run (exclusively, and this *must* be the class, as reiterated by Prof. Owen) on the GPU.

Next,

kernelfunction <<< $n_{\text{block}}, n_{\text{threads}}$ >>>: $\text{din} \mapsto \text{dout}$ (as given in the “square” example, and so I propose)

$$\text{kernelfunction} <<< n_{\text{block}}, n_{\text{threads}} >>>: (\mathbb{N}^+)^{n_{\text{threads}}} \rightarrow (\mathbb{N}^+)^{n_{\text{threads}}}$$

But keep in mind that `dout`, `din` are pointers in the C program, pointers to the place in the memory.

`cudaMemcpy` is a functor category, s.t. e.g. $\text{Obj}_{\text{CudaMemcpy}} \ni \text{cudaMemcpyDeviceToHost}$ where

$$\text{cudaMemcpy}(-, -, n_{\text{thread}}, \text{cudaMemcpyDeviceToHost}) : \text{Memory}_{\text{GPU}} \rightarrow \text{Memory}_{\text{CPU}} \in \text{Hom}(\text{Memory}_{\text{GPU}}, \text{Memory}_{\text{CPU}})$$

[Squaring Numbers Using CUDA 4](#)

Note the C language construct *declaration specifier* - denotes that this is a kernel (for the GPU) and not CPU code. Pointers need to be allocated on the GPU (otherwise your program will crash spectacularly -Prof. Owen).

1.1.3. *What are C pointers?* Is $\langle \text{type} \rangle *$, a pointer, then a mapping from the category, namely the objects of types, to a mapping from the specified value type to a memory address?

e.g.

$$\langle \rangle * : \text{float} \mapsto \text{float} *$$
$$\text{float} * : \text{din} \mapsto \text{some memory address}$$

and then we pass in mappings, not values, and so we’re actually declaring a square *functor*.

What is `threadIdx`? What is it mathematically? Consider that $\exists 3$ “modules”:

$$\text{threadIdx}.x$$
$$\text{threadIdx}.y$$
$$\text{threadIdx}.z$$

And then the line

```
int idx = threadIdx.x;
```

says that `idx` is an integer, “declares” it to be so, and then assigns `idx` to `threadIdx.x` which surely has to also have the same type, integer. So (perhaps)

$$idx \equiv \text{threadIdx}.x \in \mathbb{Z}$$

is the same thing.

Then suppose $\text{threadIdx} \subset \text{FinSet}$, a subcategory of the category of all (possible) finite sets, s.t. `threadIdx` has 3 particular morphisms, $x, y, z \in \text{MorthreadIdx}$,

$$x : \text{threadIdx} \mapsto \text{threadIdx}.x \in \text{Obj}_{\text{FinSet}}$$
$$y : \text{threadIdx} \mapsto \text{threadIdx}.x \in \text{Obj}_{\text{FinSet}}$$
$$z : \text{threadIdx} \mapsto \text{threadIdx}.x \in \text{Obj}_{\text{FinSet}}$$

[Configuring the Kernel Launch Parameters Part 1](#)

$n_{\text{blocks}}, n_{\text{threads}}$ with $n_{\text{threads}} \geq 1024$ (this maximum constant is GPU dependent). You should pick the $(n_{\text{blocks}}, n_{\text{threads}})$ that makes sense for your problem, says Prof. Owen.

1.1.4. *More thoughts on Squaring Numbers Using CPU, and then using CUDA.* Note that this squaring of numbers is really element-wise multiplication of a vector.

I sought an isomorphism between abstract algebra and computer code.

Consider

$$\begin{aligned}\mathbb{R}^N \ni x & \quad N \in \mathbb{Z}^+ \\ \mathbb{R} \ni x[i] & \quad i = 1 \dots N \rightarrow i = 0, \dots N - 1\end{aligned}$$

Then the element-wise squaring of numbers is

$$(x[i])^2 = x[i] \cdot x[i]$$

In general,

$$(x[i])^p = \underbrace{x[i] \cdot x[i] \dots x[i]}_{p \text{ times}}$$

1.1.5. *Memory layout of blocks and threads.* $\forall (n_{\text{blocks}}, n_{\text{threads}}) \in \mathbb{Z} \times \{1 \dots 1024\}$, $\{1 \dots n_{\text{block}} \times \{1 \dots n_{\text{threads}}\}$ is now an ordered index (with lexicographical ordering). This is just 1-dimensional (so possibly there’s a 1-to-1 mapping to a finite subset of \mathbb{Z}).

I propose that “adding another dimension” or the 2-dimension, that Prof. Owen mentions is being able to do the Cartesian product, up to 3 Cartesian products, of the block-thread index.

Quiz: Configuring the Kernel Launch Parameters 2

Most general syntax:

Configuring the kernel launhc

```
kernel<<<grid of blocks , block of threads >>>(...)
```

```
// for example
```

```
square<<<dim3(bx,by,bz) , dim3(tx,ty,tz) , shmem>>>(...)
```

where `dim3(tx,ty,tz)` is the grid of blocks $bx \cdot by \cdot bz$

`{dim3}(tx,ty,tz)` is the block of threads $tx \cdot ty \cdot tz$

`shmem` is the shared memory per block in bytes

Quiz: Map

I wanted to try to mathematically formulate the idea of `map`.

$$\begin{aligned}\text{MAP}(\text{ELEMENTS}, \text{FUNCTION}) &\iff && \text{given } x \in \mathbb{R}^N \\ &&& x[i] \xrightarrow{f} f(x[i]) \\ &&& \text{or} \\ &&& \text{set of elements (finite, so can be indexed)} \quad \{x_0, \dots, x_{n-1}\}_{\mathcal{A}} \in \text{ObjFin}\end{aligned}$$

$$x_i \xrightarrow{f} f(x_i), \quad \forall i \in \mathcal{A}$$

Problem Set 1 “Also, the image is represented as an 1D array in the kernel, not a 2D array like I mentioned in the video.”

Here’s part of that code for squaring numbers:

```
--global-- void square(float *d_out , float *d_in) {
    int idx = threadIdx.x;
    float f = d_in[idx];
    d_out[idx] = f*f;
}
```

1.1.6. *Problem Set 1, Udacity CS344.* Let $L_x \equiv$ total number of pixels in x -direction of image $\in \mathbb{Z}^+$

$L_y \equiv$ total number of pixels in y -direction of image $\in \mathbb{Z}^+$

and so $L_x L_y =$ total number of pixels in image.

The formula for ensuring that all threads will be computed, given an arbitrary choice of the number of threads in a (single) block, is the following:

$$\begin{aligned}\frac{L_x + (M_x - 1)}{M_x} = N_x \in \mathbb{N} & \quad N_x = \text{number of (thread) blocks in } x\text{-direction} \\ \frac{L_y + (M_y - 1)}{M_y} = N_y \in \mathbb{N} & \quad N_y = \text{number of (thread) blocks in } y\text{-direction}\end{aligned}$$

Then

$$(M_x, M_y, 1) \in \mathbb{N}^3 \iff \text{dim3}$$

needs to be determined manually, empirically, and in consideration of the actual GPU hardware architecture (look up number of CUDA cores, and allowed maximum threads), where

$M_x \equiv$ number of threads per block in x -direction

$M_y \equiv$ number of threads per block in y -direction

Consider that we want to go from the indices on each thread per block, on each block on the grid, in each of the 2 dimensions, to a global 2-dimensional position, and then “flatten” these coordinates to a 1-dimensional array that CUDA C can load onto global memory. In other words, for

$$\begin{aligned}i_x \in \{0, \dots, M_x - 1\} & \iff \text{threadIdx.x} \\ i_y \in \{0, \dots, M_y - 1\} & \iff \text{threadIdx.y} \\ j_x \in \{0, \dots, N_x - 1\} & \iff \text{blockIdx.x} \\ j_y \in \{0, \dots, N_y - 1\} & \iff \text{blockIdx.y}\end{aligned}$$

and so for

$$\begin{aligned}(k_x, k_y) \\ k_X = i_x + j_x M_x \\ k_y = i_y + j_y M_y\end{aligned}$$

then we sought the following operations:

$$\begin{aligned}(j_x, j_y) \times (i_x, i_y) &\in \{0, \dots, N_x - 1\} \times \{0 \dots N_y - 1\} \times \{0 \dots M_x - 1\} \times \{0 \dots M_y - 1\} \in \text{dim3} \times \text{dim3} \\ \mapsto (k_x, k_y) &\in \{0 \dots L_x - 1\} \times \{0 \dots L - y - 1\} \\ \mapsto k = k_x + L_x k_y &\in \{0 \dots L_x L_y - 1\}\end{aligned}$$

1.1.7. *Grid of blocks, block of threads, thread that’s indexed; (mathematical) structure of it all.* Let

$$\text{grid} = \prod_{I=1}^N (\text{block})^{n_I^{\text{block}}}$$

where $N = 1, 2, 3$ (for CUDA) and by naming convention

$$\begin{aligned}I = 1 &\equiv x \\ I = 2 &\equiv y \\ I = 3 &\equiv z\end{aligned}$$

Let’s try to make it explicitly (as others had difficulty understanding the grid, block, thread model, cf. **colored image to greyscale image using CUDA parallel processing, Cuda gridDim and blockDim**) through commutative diagrams and categories (from math):

$$\begin{array}{ccc}
 \prod_{I=1}^N \mathbb{Z}^+ & \ni (N_x^{\text{blocks}}, N_y^{\text{blocks}}, N_z^{\text{blocks}}) & \\
 \text{gridDim} \left(\begin{array}{c} \uparrow \\ \text{dim3} \\ \downarrow \end{array} \right) & \left(\begin{array}{c} \uparrow \\ \text{dim3} \\ \downarrow \end{array} \right) & (\text{gridDim}.x, \text{gridDim}.y, \text{gridDim}.z) \\
 \text{grid} & \ni \text{gridSize}(N_x^{\text{blocks}}, N_y^{\text{blocks}}, N_z^{\text{blocks}}) &
 \end{array}$$

$$\begin{array}{ccc}
 \text{grid} & \ni \text{d_rgbaImage} & \\
 \downarrow \text{blockIdx} & \downarrow & (\text{blockIdx}.x, \text{blockIdx}.y, \text{blockIdx}.z) \\
 \prod_{I=1}^N \mathbb{Z} \supset \prod_{I=1}^N \{1 \dots N_I^{\text{blocks}}\} & \ni (i^{\text{blocks}}, j^{\text{blocks}}, k^{\text{blocks}}) &
 \end{array}$$

and then similar relations (i.e. arrows, i.e. relations) go for a block of threads:

$$\begin{array}{ccc}
 \prod_{I=1}^N \mathbb{Z}^+ & \ni (N_x^{\text{threads}}, N_y^{\text{threads}}, N_z^{\text{threads}}) & \\
 \text{blockDim} \left(\begin{array}{c} \uparrow \\ \text{dim3} \\ \downarrow \end{array} \right) & \left(\begin{array}{c} \uparrow \\ \text{dim3} \\ \downarrow \end{array} \right) & (\text{blockDim}.x, \text{blockDim}.y, \text{blockDim}.z) \\
 \text{block} & \ni \text{blockSize}(N_x^{\text{threads}}, N_y^{\text{threads}}, N_z^{\text{threads}}) & \\
 \downarrow \text{threadIdx} & \downarrow & (\text{threadIdx}.x, \text{threadIdx}.y, \text{threadIdx}.z) \\
 \prod_{I=1}^N \mathbb{Z} \supset \prod_{I=1}^N \{1 \dots N_I^{\text{threads}}\} & \ni (i^{\text{threads}}, j^{\text{threads}}, k^{\text{threads}}) &
 \end{array}$$

[gridsize help assignment 1 Pp](#) explains how threads per block is variable, and remember how Owens said Luebki says that a GPU doesn't get up for more than a 1000 threads per block.

1.1.1.8. *Generalizing the model of an image.* Consider vector space V , e.g. $\dim V = 4$, vector space V over field \mathbb{K} , so $V = \mathbb{K}^{\dim V}$. Each pixel represented by $\forall v \in V$.

Consider an image, or space, M . $\dim M = 2$ (image), $\dim M = 3$. Consider a local chart (that happens to be global in our case):

$$\begin{aligned}
 \varphi : M &\rightarrow \mathbb{Z}^{\dim M} \supset \{1 \dots N_1\} \times \{1 \dots N_2\} \times \dots \times \{1 \dots N_{\dim M}\} \\
 \varphi : x &\mapsto (x^1(x), x^2(x), \dots, x^{\dim M}(x))
 \end{aligned}$$

$$\begin{array}{ccc}
 E & \xrightarrow{\varphi} & M \times V \\
 \pi \downarrow & \swarrow & \\
 M & &
 \end{array}
 \qquad
 \begin{array}{ccc}
 E & \xrightarrow{\varphi} & \text{grid} \times \text{block of threads} \\
 \pi \downarrow & \swarrow & \\
 \text{grid} & &
 \end{array}$$

Consider a “coarsing” of underlying M :

$$\begin{array}{ccc}
 M \times V & \xrightarrow{\text{proj}} & \text{proj}(M) \times \text{proj}(V) \\
 \pi \downarrow & & \downarrow \text{proj}(\pi) \\
 M = \{1 \dots N_1\} \times \{1 \dots N_2\} \times \dots \times \{1 \dots N_{\dim M}\} & \xrightarrow{\text{proj}} & \text{proj}(M) = \{1 \dots \frac{N_1}{N_1^{\text{threads}}}\} \times \{1 \dots \frac{N_2}{N_2^{\text{threads}}}\} \times \dots \times \{1 \dots \frac{N_{\dim M}}{N_{\dim M}^{\text{threads}}}\}
 \end{array}$$

e.g. $N_1^{\text{thread}} = 12$

$N_2^{\text{thread}} = 12$

Just note that in terms of syntax, you have the “block” model, in which you allocate blocks along each dimension. So in

$$\begin{aligned}
 &\text{const dim3 blockSize}(n_x^b, n_y^b, n_z^b) \\
 &\text{const dim3 gridSize}(n_x^{\text{gr}}, n_y^{\text{gr}}, n_z^{\text{gr}})
 \end{aligned}$$

Then the condition is $n_x^b/\dim V, n_y^b/\dim V, n_z^b/\dim V \in \mathbb{Z}$ (condition), $(n_x^{\text{gr}} - 1)/\dim V, n_y^{\text{gr}}/\dim V, n_z^{\text{gr}}/\dim V \in \mathbb{Z}$

1.2. Unit 2, Lesson 2 GPU Hardware and Parallel Communication Patterns. [Transpose Part 1](#)

Now

$$\text{Mat}_{\mathbb{F}}(n, n) \xrightarrow{T} \text{Mat}_{\mathbb{F}}(n, n)$$

$$A \mapsto A^T \text{ s.t. } (A^T)_{ij} = A_{ji}$$

$$\text{Mat}_{\mathbb{F}} \xrightarrow{T} \mathbb{F}^{n^2}$$

$$A_{ij} \mapsto A_{ij} = A_{in+j}$$

$$\begin{array}{ccc}
 \text{Mat}_{\mathbb{F}}(n, n) & \longrightarrow & \mathbb{F}^{n^2} \\
 T \downarrow & & \downarrow T \\
 \text{Mat}_{\mathbb{F}}(n, n) & \longrightarrow & \mathbb{F}^{n^2}
 \end{array}
 \qquad
 \begin{array}{ccc}
 A_{ij} & \longmapsto & A_{in+j} \\
 T \downarrow & & \downarrow T \\
 (A^T)_{ij} = A_{ji} & \longmapsto & A_{jn+i}
 \end{array}$$

[Transpose Part 2](#)

Possibly, transpose is a functor.

Consider struct as a category. In this special case, $\text{Objstruct} = \{\text{arrays}\}$ (a struct of arrays). Now this struct already has a hash table for indexing upon declaration (i.e. “creation”): so this category struct will need to be equipped with a “diagram” from the category of indices J to struct: $J \rightarrow \text{struct}$.

So possibly

$$\begin{array}{ccc}
 \text{struct} & \xrightarrow{T} & \text{array} \\
 \text{ObjStruct} = \{ \text{arrays} \} & \xrightarrow{T} & \text{Objarray} = \{ \text{struct} \} \\
 J \rightarrow \text{struct} & \xrightarrow{T} & J \rightarrow \text{array}
 \end{array}$$

Quiz: What Kind Of Communication Pattern This quiz made a few points that clarified the characteristics of these so-called communication patterns (amongst the memory?)

- map is bijective, and $\text{map} : \text{Idx} \rightarrow \text{Idx}$
- gather - not necessarily surjective
- scatter - not necessarily surjective
- stencil - surjective
- transpose (see before)

Parallel Communication Patterns Recap

- map - bijective
- transpose - bijective
- gather - not necessarily surjective, and is many-to-one (by def.)
- scatter - one-to-many (by def.) and is not necessarily surjective
- stencil - several-to-one (not injective, by definition), and is surjective
- reduce - all-to-one
- scan/sort - all-to-all

Programmer View of the GPU

thread blocks: group of threads that cooperate to solve a (sub)problem

Thread Blocks And GPU Hardware

CUDA GPU is a bunch of SMs:

Streaming Multiprocessors (SM)s

SMs have a bunch of simple processors and memory.

Dr. Luebki:

Let me say that again because it’s really important
GPU is responsible for allocating blocks to SMs

Programmer only gives GPU a pile of blocks.

Quiz: What Can The Programmer Specify

I myself thought this was a revelation and was not intuitive at first:

Given a single kernel that’s launched on many thread blocks include X , Y , the programmer cannot specify the sequence the blocks, e.g. block X , block Y , run (same time, or run one after the other), and which SM the block will run on (GPU does all this).

Quiz: A Thread Block Programming Example

Open up `hello_blockIdx.cu` in Lesson 2 Code Snippets (I got the repository from github, repo name is cs344).

At first, I thought you can do a single file compile and run in Eclipse without creating a new project. No. cf. **Eclipse creating projects every time to run a single file?**.

I ended up creating a new CUDA C/C++ project from File -> New project, and then chose project type Executable, Empty Project, making sure to include Toolchain CUDA Toolkit (my version is 7.5), and chose an arbitrary project name (I chose cs344single). Then, as suggested by **Kenny Nguyen**, I dragged and dropped files into the folder, from my file directory program.

I ran the program with the “Play” triangle button, clicking on the green triangle button, and it ran as expected. I also turned off Build Automatically by deselecting the option (no checkmark).

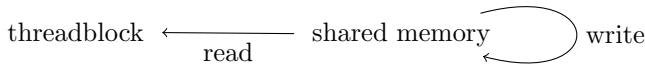
GPU Memory Model



Then consider $\text{threadblock} \equiv \text{thread block}$

$\text{Objthreadblock} \supset \{ \text{threads} \}$

$\text{FinSet} \xrightarrow{\text{threadIdx}} \text{thread} \in \text{Morthreadblock}$



\forall thread,



Synchronization - Barrier

Danger: what if a thread reads a result before another thread writes it?

Threads need to *synchronize*.

one of the most fundamental problems in parallel computing

Quiz: The Need For Barriers

3 barriers were needed (wasn’t obvious to me at first). All threads need to finish the write, or initialization, so it’ll need a barrier.

While

```
array[idx] = array[idx+1];
```

is 1 line, it’ll actually need 2 barriers; first read. Then write.

So *actually* we’ll need to *rewrite* this code:

```
int temp = array[idx+1];
__syncthreads();
array[idx] = temp;
__syncthreads();
```

Make sure each *read* and *write* operation is completed.

kernels have implicit barrier for each.

Writing Efficient Programs

(1) Maximize *arithmetic intensity* $\text{arithmetic intensity} := \frac{\text{math}}{\text{memory}}$

video: Minimize Time Spent On Memory

local memory is fastest; global memory is slower

local > shared >> global >> CPU

kernel we know (in the code) is tagged with `__global__`

1.2.1. Coalesce global memory accesses. Coalesce Memory Access

We saw such access pattern is coalesced; GPU must efficient when threads read or write contiguous memory locations.

quiz: A Quiz on Coalescing Memory Access

Work it out as Dr. Luebki did to figure out if it’s coalesced memory access or not.

Atomic Memory Operations

Atomic Memory Operations

atomicadd atomicmin atomicXOR atomicCAS Compare And Swap

1.2.2. *On Problem Set 2.* There is what I call the “naive global memory” scheme, that solves the objective of blurring a photo with a local stencil of the values, using only global memory on the GPU.

Given image of size $L_x \times L_y$, i.e. $(L_x, L_y) \in (\mathbb{Z}^+)^2$; image is really a designated or particular mapping f ,

$$f : \{0 \dots L_x - 1\} \times \{0 \dots L_y - 1\} \rightarrow \{0 \dots 255\}^4$$

$$f(x, y) = (f^{(r)}(x, y), f^{(b)}(x, y), f^{(g)}(x, y), f^{(\alpha)}(x, y))$$

Consider “naive global memory scheme” - establishing the following notation:

$$\begin{aligned} i_x &\in \{0 \dots M_x - 1\} \iff \texttt{threadIdx.x} \\ i_y &\in \{0 \dots M_y - 1\} \iff \texttt{threadIdx.y} \\ j_x &\in \{0 \dots N_x - 1\} \iff \texttt{blockIdx.x} \\ j_y &\in \{0 \dots N_y - 1\} \iff \texttt{blockIdx.y} \\ M_x &\in \{1 \dots 1024\} \iff \texttt{blockDim.x} \\ M_y &\in \{1 \dots 1024\} \iff \texttt{blockDim.y} \end{aligned}$$

with

$$\begin{aligned} N_x &:= (L_x + M_x - 1) / M_x \in \mathbb{Z}^+ \\ N_y &:= (L_y + M_y - 1) / M_y \in \mathbb{Z}^+ \end{aligned}$$

There should be a functor called “flatten” such that we end up with the image as a 1-dimensional, contiguous array on the global memory of the GPU; so for

$$k = k_x + k_y L_x \in \{0 \dots L_x L_y - 1\}$$

then

$$\begin{aligned} (k_x, k_y) &\iff (x, y) \in \{0 \dots L_x - 1\} \times \{0 \dots L_y - 1\} \xrightarrow{\texttt{flatten}} k \in \{0 \dots L_x L_y - 1\} \\ f : \{0 \dots L_x - 1\} \times \{0 \dots L_y - 1\} &\xrightarrow{\texttt{flatten}} f : \{0 \dots L_x L_y - 1\} \rightarrow \{0 \dots 255\}^4 \\ f(x, y) &= f(k_x, k_y) \xrightarrow{\texttt{flatten}} f(k) \end{aligned}$$

Then there should be a functor called “separateChannels” to represent the `__global__` kernel `separateChannels`.

$$\begin{aligned} f : \{0 \dots L_x L_y - 1\} &\rightarrow \{0 \dots 255\}^4 \xrightarrow{\texttt{separateChannels}} f^{(c)} : \{0 \dots L_x L_y - 1\} \rightarrow \{0 \dots 255\}, c = \{r, g, b\} \\ f(k) &\xrightarrow{\texttt{separateChannels}} f^{(c)}(k) \end{aligned}$$

Then consider a “stencil” of size `filterWidth` \times `filterWidth` $\iff W \times W \in (\mathbb{Z}^+)^2$.

Let $(\nu_x, \nu_y) \in \{0 \dots W - 1\}^2$ and so

$$\left(\nu_x - \frac{W}{2}, \nu_y - \frac{W}{2} \right) \in \left\{ \frac{-W}{2}, \dots, \frac{W}{2} - 1 \right\} \subset \mathbb{Z}$$

Now let

$$\begin{aligned} k_x^{\text{st}} &= k_x + \nu_x - \frac{W}{2} \iff \texttt{stencilindex.x} \\ k_y^{\text{st}} &= k_y + \nu_y - \frac{W}{2} \iff \texttt{stencilindex.y} \end{aligned}$$

with $k_x^{\text{st}} \in \{0 \dots L_x - 1\}$

$$k_y^{\text{st}} \in \{0 \dots L_y - 1\}$$

We also have to apply the flatten functor on the stencil:

$$(\nu_x, \nu_y) \in \{0 \dots W - 1\}^2 \xrightarrow{\texttt{flatten}} \nu = \nu_x + W\nu_y \in \{0 \dots W^2 - 1\}$$

And so the gist of the blurring operation is in this equation:

$$(1) \quad g^{(c)}(k) = \sum_{\nu_x=0}^{W-1} \sum_{\nu_y=0}^{W-1} c_{\nu=\nu_x+W\nu_y} f^{(c)}(k_x^{\text{st}} + L_x \cdot k_y^{\text{st}}) \quad \forall c = \{r, g, b\}$$

$$\begin{aligned} \text{with } k_x^{\text{st}} &= k_x^{\text{st}}(\nu_x) := k_x + \nu_x - \frac{W}{2} \\ k_y^{\text{st}} &= k_y^{\text{st}}(\nu_y) := k_y + \nu_y - \frac{W}{2} \end{aligned}$$

1.2.3. *Problem Set 2, shared memory “tiling” scheme.* I think the `__shared__` memory “tiling” scheme is non-trivial due to accounting for the values “at the edges” of the thread block, including the “corners” the so-called “halo” cells. Storing the value of the “cells” or threads within a thread block into shared memory is *relatively* straightforward - it is a 1-to-1 mapping. But taking care of the corner cases, due to the desired “stencil” for blurring, is nontrivial, I think.

Consider my scheme for “tiling” using shared memory:

Let

$$\begin{aligned} k_x &= i_x + j_x M_x \in \{0 \dots L_x - 1\} \\ k_y &= i_y + j_y M_y \in \{0 \dots L_y - 1\} \\ k_x &< L_x \text{ and } k_y < L_y \\ 0 &\leq k_x < L_x \text{ and } 0 \leq k_y < L_y \\ k &:= k_x + L_x k_y \end{aligned}$$

and let

$$\begin{aligned} S_x &:= M_x + 2r \\ S_y &:= M_y + 2r \\ s_x &:= i_x + r \\ s_y &:= i_y + r \\ 0 &\leq s_x < S_x \text{ and } 0 \leq s_y < S_y \\ s_k &:= s_x + S_x s_y \end{aligned}$$

where r is the “radius” or essentially the stencil size, out in 1-direction.

Loading the regular cells,

$$s_{\text{in}}[s_k] = f^{(c)}(k)$$

Loading the halo cells,

if $(i_x < r)$,
then requiring

$$\begin{aligned} 0 &\leq s_x - r < S_x & 0 &\leq k_x - r < L_x \\ 0 &\leq s_y < S_y & 0 &\leq k_y < L_y \\ s_{\text{in}}[s_x - r + S_x s_y] &= f^{(c)}[k_x - r + L_x k_y] \\ 0 &\leq s_x + M_x < S_x & 0 &\leq k_x + M_x < L_x \\ 0 &\leq s_y < S_y & 0 &\leq k_y < L_y \\ s_{\text{in}}[s_x + M_x + S_x s_y] &= f^{(c)}[k_x + M_x + L_x k_y] \end{aligned}$$

If $(i_y < r)$,

then requiring

$$\begin{aligned} 0 \leq s_x < S_x & \qquad 0 \leq k_x < L_x \\ 0 \leq s_y - r < S_y & \qquad 0 \leq k_y - r < L_y \\ s_{\text{in}}[s_x + S_x(s_y - r)] = f^{(c)}[k_x + L_x(k_y - r)] \\ 0 \leq s_x < S_x & \qquad 0 \leq k_x < L_x \\ 0 \leq s_y + M_y < S_y & \qquad 0 \leq k_y + M_y < L_y \\ s_{\text{in}}[s_x + S_x(s_y + M_y)] = f^{(c)}[k_x + L_x(k_y + M_y)] \end{aligned}$$

And now the actual stencil calculation:

$$\begin{aligned} \forall \nu_y \in \{\nu_y = 0, 1 \dots W - 1 | 0 \leq \nu_y < W\}, \\ k_y^{\text{st}} := s_y + \nu_y - r \\ \forall \nu_x \in \{\nu_x = 0, 1 \dots W - 1 | 0 \leq \nu_x < W\}, \\ k_x^{\text{st}} := s_x + \nu_x - r \end{aligned}$$

$$\text{inputvalue} = s_{\text{in}}[k_x^{\text{st}} + S_x k_y^{\text{st}}] \text{ with } 0 \leq k_x^{\text{st}} < S_x \\ 0 \leq k_y^{\text{st}} < S_y$$

$$\text{filtervalue} = c(\nu_x + W \nu_y) \\ \text{value} += \text{filtervalue} \cdot \text{inputvalue},$$

i.e.

$$g^{(c)}(k) = \sum_{\nu_y=0}^{W-1} \sum_{\nu_x=0}^{W-1} c_{\nu=\nu_x+W\nu_y} s_{\text{in}}[k_x^{\text{st}} + k_y^{\text{st}} S_x]$$

Unfortunately, the (literal) corner cases aren’t accounted for correctly, (when $i_x < r$ and $i_y < r$), as can be seen by the difference image and output image when it’s run.

[Samuel Lin or Samuel271828](#) had both an elegant and *correct* implementation. It’s also in [Samuel Lin or samuellin3310’s github repositories](#), in [student fuction improved share.cu \(sic\)](#).

Here it is, mathematically:

Let

$$\begin{aligned} s_{\text{in}} &\in \mathbb{R}^{(M_x+2r)(M_y+2r)} \\ k_x &= i_x + j_x M_x \in \mathbb{Z} \\ k_y &= i_y + j_y M_y \in \mathbb{Z} \\ k_x &< L_x \text{ and } k_y < L_y \\ 0 \leq k_x < L_x \text{ and } 0 \leq k_y < L_y \\ k &:= k_x + L_x k_y \end{aligned}$$

Then,

$$\begin{aligned} \forall i \in \{i = i_x - r, i_x - r + M_x, i_x - r + 2M_x, \dots | i_x - r \leq i < M_x + r\}, \\ \forall j \in \{j = i_y - r, i_y - r + M_y, i_y - r + 2M_y \dots | i_y - r \leq j < M_y + r\}, \\ l_x &:= i + M_x j_x \in \mathbb{Z} \text{ with (enforcing) } 0 \leq l_x < L_x \\ l_y &:= i + M_y j_y \in \mathbb{Z} \text{ with (enforcing) } 0 \leq l_y < L_y \\ s_{\text{in}}[i + r + (j + r)(M_x + 2r)] &= f^{(c)}(l_x + l_y L_x) \end{aligned}$$

Enforce $k_x < L_x$ and $k_y < L_y$, otherwise nothing happens.

And now the actual stencil calculation:

$$\begin{aligned} \forall \nu_y \in \{\nu_y = 0, 1 \dots W - 1 | 0 \leq \nu_y < W\}, \\ k_y^{\text{st}} := s_y + \nu_y - r \end{aligned}$$

$$\begin{aligned} \forall \nu_x \in \{\nu_x = 0, 1 \dots W - 1 | 0 \leq \nu_x < W\}, \\ k_x^{\text{st}} := s_x + \nu_x - r \end{aligned}$$

$$\text{inputvalue} = s_{\text{in}}[k_x^{\text{st}} + S_x k_y^{\text{st}}] \text{ with } 0 \leq k_x^{\text{st}} < S_x \\ 0 \leq k_y^{\text{st}} < S_y$$

$$\text{filtervalue} = c(\nu_x + W \nu_y) \\ \text{value} += \text{filtervalue} \cdot \text{inputvalue},$$

i.e.

$$g^{(c)}(k) = \sum_{\nu_y=0}^{W-1} \sum_{\nu_x=0}^{W-1} c_{\nu=\nu_x+W\nu_y} s_{\text{in}}[k_x^{\text{st}} + k_y^{\text{st}} S_x]$$

It may be non-intuitive, as was in my case, from my personal experience, to have to move the requirement that $k_x < L_x$, and $k_y < L_y$, i.e. the line

$$\text{if (k_x >= numCols || k_y >= numRows) \{ } \\ \text{return; \}$$

after loading all the values from global memory into shared memory, and not in the beginning of the kernel. This can be proven, in general. And again, shout-outs (i.e. credit should go) to [Samuel Lin or Samuel271828](#) for the clarifying discussion [here](#).

For simplicity, consider the 1-dimensional case, e.g. a 1-dimensional pixelated image, represented by a 1-dimensional array. The discussion below can easily be generalized to n -dimensions.

Recall that

$$k_x := i_x + M_x j_x$$

for

$$\begin{aligned} i_x &\in \{0 \dots M_x - 1\} \\ j_x &\in \{0 \dots N_x - 1\} \end{aligned}$$

for

$$\begin{aligned} i_x &\iff \text{threadIdx.x} \\ j_x &\iff \text{blockIdx.x} \\ M_x &\iff \text{blockDim.x} \end{aligned}$$

with N_x determined by a formula immediately below.

Then

$$k_x \in \{0 \dots N_x M_x - 1\}$$

with N_x being determined by

$$N_x := \frac{L_x + M_x - 1}{M_x} \in \mathbb{Z}$$

By integer division, this formula for N_x guarantees that the number of blocks in the grid is the lowest number that would guarantee that *all* the needed grid points are computed, i.e. N_x is the lowest number such that there are enough (i.e. minimal number of) threads that’ll compute all the needed grid points, or pixels, or computations, etc., by how integer division works. Note that

$$L_x \iff \text{numCols} \text{ and so } N_x \iff \text{gridDim.x}$$

Now

$$N_x M_x \geq L_x$$

meaning, that we could have the case where the very last (thread) block would have more threads than is needed to compute all the pixels.

e.g.

$$\begin{aligned} L_x &= 127 \\ M_x &= 128 \text{ (so } N_x = 1) \\ k_x &= i_x + 128 \cdot 0 = i_x \\ k_x &= 127 \end{aligned}$$

and so $k_x \geq L_x$, namely $127 \geq 127$. If we had, in the beginning, the line that returns nothing for the condition $k_x \geq L_x$, we won’t be including this case.

Now recall the **shared memory tiling scheme**, but in 1-dimension (for the purposes of this present discussion):

$$\forall \{i \in i_x - r, i_x - r + M_x, i_x - r + 2M_x, \dots | i_x - r \leq i < M_x + r\}$$

so $-r \leq i < M_x + r$.

Then

$$l_x := i + M_x j_x$$

and

$$s_{\text{in}}[i + r] = f^{(c)}(l_x)$$

e.g.

$$\begin{aligned} r &= \text{filterWidth}/2 = 9/2 = 4 \\ i_x &= 127 \\ i &= 127 - 4 = 123 = l_x \\ s_{\text{in}}[127] &= f^{(c)}(123) \end{aligned}$$

$k_x = 127 = L_x$ so $k_x \geq L_x$

So in this very insightful example, we’ve seen that for $k_x = 127$, it loads the regular value at index $l_x = 123$ into the appropriate slot in the shared memory, namely $s_{\text{in}}[127]$ correctly, and yet if we placed the code line in question that tests k_x against L_x in the wrong order, this step would’ve been excluded!

How can we see this in general?

Consider now that $l_x = i + M_x j_x = i_x + M_x j_x - r = k_x - r$.

Also

$$i + r = i_x - r + r = i_x$$

Now then

$$s_{\text{in}}[i_x] = f^{(c)}(l_x) = f^{(c)}(k_x - r)$$

while $N_x M_x - r \geq L_x$ or $N_x M_x - r < L_x$. It’s this ambiguity that forces the check of $k_x \geq L_x$ to be moved to after loading all values into shared memory. The $l_x \leq L_x$ check guarantees both that we aren’t going “outside” the array indices in global memory *and* that we’re “clamping” down on the absolute boundary value if we reach the absolute boundary or “end” of the array.

My big takeaway is that doing the shared memory tiling scheme is much more nuanced and deserves more inspection than the relatively straightforward “naive” global memory scheme.

1.3. Unit 3, Lesson 3 Fundamental GPU Algorithms (Reduce, Scan, Histogram; Udacity cs344). More on Udacity forums, in particular, forum for cs344: **Please elaborate the micro-optimization techniques discussed by the instructor**

cf. **Reduce Part 2**

Reduce: Inputs.

(1) Set of elements.

Assume they are in an array.

(2) reduction operator

(a) binary

(b) associative

e.g. of binary operators, cf. **Binary and Associative Operators**

- multiply *
- min
- logical or ($a \parallel b$)
- bitwise and ($a \& b$)

Indeed, let f represent the input:

$$\begin{aligned} f &\in K^N; & N &\in \mathbb{Z}^+ \\ f(i) &\in K; & i &\in \{0, 1, \dots, N - 1\} \end{aligned}$$

Serial Implementation of Reduce

$$\begin{aligned} S &= 0, \\ \forall i \in \{0, 1, \dots, N - 1\} &\iff S = \sum_{i=0}^{N-1} f(i) \\ S &+ = f(i) \end{aligned}$$

Step Complexity of Parallel Reduce

N	steps
2	1
4	2
8	3
2^n	n

The answer is $\log_2 N = n$. Let’s prove this with induction.

Consider $N = 2^{n+1}$, number of things to compute.

$\frac{N}{2} = 2^n$. After 1 step (of binary operations), there are 2^n things left to compute.

By induction step, n steps are required.

$$\log_2 N = \log_2 2^{n+1} = n + 1$$

Done.

Reduction Using Global and Shared Memory

For a good explanation of *bitwise left shift* and *bitwise right shift* operators (and also for assignment), see **What does a bitwise shift (left or right) do and what is it used for?**. Also, a good implementation that converts from integers to bitwise representation is given in **Converting integer to a bit representation**

Keep in mind these are called *bitwise shift* operations (so we could look them up by this name).

Left shift.

$$\mathbf{x} \ll \mathbf{y} \iff x \cdot 2^y$$

Right shift.

$$\mathbf{x} \gg \mathbf{y} \iff x / 2^y$$

cf. **reduce.cu of Lesson 3 Code Snippet for Udacity cs344**

global memory reduce. $M_x \in \mathbb{Z}^+$, e.g. $M_x = 1024$

$$k_x := i_x + M_x j_x \in \mathbb{Z}^+$$

$$t_{id} := i_x \in \{0 \dots M_x - 1\} \subset \mathbb{Z}^+$$

$$\forall s \in \{ \frac{M_x}{2}, \frac{M_x}{2^2}, \frac{M_x}{2^3}, \dots \},$$

If $t_{id} < s$,

$$d_{in}[k_x] + = d_{in}[k_x + s] \iff d_{out}[j_x] = \sum_{\substack{t_{id} < s \\ s \in \{ \frac{M_x}{2}, \frac{M_x}{2^2}, \frac{M_x}{2^3}, \dots \}}} d_{in}[k_x + s] + d_{in}[k_x]$$

Finally,

$$d_{out}[j_x] = d_{in}[k_x]$$

shared memory reduce. $k_x := i_x + M_x j_x \in \mathbb{Z}^+$

$t_{id} := i_x \in \{0 \dots M_x - 1\} \subset \mathbb{Z}^+$

Load values into shared memory:

$$s_{\text{data}}[i_x] = d_{\text{in}}[k_x] \text{ where } s_{\text{data}} \in \mathbb{R}^{M_x}$$

$$\forall s \in \left\{ \frac{M_x}{2}, \frac{M_x}{2^2}, \frac{M_x}{2^3}, \dots \right\},$$

If $t_{id} < s$,

$$s_{\text{data}}[t_{id}] + = s_{\text{data}}[t_{id} + s] \iff d_{\text{out}}[j_x] = \sum_{\substack{s \in \{ \frac{M_x}{2}, \frac{M_x}{2^2}, \frac{M_x}{2^3}, \dots \} \\ t_{id} < s}} s_{\text{data}}[t_{id} + s] + s_{\text{data}}[t_{id}]$$

Finally,

$$d_{\text{out}}[j_x] = s_{\text{data}}[0]$$

Scan
Inputs to Scan

Inputs to scan. Like reduce,

- input array, $f \in K^N \quad N \in \mathbb{Z}^+$
 $f(i) \in K \quad i \in \{0 \dots, N - 1\}$
- binary associative operator

new feature (not in reduce),

- identity element $[I \text{ op } a = a]$

cf. **What Scan Actually Does**

$$f \in K^N \xrightarrow{\text{scan}} g \in K^N$$

inclusive scan

$$g(i) = \bigoplus_{j=0}^i f(j)$$

exclusive scan

$$g(i) = \begin{cases} \bigoplus_{j=0}^{i-1} f(j) & \text{if } i \geq 1 \\ 1 & \text{if } i = 0 \end{cases}$$

These definitions are worth repeating:

Definition 1 (Scan). *Given*

- input array $f \in K^N$, *i.e.*

$$\begin{aligned} f &\in K^N & N &\in \mathbb{Z}^+ \\ f[i] &\in K & i &\in \{0, \dots, N - 1\} \end{aligned}$$

with K equipped with a

- binary associative operator

- identity element $[I \text{ op } a = a]$

Then for

$$f \xrightarrow{\text{scan}} g$$

(2)

with **inclusive scan** defined as

(3)

$$K^N \xrightarrow{\text{scan}} K^N$$

$$g(i) = \bigoplus_{j=0}^i f(j)$$

and **exclusive scan** defined as

(4)

$$g(i) = \begin{cases} \bigoplus_{j=0}^{i-1} f(j) & \text{if } i \geq 1 \\ 1 & \text{if } i = 0 \end{cases}$$

cf. **Hillis Steele Scan**

Hillis/Steele inclusive scan. Let $f \in K^N \quad N \in \mathbb{Z}^+$

$$f(j) \in K \quad j \in \{0 \dots N - 1\}$$

Consider step $i = 0$.

For $g_i \in K^N$, $\forall i \in \{0, \dots, \log_2 N - 1\}$,

Doing the first 3 steps,

$$\begin{aligned} g_0(j) &= \begin{cases} f(j) + f(j - 2^0) & \text{if } j \geq 1 \\ f(j) & \text{if } j < 1 \end{cases} \\ g_1(j) &= \begin{cases} g_0(j) + g_0(j - 2^1) & \text{if } j \geq 2^1 \\ g_0(j) & \text{if } j < 2^1 \end{cases} \\ g_2(j) &= \begin{cases} g_1(j) + g_1(j - 2^2) & \text{if } j \geq 2^2 \\ g_1(j) & \text{if } j < 2^2 \end{cases} \end{aligned}$$

Thus

$$g_i(j) = \begin{cases} g_{i-1}(j) + g_{i-1}(j - 2^i) & \text{if } j \geq 2^i \\ g_{i-1}(j) & \text{if } j < 2^i \end{cases}$$

Now do the following induction cases:

$$\begin{aligned} g_{\log_2 N}(0) &= f(0) \\ g_{\log_2 N}(1) &= g_0(1) = f(1) + f(0) \\ g_{\log_2 N}(2) &= g_1(2) = g_0(2) + g_0(0) = f(2) + f(1) + f(0) \\ g_{\log_2 N}(3) &= g_1(3) = g_0(3) + g_0(1) = f(3) + f(2) + f(1) + f(0) \end{aligned}$$

So generalize to the induction case:

$$g_{\log_2 N}(j) = \bigoplus_{i=0}^j f(i)$$

Then one would check the induction step for a number of cases, whether j was greater or smaller or equal to $2^{\log_2 N}$.

To summarize for the Hillis/Steele inclusive scan,

$$(5) \quad \boxed{\begin{array}{l} \forall i \in \{0, 1, \dots, \log_2 N | 2^i < N\} \\ g_{i-1}(j) = f(j) \quad \forall j \in \{0 \dots N-1\} \\ g_i(j) = \begin{cases} g_{i-1}(j) + g_{i-1}(j-2^i) & \text{if } j \geq 2^i \\ g_{i-1}(j) & \text{if } j < 2^i \end{cases} \end{array}}$$

i.e.

$$(6) \quad \boxed{\begin{array}{l} \forall i \in \{0, 1, \dots, \lfloor \log_2 N \rfloor | 2^i < N\} \\ g_{-1}(j) = f(j) \quad \forall j \in \{0 \dots N-1\} \\ g_i(j) = \begin{cases} g_{i-1}(j) + g_{i-1}(j-2^i) & \text{if } j \geq 2^i \\ g_{i-1}(j) & \text{if } j < 2^i \end{cases} \end{array}}$$

Implementation is CUDA C/C++: consider $k_x := i_x + M_x j_x \in \{0, 1, \dots, L_x\}$, $L_x \in \mathbb{Z}^+$, with the formula

$$N_x := \frac{L_x + M_x - 1}{M_x}$$

that'll give us `blockDim.x`.

$\forall i \in \{0, 1 \dots \log_2 k_x\}$ (or $k_x < 2^i$),

$$g_i(k_x) = g_{i-1}(k_x) + g_{i-1}(k_x - 2^i)$$

For *(inclusive) scan*,

step : $O(\log n)$

work : $O(n^2)$

For *Hillis/Steele* ,

step : $O(\log n)$

work : $O(n \log n)$

(more step efficient)

For *B*

cf. [Inclusive Scan Revisited](#), [Hillis Steele vs Blelloch Scan](#), [Hillis Steele Scan](#),

1.4. **Blelloch scan.** cf. [Blelloch Scan](#)

1.4.1. *Blelloch scan, reduce, 1st part, up sweep.* My development: consider the most basic cases. So for $f \in K^N$,

$$\begin{array}{ll} i \in \{0, 1, \dots, N-1\} & N=8 \quad i \in \{0, 1, \dots, 8-1=7\} \\ 2j, \quad j \in \{1, 2, \dots, \lfloor N/2 \rfloor\} & 2j \in \{2, 4, 6, 8\}, j \in \{1, 2, 3, 4\} \\ \quad i = 2j-1 & i = 2j-1 \in \{1, 3, 5, 7\} \\ 2^2 j-1, \quad j \in \{1, 2, \dots, \lfloor N/2^2 \rfloor\} & j \in \{1, 2\}, \quad i \in \{3, 7\} \\ 2^3 j-1, \quad j \in \{1, 2, \dots, \lfloor N/2^3 \rfloor\} & j \in \{1\}, \quad i \in \{7\} \end{array}$$

Then $\forall k = \{1, 2, \dots, \lfloor \log_2 N \rfloor\}$,

$$\begin{array}{ll} k=1, & f_{\text{out}}[i] = \begin{cases} f[i] + f[i-1] & \text{if } i = 2j-1, j \in \{1, 2, \dots, \lfloor N/2 \rfloor\} \\ f[i] & \text{otherwise} \end{cases} \\ k=2, & f_{\text{out}}[i] = \begin{cases} f[i] + f[i-2^{(k-1)}] & \text{if } i = 2^2 j-1, j \in \{1, 2, \dots, \lfloor N/2^2 \rfloor\} \\ f[i] & \text{otherwise} \end{cases} \\ k & , \quad f_{\text{out}}[i] = \begin{cases} f[i] + f[i-2^{(k-1)}] & \text{if } i = 2^k j-1, j \in \{1, 2, \dots, \lfloor N/2^k \rfloor\} \\ f[i] & \text{otherwise} \end{cases} \end{array}$$

Thus

Definition 2 (Blelloch scan: 1st part that's reduce, i.e. up sweep).

$$\forall k \in \{1, 2, \dots, \lfloor \log_2 N \rfloor\}$$

$$(7) \quad f_{\text{out}}[i] = \begin{cases} f[i] + f[i-2^{(k-1)}] & \text{if } i = 2^k j-1, j \in \{1, 2, \dots, \lfloor N/2^k \rfloor\} \\ f[i] & \text{otherwise} \end{cases}$$

$$f \longmapsto f_{\text{out}}$$

$$\text{for } K^N \longrightarrow K^N$$

1.4.2. *Blelloch scan, down sweep part.* Now consider the down sweep. First, importantly, set the “last” element in the array, entry number $\lfloor \log_2 N \rfloor$, N being the length of the array, to the identity element of K .

I'll present the first few induction steps explicitly, and the general form can be guessed from there.

$$\forall k \in \{\lfloor \log_2 N \rfloor, \lfloor \log_2 N \rfloor - 1, \dots, 2, 1\}$$

If $i = 2^k j - 1$, $j \in \{1, 2, \dots, \lfloor N/2^k \rfloor\}$, for e.g. $k = \lfloor \log_2 N \rfloor$,

$$f_{\text{out}}[i - 2^{(k-1)}] = f[i]$$

$$f_{\text{out}}[i] = f[i] + f[i - 2^{(k-1)}]$$

For $k = \lfloor \log_2 N \rfloor - 1$, e.g. $k = 3 - 1 = 2$, then for this exmaple,

e.g. $i = 2^k j - 1 = 4j - 1$; $j \in \{1, 2\}$, $i \in \{3, 7\}$,

$$f_{\text{out}}[i] = f[i] + f[i-2]$$

$$f_{\text{out}}[i-2] = f[i]$$

Thus, in general,

Definition 3 (Blelloch scan: down sweep step).

$$\forall k \in \{\lfloor \log_2 N \rfloor, \lfloor \log_2 N \rfloor - 1, \dots, 2, 1\}$$

$$(8) \quad \begin{cases} f_{\text{out}}[i] := f[i] + f[i - 2^{(k-1)}] & \text{if } i = 2^k j - 1 \text{ and } j \in \{1, 2, \dots, \lfloor N/2^k \rfloor\} \\ f_{\text{out}}[i - 2^{(k-1)}] := f[i] & \\ f_{\text{out}}[i] := f[i] & \text{otherwise} \end{cases}$$

cf. [Problem Set 3](#)

Histogram Equalization.

- (1) Map
- (2) Reduce
- (3) Scatter
- (4) Scan

1.5. **Reduce, Parallel Reduction.** I will expound upon the excellent article from Mark Harris, “Optimizing Parallel Reduction in CUDA”. cf. *Optimizing Parallel Reduction in CUDA*, Mark Harris : http://developer.download.nvidia.com/compute/cuda/1.1-Beta/x86_website/projects/reduction/doc/reduction.pdf

1.6. **Unit 4.**

1.7. **Unit 5.**

1.7.1. *Occupancy.* cf. **Occupancy Part 1**

Each SM (streaming multi-processor) has a limited number of

- thread blocks (so there’s a maximum number of thread blocks, e.g. 8)
- threads (so there’s a maximum number of threads, e.g. 1536-2048)
- registers for all threads (every thread takes a certain number of registers, and there’s a maximum number of registers for all the threads, e.g. 65536)
- bytes of shared memory

cf. **Quiz: Occupancy Part 2**

Compile and run `deviceQuery_simplified.cpp` (can be found in Lesson 5 Code Snippets).

Look at

- Total amount of shared memory per block
- Maximum number of threads per multiprocessor
- Maximum number of threads per block

For Luebke’s laptop, it’s 49152 bytes, 2048, 1024 respectively, and 65536 total registers available per block.

1.7.2. *Thread Divergence.* cf. **Quiz: Swith Statements and Thread Divergence**

Threads in warp is 32 (check hardware, usually this is the case for modern GPUs). Only 32 threads in a warp, i.e. 2^5 .

CUDA assigns thread IDs to warps:

- x varies fastest
- y varies slower
- z varies slowest

warp assignment, consider $(M_x, M_y, M_z) \equiv$ number of threads in a single thread block, in each dimensional direction.

Consider

$$i_x \in \{0, \dots, M_x - 1\}$$

$$i_y \in \{0, \dots, M_y - 1\}$$

If $M_x/2^5 \in \mathbb{Z}^+$, i.e. $M_x/2^5 \geq 1$, then threads in x -direction are in warps that branch (go to same case in if-else, or switch, case statement in kernel) to same case.

1.7.3. *Problem Set 5.* 2. One Basic Strategy

A. Sort the Data, into bins, sort the input data, into coarse bins

B. use threads, compute local histogram, use each thread block, to compute local histogram

c. concatenate

e.g.

Consider

J = total number of coarse bins, $J \in \mathbb{Z}^+$.

$j \in \{0 \dots J - 1\}$, e.g. $J = 10$.

K = total number of bins,

$k \in \{0 \dots K - 1\}$, e.g. $K = 100$

The condition is that $J < K$

For each thread block,

1.7.4. *Parallel radix sort.* cf. look at, in the directory http://www.compsci.hunter.cuny.edu/~sweiss/course_materials/csci360/lecture_notes/ for the parallel radix sort implementation called `radix_sort_cuda.cc`.

Let $M_x \in \mathbb{Z}^+$ represent

(9)
$$M_x \leftrightarrow \text{blockDim.x}$$

(10)
$$j_x \in \{0, 1, \dots M_x - 1\} \leftrightarrow \text{blockIdx.x}$$

$\forall j_x \in \{0, 1 \dots, M_x - 1\}$, j_x denote which thread block we are in.

j_x also corresponds to the so-called coarse bin id.

1.8. **Lesson 6.1 - Parallel Computing Patterns Part A.**

1.8.1. *parallel All Pairs N-body.* cf. **Quiz: All Pairs N-Body**

From Arnold, Kozlov, Neishtadt, and Khukhro (2006) [16], given N -bodies $\{1, 2 \dots N\} \subset \mathbb{Z}^+$.

$$(\mathbf{r}_1, M_1), (\mathbf{r}_2, M_2), \dots (\mathbf{r}_N, M_N) \quad \mathbf{r}_i \in \mathbb{R}^d$$

With the force acting upon the i th body (“destination”) due to the j th body (“source”), \mathbf{F}_{ij} ,

(11)
$$\mathbf{F}_{ij} = -\frac{\gamma M_i M_j}{|\mathbf{r}_{ij}|^3} \mathbf{r}_{ij} \quad \text{with } \mathbf{r}_{ij} := \mathbf{r}_j - \mathbf{r}_i$$

$N(N - 1)$ are the number of (unordered) pairs, for

(12)
$$\mathbf{F}_i = \sum_{i \neq j} \mathbf{F}_{ij}$$

with \mathbf{F}_i denoting the force acting upon i th body, $N - 1$ computations

(13)
$$\mathbf{F}_i = \sum_{i \neq j} \frac{-\gamma M_i M_j}{|\mathbf{r}_{ij}|^3} \mathbf{r}_{ij} = -\gamma M_i \sum_{i \neq j} \frac{M_j}{|\mathbf{r}_{ij}|^3} \mathbf{r}_{ij}$$

Consider another pairwise computation:

(14)
$$W(\lambda) = \sum_{i=1}^m \lambda_i - \frac{1}{2} \sum_{i,j=1}^m y^{(i)} y^{(j)} \lambda_i \lambda_j K(X^{(i)}, X^{(j)})$$

Define, for (writing) convenience, $f_1(\lambda) = -\frac{1}{2} \sum_{i,j=1}^m y^{(i)} y^{(j)} \lambda_i \lambda_j K(X^{(i)}, X^{(j)})$.

cf. **Quiz: How To Implement Dense N-Body as Simply As Possible**

For a single body, say I th body (\mathbf{r}_I, M_I) , consider $\mathbf{F}_i = \sum_{i \neq j} \mathbf{F}_{ij} \quad \forall i = 1, \dots N$.

$j = I$ once, $\forall i = 1, \dots N$ and $i \neq I$ (I th body as a “source”) $N - 1$ times.

$i = I$ once, in $\mathbf{F}_I = \sum_{i \neq j} \mathbf{F}_{Ij}$, we’ll need (\mathbf{r}_I, M_I) , to calculate \mathbf{F}_{Ij} , $N - 1$ times.

If all N^2 force computations, go to global memory, the number of times we’d fetch each element, as a function of N is $\boxed{2(N - 1)}$.

cf. **Quiz: Dividing N by N Matrix Into Tiles**

Consider $N \times N$ matrix A , $A \in \text{Mat}_{\mathbb{K}}(N_x, N_y)$.

Consider dividing it by tiles, calculate each tile, a tile of $P_x \times P_y$.

So there are $\frac{N_x N_y}{P_x P_y}$ tiles overall.

For this particular problem, and in general, a problem involving pairwise *combination* out of a given set, say N bodies, consider N^2 possible pairs total. Then this all N -body algorithm idea is to transform all these combinations N^2 , into a matrix, and so $N_x = N_y = N$, $P_x = P_y = P$.

(15)
$$\mathbf{F}_i = \sum_{i \neq j}^N \mathbf{F}_{ij} = \sum_{t=0}^{\frac{N}{P}-1} \sum_{\substack{j=0 \\ Pt+j \neq i}}^{P-1} \mathbf{F}_{i(Pt+j)} \equiv \sum_{t=0}^{\frac{N}{P}-1} \mathbf{F}_i^{(t)}$$

$2P$ fetches per tile, instead of $2P^2$, if \forall tile, store the P elements into shared memory.
cf. [Using on \$P\$ threads](#)
Consider this computation:

(16)
$$\mathbf{F}_i^{(t)} = \sum_{\substack{j=0 \\ Pt+j \neq i}}^{P-1} \mathbf{F}_{i(Pt+j)} \quad i = 0 \dots N-1, t = 0, 1, \dots \frac{N}{P} - 1$$

For
$$i \iff i_x + M_x j_x, \quad \begin{aligned} i_x &\equiv \texttt{threadIdx.x} \in \{0, 1 \dots M_x - 1\} \\ j_x &\equiv \texttt{blockIdx.x} \in \{0, 1, \dots N/M_x - 1\} \\ M_x &= P \end{aligned}$$

So $\forall (i_x, j_x)$, (i_x, j_x) representing a single thread, this single thread will be responsible for computing

$$\mathbf{F}_i^{(t)} = \sum_{j=0}^{P-1} \mathbf{F}_{i(Pt+j)}$$

, as opposed to only computing $\mathbf{F}_{i(Pt+j)}$ only.
The pseudocode that Owens gives in [Using On P Threads, Lesson 6.1](#) is the following:

```
--device__ float3
tile_calculation(Params myParams, float3 force) {
    int i;
    extern __shared__ Params[] sourceParams;
    for (i=0; i<blockDim.x; i++) {
        force += bodyBodyInteraction(myParams, sourceParams[i]);
    }
    return force;
}
```

1.9. **Final for Udacity cs344.**

1.9.1. *Quiz: Final - Question 7.* cf. <https://classroom.udacity.com/courses/cs344/lessons/2133758814/concepts/1421890260923#>
Second question:
What is the ratio (expressed as a percentage) between kernel that uses more memory bandwidth and the kernel that uses less?
For both parts of this question, here’s how to approach it.
Consider a stencil of radius $\text{RAD} \equiv R = 2$. So the filter-width in 1-dimension for this 2-dimensional stencil is

$$\text{filterwidth} \equiv W = 2 \cdot \text{RAD} + 1 = 5$$

W^2 multiples, and $W^2 - 1$ adds.
 $\forall k \in K$, a (single) pixel of greyscale image, $k \in \{0, 1, \dots N - 1\}$,

Consider the threads per block configuration (i.e. a single thread block).
Let $M \equiv M_x M_y = 1024$ in both, either, case, i.e. totla number of threads per block is 1024.

Consider $(M_x, M_y) = (1024, 1)$ vs. ,
 $(M_x, M_y) = (32, 32)$ vs.

Corresponding to

$$\text{dim3} M_i(M_x, M_y)$$

for

```
smooth <<< , M_i >>> (v_new, v);
```

The big idea is that we have a tile of input data we want to load into 2-dimensional shared memory. It is of size

(17)
$$(M_x + 2R)(M_y + 2R)$$

Compare this quantity to when $(M_x, M_y) = (1024, 1)$ vs. when it’s $(32, 32)$.

$$(1024 + 4)(1 + 4)/(32 + 4) * (32 + 4) = \boxed{3.966}$$

This is the ratio between the kernel with more memory bandwidth and the kernel with less memory bandwidth.

Note, in the Udacity forum, [Final: Updated Questions 7 & 8](#), jlmayfield gave the hint to consider those stencil halo cells or what I call the radius R of the stencil, as this stencil is $5x5$.

1.9.2. *Quiz: Final - Question 8.* cf. [Quiz:Final - Question 8](#)
 32×32 block, but consider

- 4x storage per SM
- 4x registers
- 4x shared memory

now 64×64 since, in each dimension, 2x the global memory available ($2 \times 2 = 4$).

Express, in decimals, what the speedup is on this new GPU.

Here’s how I approached this problem:

so in a single thread block, we can have a much larger tile with inputted data to reside in shared memory and so we can process a much larger tile of inputted data in a single thread block:

(18)
$$(M_x + 2R)(M_y + 2R) = (64 + 2 * 2) * (64 + 2 * 2) = 4624$$

instead of when $(M_x, M_y) = (32, 32)$.

Not only that 4x of shared memory, 4x storage per SM, but also *4x registers*. More registers for the threads, more a thread can be doing something and not have to wait for another thread (or even to hop on to another thread if the thread itself doesn’t have enough registers). More throughput, by 4x.

(19)
$$(64 + 2R)(64 + 2R) * 4/(32 + 2R)(32 + 2R) = \boxed{14.27 \text{ or } 14.0}$$

There was confusion about Final - Question 8 in the Udacity Forums: [Final - Question 8](#)

1.9.3. *Quiz: Final - Question 9.* cf. <https://classroom.udacity.com/courses/cs344/lessons/2133758814/concepts/1389566540923>

If I want to run the maximum number of threads possible that are all resident on an SM (streaming multiprocessor) at the same time, each thread must use no more than, *how many*, registers?

(20)
$$\boxed{32 \text{ registers}}$$

This is obtained by looking at <http://en.wikipedia.org/wiki/CUDA> (this was given) and look for

- “Maximum number of resident threads per multiprocessor”: 2048 for Compute ability (version) 3.5.
-

[Final Exam Question 9 confusion](#)
<https://discussions.udacity.com/t/final-exam-question-9-confusion/88637>

1.9.4. *Quiz: Final - Question 11.* Maximum number of blocks resident on the same SM, what is the maximum number of threads / block that we can have?

This is obtained by looking at <http://en.wikipedia.org/wiki/CUDA> (this was given) and look for

- “Maximum number of resident blocks per multiprocessor”: 16 for Compute ability (version) 3.5.
- “Maximum number of resident threads per multiprocessor”: 2048 for Compute ability (version) 3.5.

The answer is

$$(21) \quad \boxed{2048/16 = 128}$$

Since we take max. number of resident threads per SM, 2048, but the condition is using the max. number of resident blocks per SM, so, divide by 16.

1.9.5. *Quiz: Final - Question 12; Fast “compact” primitive.* Consider the *Fast “compact” primitive*.

(1) sum up the no. of “T” flags

e.g. warp of size 4 (only to make it easy to understand), (4 warps of 4 flags each), e.g.

TFTF/FTFT/TTTT/tfff

(1)

2/2/4/1

Let warp size $W = 2^n$; $n \in \mathbb{Z}^+$, e.g. $n = 5$ for $W = 32$.

Given shared memory $s \in (\mathbb{Z}^+)^W$,

$$\forall i \in \{W/2, W/4, \dots | i > 0\}$$

For given `ARRAY_SIZE = 32 = M_x` so $M_x = W$ (thread block size is equal to warp size in this case).

Keep in mind, even in a `__device__` function, we’ve got **access** to

$$i_x \equiv \text{threadIdx.x} \in \{0, 1, \dots, M_x - 1\}$$

(and so on).

$$i_x < i, \text{ i.e. } \forall i_x < i \text{ and } i_x \in \{0, 1, \dots, M_x - 1\}$$

$$s[i_x] = s[i_x] + s[i_x + i]$$

e.g. $i = W/2$

$$i_x \in \{0, 1, \dots, \frac{W}{2} - 1\}$$

$i = W/4$

$$i_x \in \{0, 1, \dots, \frac{W}{4} - 1\}$$

$$s[0] = s[0] + s[\frac{W}{2}] + s[0 + \frac{W}{4}]$$

$$s[i_x] = s[i_x] + s[i_x + 1] \implies :$$

$$s[\frac{W}{4} - 1] = s[\frac{W}{4} - 1] + s[\frac{3W}{4} - 1] + s[\frac{W}{2} - 1]$$

So by induction, for $i = 1$, $i_x = 0$.

$$s[0] = \sum_{j=0}^{W-1} s[j]$$

which is the desired result (!!!).

Code answer uploaded here: [Q12warpreduce shared.cu](#) or here [warpreduce.cu](#)

2. POINTERS IN C; POINTERS IN C CATEGORIFIED (INTERPRETED IN CATEGORY THEORY)

Suppose $v \in \text{ObjData}$, category of data **Data**,

e.g. $v \in \text{Int} \in \text{ObjType}$, category of types **Type**.

$$\text{Data} \xrightarrow{\&} \text{Memory}$$

$$v \mapsto \&v$$

with address $\&v \in \text{Memory}$.

With

assignment $pv = \&v$,

$pv \in \text{Objpointer}$, category of pointers, pointer

$pv \in \text{Memory}$ (i.e. not $pv \in \text{Dat}$, i.e. $pv \notin \text{Dat}$)

$$\text{pointer} \ni pv \mapsto *pv \in \text{Dat}$$

$$\begin{array}{ccc} v & \xrightarrow{\&} & \&v \\ \uparrow & & \downarrow \\ *pv & \xleftarrow{*} & pv \end{array} \quad \begin{array}{ccc} \text{Data} & \xrightarrow{\&} & \text{Memory} \\ \uparrow & & \downarrow \\ \text{Data} & \xleftarrow{*} & \text{pointer} \end{array}$$

Examples. Consider `passfunction.c` in Fitzpatrick [6].

Consider the type `double`, `double` $\in \text{ObjTypes}$.

`fun1`, `fun2` $\in \text{MorTypes}$ namely

$$\text{fun1}, \text{fun2} \in \text{Hom}(\text{double}, \text{double}) \equiv \text{Hom}_{\text{Types}}(\text{double}, \text{double})$$

Recall that

$$\text{pointer} \xrightarrow{*} \text{Dat}$$

$$\text{pointer} \xrightarrow{\&} \text{Memory}$$

$*$, $\&$ are functors with domain on the category `pointer`.

Pointers to functions is the “extension” of functor $*$ to the codomain of `MorTypes`:

$$\begin{array}{ccc} \text{pointer} & \xrightarrow{*} & \text{MorTypes} \\ \text{fun1} & \mapsto * & \text{fun1} \in \text{Hom}_{\text{Types}}(\text{double}, \text{double}) \end{array}$$

$$\begin{array}{ccc} \text{double} & \xrightarrow{\&} & \text{Memory} \\ \downarrow \cong & & \downarrow \cong \\ \text{double} & \xleftarrow{*} & \text{pointer} \end{array}$$

$$\begin{array}{ccc} \text{res1} & \xrightarrow{\&} & \&\text{res1} \\ \downarrow \cong & & \downarrow \cong \\ * \text{res1} & \xleftarrow{*} & \text{res1} \end{array}$$

$$\begin{array}{ccc} \text{cube} & \downarrow & \text{cube} \\ \downarrow & \swarrow * & \downarrow \\ \text{double} & & * \text{res1} = y^3 \end{array}$$

It’s unclear to me how `void cube` can be represented in terms of category theory, as surely it cannot be represented as a mapping (it acts upon a functor, namely the `*` functor for pointers). It doesn’t return a value, and so one cannot be confident to say there’s explicitly a domain and codomain, or range for that matter.

But what is going on is that

$$\begin{array}{ccc} \text{pointer}, \text{double}, \text{pointer} & \xrightarrow{\text{cube}} & \text{pointer}, \text{pointer} \\ \text{fun1}, x, \text{res1} & \xmapsto{\text{cube}} & \text{fun1}, \text{res1} \end{array}$$

s.t. $*\text{res1} = y^3 = (*\text{fun1}(x))^3$

So I’ll speculate that in this case, `cube` is a functor, and in particular, is acting on `*`, the so-called deferencing operator:

$$\begin{array}{ccc} \text{pointer} \xrightarrow{*} \text{float} \in \text{Data} & \xrightarrow{\text{cube}} & \text{pointer} \xrightarrow{\text{cube}(*)} \text{float} \in \text{Data} \\ \text{res1} \xmapsto{*} *\text{res1} & & \text{res1} \xmapsto{\text{cube}(*)} \text{cube}(*\text{res1}) = y^3 \end{array}$$

cf. Arrays, from Fitzpatrick [6]

$$\text{Types} \xrightarrow{\text{declaration}} \text{arrays}$$

If $x \in \text{Objarrays}$,

$$\&x[0] \in \text{Memory} \xlongequal{=} x \in \text{pointer (to 1st element of array)}$$

cf. Section 2.13 Character Strings from Fitzpatrick [6]

```
char word[20] = ‘‘four’’
char *word = ‘‘four’’
```

cf. C++ extensions for C according to Fitzpatrick [6]

- simplified syntax to pass by reference pointers into functions
- inline functions
- variable size arrays

```
int n;
double x[n];
```

- complex number class

3. SUMMARY OF UDACITY CS344 CONCEPTS

3.1. **Histogram.** Consider given input values (of observations).

For n observations,

For $i \in \{1, 2, \dots n\} \subset \mathbb{Z}^+, \equiv i\{0, 1, \dots n - 1\} \subset \mathbb{Z}^+.$

Consider $x[i] \in B \subset \mathbb{K}$, so $x \in B^n \subset \mathbb{K}^n$.

e.g. $\mathbb{K} = \mathbb{R}$, B is a subset that we can make K bins out of, i.e.

$$B = \prod_{j=1}^K B_j \equiv \prod_{j=0}^{K-1} B_j$$

e.g. B is bounded interval of \mathbb{R} , i.e. $\max B < \infty$
 $\min B > -\infty$

Then the *histogram* H is mapping from bins to *number* of observations, i.e.

$$\begin{array}{l} H : \{1, 2, \dots K\} \rightarrow \mathbb{N}^K \\ H(j) \in \mathbb{N} = \{0, 1, \dots\} \end{array}$$

s.t.

$$(23) \quad n = \sum_{j=1}^K H(j)$$

with n being the total number of observations.

In the implementation of $x : \{1, 2, \dots n\} \rightarrow B \subset \mathbb{K}$, this was simplified to

$$x : \{1, 2, \dots n\} \rightarrow \{1, 2, \dots K\} \subset \mathbb{Z}$$

meaning each observation value is *itself* which bin the observation belongs to.

Otherwise, a separate “binning” operation is needed:

$$(24) \quad \begin{array}{ccc} x : \{1, 2, \dots n\} \rightarrow & B \subset \mathbb{K} \rightarrow & \{1, 2, \dots K\} \\ i \mapsto & & x[i] \mapsto \text{if } B_{j-1} \leq x[i] < B_j, \text{ then return } j \end{array}$$

3.1.1. *Histogram implementations; histogram references.* [ernestyalumni/cs344/Problem Sets/Problem Set 5/histogram/https://github.com/ernestyalumni/cs344/tree/master/Problem%20Sets/Problem%20Set%205/histogram](https://github.com/ernestyalumni/cs344/tree/master/Problem%20Sets/Problem%20Set%205/histogram/)

Part 2. More Parallel Computing

4. LATENCY, BANDWIDTH, THROUGHPUT

Latency T (μ secs.) - time it takes for operation to start and compute. **Bandwidth** - amount $v = d/t$ I tried introducing this notation, following Bader, Pöpl, and Khakhutsky [?] :

$$\begin{array}{ccc} T(1) & T : \mathbb{Z}^+ \rightarrow \mathbb{R}^+ \\ T(n_{\text{ops}}) & T : n_{\text{ops}} \mapsto T(n_{\text{ops}}) \end{array}$$

with $n_{\text{ops}} :=$ number of operations. $T(1)$ vs. $T(n_{\text{ops}})$ $T(1) =$ time to complete 1 operation = latency. $T(n_{\text{ops}}) =$ time of complete n_{ops} operations.

$$(25) \quad n_{\text{ops}}/T(n_{\text{ops}}) = U = \text{throughput}$$

5. CUDA EXECUTION MODEL; SIMD, SIMT

cf. Bader, Pöpl, and Khakhutsky [?] **SIMD** (Single Instruction Multiple Data) Registers:

- same operations executed on pairs/triples of 2,4,... operands (ideally in 1 clock cycle)
- vector instructions generated by compiler (“vectorization”) or (guided by) programmer (“intinsics”, e.g.)

cf. Ch. 3. CUDA Execution Model of Cheng, Grossman, and McKercher (2014) [4]. When kernel grid is launched, thread blocks of that kernel grid are distributed among available SMs for execution. Once scheduled on SM, threads of a thread block execute concurrently only on that assigned SM. CUDA employs *Single Instruction Multiple Thread (SIMT)* architecture to manage and execute threads in groups of 32 called *warps*. 1 instruction chain is imposed on multiple lightweight cores (via warp scheduler/dispatch unit)[?]. All threads in warp execute same instruction at the same time. Each thread has its own instruction address counter and register state, and carries out the current instruction on its own data. Each SM partitions the thread blocks assigned to it into 32-thread warps that it then schedules for execution on available hardware resources. SIMT allows multiple threads in same warp to execute independently. Even though all threads in a warps start together at the same program address, it’s possible for individual threads to have different behavior.

6. PERFORMANCE EVALUATION; SPEED-UP, EFFICIENCY, AMDAHL’S LAW

cf. Bader, Pöpl, and Khakhutsky [?], [Fundamentals](#)

Definition 4. *Let*

$$(26) \quad T(p) := \text{runtime on } p \text{ processors}$$

speed-up $S(p)$

$$(27) \quad S(p) := \frac{T(1)}{T(p)}$$

Typically, $1 \leq S(p) \leq p$.

6.1. Absolute vs. Relative Speed-Up (definitions). *Absolute speed-up* : best sequential algorithm for mono-processor (single processor) $T(1)$, compared to best parallel algorithm for multi-processor system. *relative speed-up* : Compare same (parallel) algorithm for mono-(single-) and multi processor system.

6.2. (Parallel) Efficiency.

Definition 5. *Efficiency* $E(p)$

$$(28) \quad E(p) = \frac{S(p)}{p}$$

Typically $0 \leq E(p) \leq 1$. \exists absolute efficiency vs. relative efficiency.

6.3. Amdahl’s law. Assumptions: program consists of sequential part s , $0 \leq s \leq 1$, which can’t be parallelized (synchroniza-tion, data I/O, etc.).

- parallelizable part: $1 - s$
- execution time for parallel program on p processors

$$(29) \quad T(p) \equiv sT(1) + (1 - s)T(p) = sT(1) + (1 - s) \left(\frac{T(1)}{p} \right) = T(1) = \left[(s) \left(1 - \frac{1}{p} \right) + \frac{1}{p} \right]$$

To derive the relationship

$$(30) \quad T(p) = \frac{T(1)}{p}$$

use the $vt = l$ relation:

$$\sum_{i=1}^p v_1 T(p) = v_p T(p) = l = p v_1 T(p) \implies T(1) = p T(p) \text{ or } T(p) = \frac{T(1)}{p}$$

Moving onwards, assume perfect-speed-up on arbitrary number of processors. Resulting speed-up $S(p)$

$$(31) \quad S(p) = \frac{T(1)}{T(p)} = \frac{1}{s + \frac{1-s}{p}}$$

$$\lim_{p \rightarrow \infty} S(p) = \frac{1}{s}, \text{ so } S(p) \leq \frac{1}{s}$$

Theorem 1 (Amdahl’s law). *Amdahl’s law: speed-up is bounded by*

$$(32) \quad \boxed{S(p) \leq \frac{1}{s}}$$

6.4. Gustafson’s law. Assumptions: Amdahl: sequential-part stays for increased problem size vs. Gustavson: assume that any sufficiently large problem can be efficiently parallelized. Fixed-time concept:

- parallel execution time normalized to $T(p) = 1$
- this contains a non-parallelizable part σ , $0 \leq \sigma \leq 1$

Execution time on single-processor:

$$T(1) = \sigma + p(1 - \sigma)$$

$$S(p) = \frac{T(1)}{T(p)} = \sigma + p(1 - \sigma) = p - \sigma(p - 1)$$

Parallel efficiency $E(p)$

$$E(p) = \frac{S(p)}{p} = \frac{\sigma + p(1 - \sigma)}{p} = \frac{\sigma}{p} + 1 - \sigma \xrightarrow{p \rightarrow \infty} 1 - \sigma$$

More realistically, the larger problem sizes, if more processors are available, parallelizable parts typically increase.

7. COMPUTE-BOUND VS. MEMORY-BOUND PERFORMANCE; COMPUTE-BOUND, MEMORY-BOUND

cf. Bader, Pöpl, and Khakhutsky [?], [Fundamentals](#) Consider a memory-bandwidth intensive algorithm:

- you can do a lot more flops than can be read from memory

Definition 6 (arithmetic intensity, operational intensity). *operational intensity* (or *arithmetic intensity*) of a code: number of performed flops per accessed byte

7.1. Memory-Bound Performance:

- arithmetic intensity smaller than critical ratio
- you could execute additional flops ”for free”
- speedup only possible by reducing memory accesses

7.2. Compute-Bound Performance.

- enough computational work to ”hide” memory latency
- speedup only possibly by reducing operations

7.3. Compute-Bound, Memory-Bound. From [stackoverflow](#), ”What do the terms “CPU bound” and “I/O bound” mean?”, compute bound seems to be **CPU Bound**, means rate at which process progresses is limited by speed of the CPU. Task that performs calculations on a small set of numbers, for example, multiplying small matrices, is likely to be CPU bound. **Memory bound** means rate at which a process progresses is limited by amount memory available and speed of that memory access. A task that processes large amounts of in memory data, for example, multiplying large matrices, is likely to be Memory bound.

8. FACTORIZATION

[Morgan Cooper for CSE710, Fall 2009](#) Let $n' := \sqrt{n} \in \mathbb{Z}^+$. We want a_i, b_i , $i = 1, \dots, n'$ s.t. $a_i b_i = n$. e.g. $n = 20$.

$$n' = \sqrt{20} = 5 \in \mathbb{Z}^+$$

(so we want the *ceiling*). Consider $x_i := i \quad \forall i = 1, \dots, 2n'$. Clearly,

if $\frac{n}{i} \in \mathbb{Z}$, i.e. $\gcd(n, i) = i$ for some $i \leq n'$,

then let $x_{i+n'} := q = \frac{n}{i}$ (let $x_{i+n'}$ be the other factor). Generalize this to $n' \in \mathbb{Z}^+$ ($n' := \sqrt{n}$, usually).

Let $a_0 \in \mathbb{Z}^+$ (most likely $a_0 = 1$ or $a_0 = 2$ is what you’d ”start off with”, but no hard restrictions).

Then let

$$x_{i_x} := i_x + a_0 \quad \forall i_x = 0, 1, \dots, M_x - 1$$

Essentially, looping through entire blocks of threads so to ”catch” every element in the array,

$\forall i_x, i_x + M_x, i_x + 2M_x \dots < n'$, If $n \bmod x_{i_x} = n \bmod (i_x + a_0) = 0$, i.e. $\frac{0-n}{i_x+a_0} \in \mathbb{Z} \iff \frac{n}{i_x+a_0} \in \mathbb{Z}$, then
 $i_x + a_0, i_x + a_0 + M_x, i_x + a_0 + 2M_x \dots$

$$y(i_x) := x_{i_x}$$

$$y(i_x + n') := \frac{n}{x_{i_x}}$$

9. MATRIX MULTIPLICATION, TILED, WITH SHARED MEMORY

Consider matrix multiplication:

$$A \in \text{Mat}_{\mathbb{K}}(N_i^A, N_j^A)$$

$$B \in \text{Mat}_{\mathbb{K}}(N_i^B, N_j^B)$$

$$C \in \text{Mat}_{\mathbb{K}}(N_i^C, N_j^C)$$

$$AB = C,$$

$$N_i^A = N_i^C \equiv N^A$$

$$N_j^A = N_j^B \equiv N^B$$

$$N_j^C = N_j^B \equiv N^C$$

$$C_{ij} = A_{ik}B_{kj} = \sum_{k=1}^N A_{ik}B_{kj} \quad \forall i = 1, 2, \dots N^A$$

$$j = 1, 2, \dots N^C$$

Consider, given *block size* $M \in \mathbb{Z}^+$,

$$j_y = 0, 1, \dots \frac{N^A}{M} - 1 \equiv \text{blockIdx.y} =: j_I$$

$$j_x = 0, 1, \dots \frac{N^C}{M} - 1 \equiv \text{blockIdx.x} =: j_J$$

Given $A \in \text{Mat}(N_i^A, N_j^A)$, given $(i, j) \in (\mathbb{Z}^+)^2$, As C++ works also with pointers as C, ”point” the first element of our $A_{\text{sub}} \in \text{Mat}_{\mathbb{K}}(M, M)$ submatrix that we want, to where we ”start from” in the ”source” matrix A , which is at $A_{iM, jM}$:

$$A_{\text{sub}}(0, 0) := A(iM, jM)$$

and so this means that i.e. we had supposed $i = 0, 1, \dots \frac{N_i^A}{M} - 1$. So consider

$$j = 0, 1, \dots \frac{N_j^A}{M} - 1$$

$$i = i_y + j_I M = 0, 1, \dots N^A - 1$$

$$j = i_x + j_J M = 0, 1, \dots N^C - 1$$

and so the crucial step is seen as

$$C_{ij} = \sum_{k=0}^{N^B-1} A_{ik}B_{kj} = \sum_{j_K=0}^{\frac{N^B}{M}-1} \sum_{k=0}^{M-1} A_{i(k+j_K M)} B_{(k+j_K M), j}$$

I originally implemented matrix multiplication based on the CUDA C Programming Guide here at [github : ernestyalumni/-CompPhys/moreCUDA/matmultShare.cu](https://github.com/ernestyalumni/CompPhys/moreCUDA/matmultShare.cu) Indeed, the idea from the CUDA C Programming Guide is essentially the above -

I will reiterate again, since it was important in Pöpl’s presentation for TUM in HPC - Algorithms and Applications (cf. [Introduction to CUDA](#)): given

$$A \in \text{Mat}_{\mathbb{R}}(N_i^A, N_j^A)$$

$$B \in \text{Mat}_{\mathbb{R}}(N_i^B, N_j^B)$$

$$C \in \text{Mat}_{\mathbb{R}}(N_i^C, N_j^C)$$

Necessarily, for

$$AB = C$$

(33)

$$A_{ik}B_{kj} = C_{ij} \quad \forall i = 1, 2, \dots N_i^A$$

$$\forall j = 1, 2, \dots N_j^B$$

we have to have

$$N_i^C = N_i^A$$

$$N_j^C = N_j^B$$

$$N_j^A = N_j^B$$

We sought thread warp coalescing, in conjunction with the so-called ”row-major ordering” (also known as ”row-major”, ”order”) in the x -direction of thread block. Keep that in mind for the grid, block thread(s) assignment strategy.

9.0.1. *Grid, block thread(s) assignment strategy for Matrix multiplication.* Consider $N_j^B \times N_i^A$ calculations, i.e. $(j, i) \in \{0, 1 \dots N_j^B - 1\} \times \{0, 1 \dots N_i^A - 1\} \subset (\mathbb{Z}^+)^2$. For (thread block) size $M \times M \equiv \text{dim3 dimBlock(M,M)}$. Then, the number of thread blocks along each dim. of the grid would be $\frac{N_j^B + M - 1}{M} = \lfloor \frac{N_j^B}{M} \rfloor$, $\frac{N_i^A + M - 1}{M} = \lfloor \frac{N_i^A}{M} \rfloor$ Nevertheless, the whole concept of tiling, tiling pattern with shared memory, is encapsulated in this equation:

$$(34) \quad C_{ij} = \sum_{k=0}^{N^B-1} A_{ik}B_{kj} = \sum_{j_K=0}^{\frac{N^B}{M}-1} \sum_{k=0}^{M-1} A_{i(k+j_K M)} B_{(k+j_K M), j}$$

10. DENSE LINEAR ALGEBRA

cf. [Dense Linear Algebra, HPC - Algorithms and Applications, Alexander Pöpl, TUM](#) Bader, Pöpl, and Khakhutskyy [?]

11. COOPERATIVE GROUPS

11.1. **Example of reduction (summation).** cf. `thread_sum` Let

```
cooperative_group::thread_block ctg;
int lane = ctg.thread_rank();
```

Let `lane` $\equiv l = 0, 1, \dots (|\text{ctg}| - 1)$, where $|\text{ctg}| = \text{ctg.size}()$. Consider,

$$\forall i = \frac{|ctg|}{2}, \frac{|ctg|}{2^2}, \dots \frac{|ctg|}{2^{K_{|ctg|}-1}}, 1$$

with $2^{K_{|\text{ctg}|}} = \text{ctg}$ or $\lfloor K_{|\text{ctg}|} \rfloor = \log_2 |\text{ctg}|$, i.e. $i = \frac{|ctg|}{2^j}$ and $i > 0$, $j = 1, 2, \dots K_{|\text{ctg}|}$. In almost all use cases, since thread group is in multiples of 32 (or 16), then $\log_2 |ctg| \in \mathbb{Z}^+$. Given

$$X : \{0, 1, \dots N - 1\} \rightarrow \mathbb{K}$$

$$X : l \mapsto X(l)$$

and given some $s \in \mathbb{K}$, with $\mathbb{K} = \mathbb{Z}$ in this example case, then

$$X(l) := s$$

(which implies $|\text{ctg}| = N$; otherwise, it's necessary that $N \leq |\text{ctg}|$).

$\text{ctg.sync}() \iff \forall l = 0, 1, \dots (\text{ctg} - 1), X(l) := s$

If $l < i$, $s := s + X(l + i)$. Explicitly, Suppose $i = \frac{|\text{ctg}|}{2}$, $l = 0, 1, \dots \frac{|\text{ctg}|}{2} - 1$, $l + i = \frac{|\text{ctg}|}{2}$, $\frac{|\text{ctg}|}{2} + 1, \dots |\text{ctg}| - 1$

Suppose $i = \frac{|\text{ctg}|}{2^2}$, $l = 0, 1, \dots \frac{|\text{ctg}|}{2^2} - 1$, $l + i = \frac{|\text{ctg}|}{2^2}$, $\frac{|\text{ctg}|}{2^2} + 1, \dots \frac{|\text{ctg}|}{2} - 1$

Suppose $i = 1$, $l = 0$, $l + i = 1$. But we should also consider first doing vector loading, and then, as a general strategy, load the result of vector loading into shared memory, *and then* do the reduction directly on shared memory.

12. VECTOR LOADS FOR HIGHER MEMORY ACCESS EFFICIENCY

cf. **reduce_sum** Given

$$X : \{0, 1, \dots L - 1\} \rightarrow \mathbb{K}$$

$$X : i \mapsto X[i] \in \mathbb{K}$$

with $\mathbb{K} = \mathbb{R}, \mathbb{Z}$ or even \mathbb{C} , and Given

$$i_x \in \{0, 1, \dots M_x - 1\} \equiv \text{threadIdx.x}$$

$$j_x \in \{0, 1, \dots N_x - 1\} \equiv \text{blockIdx.x}$$

Then $\forall k_x := i_x + j_x M_x = 0, 1, \dots N_x M_x - 1$,

Consider $\frac{L}{4}$, $\exists 0 \leq r_4 < 4$ s.t. $L = 4q_4 + r_4$, so that $\frac{L}{4} = q_4 \in \mathbb{Z}^+$ (Division theorem). Consider $i = k_x, k_x + N_x M_x, \dots$, with $i < L/4 = q_4$.

if $k_x = 0$, $i = 0, N_x M_x, 2N_x M_x, \dots$, with $i < L/4 = q_4$

if $k_x = 1$, $i = 1, 1 + N_x M_x, 1 + 2N_x M_x, \dots$ with $i < L/4 = q_4$.

if $k_x = N_x M_x - 1$, $i = N_x M_x - 1, 2N_x M_x - 1, 3N_x M_x - 1, \dots$ with $i < L/4 = q_4$. Then consider $i = k_4 + 4q_4$, $i < L$. Then $i = 4q_4, 4q_4 + 1, \dots 4q_4 + 3$. So for k_x , consider $q_x \in \{0, 1, \dots \frac{L}{4} - 1\}$, with $k_x = q_x$. Let

$$v = v(q_x) := \sum_{\nu=0}^{4-1} X[4q_x + \nu]$$

12.1. Vector loading and reduction on a single thread block in shared memory. Then consider $i_x + j_x M_x = j_x M_x, j_x M_x + 1, \dots (j_x + 1)M_x - 1$, for fixed j_x . Let

$$shX : \{0, 1, \dots M_x - 1\} \rightarrow \mathbb{K}$$

$$shX : i_x \mapsto shX[i_x]$$

represent the shared memory, upon which we'll directly reduce the sum. $\text{lane} \equiv l := i_x$,

$$shX[l] := v(q_x)$$

$\text{ctg.sync}() \iff \forall l = 0, 1, \dots (\text{ctg} - 1), shX[l] := v(q_x)$ since $q_x = q_x(i_x) = q_x(l)$. And so, $\forall i = \frac{|\text{ctg}|}{2}, \frac{|\text{ctg}|}{2^2} \dots \frac{|\text{ctg}|}{2^{K_{|\text{ctg}|}-1}} = 2, 1$, if $i = \frac{|\text{ctg}|}{2}$, $l < i$, $l = 0, 1, \dots \frac{|\text{ctg}|}{2} - 1$, $l + i = \frac{|\text{ctg}|}{2}$, $\frac{|\text{ctg}|}{2} + 1, \dots |\text{ctg}| - 1$,

$$v(q_x) := v(q_x) + shX[l + i]$$

Then $shX[l] := v(q_x)$ and so

$$shX[l] := shX[l] + shX[l + i]$$

So reduce block done entirely on shared memory, which is fast.

Part 3. Notes on Professional CUDA C Programming, Cheng, Grossman, McKercher

cf. Cheng, Grossman, and McKercher (2014) [4]

13. CUDA EXECUTION MODEL

cf. Ch. 3. CUDA Execution Model

When kernel grid is launched, thread blocks of that kernel grid are distributed among available SMs for execution. Once scheduled on SM, threads of a thread block execute concurrently only on that assigned SM.

CUDA employs *Single Instruction Multiple Thread (SIMT)* architecture to manage and execute threads in groups of 32 called *warps*.

All threads in warp execute same instruction at the same time. Each thread has its own instruction address counter and register state, and carries out the current instruction on its own data. Each SM partitions the thread blocks assigned to it into 32-thread warps that it then schedules for execution on available hardware resources.

SIMT allows multiple threads in same warp to execute independently. Even though all threads in a warps start together at the same program address, it's possible for individual threads to have different behavior.

13.0.1. *GPU Core Versus CPU Core.*

- CPU core, relatively heavy-weight, is designed for very complex control logic, optimizing latency T , namely $T(1)$, with $n_{\text{ops}} = 1$.
- GPU core, relatively light-weight, optimized for data-parallel tasks with simpler control logic, optimizing for throughput

cf. pp. 8 of Cheng, Grossman, and McKercher (2014) [4]

GPU capability, described by

- number of CUDA cores
- Memory size

GPU performance, describe by

- *Peak computational performance* := how many single-precision or double-precision floating point calculations that can be processed per second. (gflops (billion floating-point operations per second) or tflops (trillion floating-point calculations per second)).
– EY : 20170911 Peak computational performance = (compute) throughput (???)
- **Memory bandwidth** := ratio at which data can be read from or stored to memory; gigabytes per second, GB/s

cf. pp. 8 of Cheng, Grossman, and McKercher (2014) [4]

CUDA kernels have no support for *static* variables.

Then from **What does “static” mean?**, [Stackoverflow](#),

- (1) A static variable inside a function keeps its value between invocations.
- (2) A static global variable or a function is ”seen” only in the file it's declared in

cf. pp. 38 of Cheng, Grossman, and McKercher (2014) [4]

13.1. Understanding the Nature of Warp Execution; Warps and Thread Blocks. Once a thread block is scheduled to an SM, threads in thread block are further partitioned into warps. A warp consists of 32 consecutive threads and all threads in a warp are executed in SIMT, i.e. all threads execute same instruction, and each thread carries out operation on its own private data.

13.1.1. *Warp Divergence.* For example, consider the following statement:

```
if (cond) {
    ...
} else {
    ...
}
```

Suppose for 16 threads in a warp executing this code, **cond** is **true**, but for other 16 **cond** is **false**. Then half of the warp will need to execute the instructions in the **if** block, and the other half will need to execute the instructions in the **else** block. Threads in the same warp executing different instructions is referred to as **warp divergence**.

If threads of a warp diverge, the warp serially executes each branch path, disabling threads that do not take that path.

e.g. All threads within a warp must take both branches of the `if ... then` statement. If condition is `true` for a thread, it executes the `if` clause; otherwise, the thread stalls while waiting for that execution to complete.

14. STREAMS AND CONCURRENCY

cf. Ch. 6 Streams and Concurrency of Chen, Grossman, and McKercher (2014) [4]

14.0.1. *Introducing Streams and Events.*

- Functions in the CUDA API with *synchronous behavior* block the host thread until they complete.
- Functions in the CUDA API with *asynchronous behavior* return control to host immediately after being called.

14.0.2. *CUDA Streams.*

- *NULL stream*, default stream the kernel launches, implicitly declared, and data transfers use if you don't explicitly specify a stream.
- *non-null streams* explicitly created and managed; if you want to overlap different CUDA operations, you must use non-null streams.

Consider

```
cudaMemcpy(..., cudaMemcpyHostToDevice);|
kernel<<<grid, block>>>(...);
cudaMemcpy(..., cudaMemcpyDeviceToHost);|
```

From device perspective, all 3 operations are issued to default stream, and executed in order they were issued. Device has no awareness any other host operations performed. From host perspective, each data transfer is synchronous and forces idle host time while waiting for them to complete. The kernel launch is *asynchronous*, so host application almost immediately resumes execution afterwards. When performing an asynchronous data trasfer, you must use pinned (or non-pageable) host memory. Pinned memory can be allocated using either `cudaMallocHost` or `cudaHostAlloc`:

```
cudaError_t cudaMallocHost(void **ptr, size_t size);
cudaError_t cudaHostAlloc(void **pHost, size_t size, unsigned int flags);
```

Part 4. More Parallel Computing

15. MATRIX MULTIPLICATION, TILED, WITH SHARED MEMORY

Consider matrix multiplication:

$$\begin{aligned} A &\in \text{Mat}_{\mathbb{K}}(N_i^A, N_j^A) \\ B &\in \text{Mat}_{\mathbb{K}}(N_i^B, N_j^B) \\ C &\in \text{Mat}_{\mathbb{K}}(N_i^C, N_j^C) \end{aligned}$$

$$AB = C,$$

$$\begin{aligned} N_i^A &= N_i^C \equiv N^A \\ N_j^A &= N_i^B \equiv N^B \\ N_j^C &= N_j^B \equiv N^C \end{aligned}$$

$$C_{ij} = A_{ik}B_{kj} = \sum_{k=1}^N A_{ik}B_{kj} \quad \forall \begin{matrix} i = 1, 2, \dots N^A \\ j = 1, 2, \dots N^C \end{matrix}$$

Consider, given *block size* $M \in \mathbb{Z}^+$,

$$\begin{aligned} j_y = 0, 1, \dots \frac{N^A}{M} - 1 &\equiv \text{blockIdx.y} =: j_I \\ j_x = 0, 1, \dots \frac{N^C}{M} - 1 &\equiv \text{blockIdx.x} =: j_J \end{aligned}$$

Given $A \in \text{Mat}(N_i^A, N_j^A)$, given $(i, j) \in (\mathbb{Z}^+)^2$, As C++ works also with pointers as C, "point" the first element of our $A_{\text{sub}} \in \text{Mat}_{\mathbb{K}}(M, M)$ submatrix that we want, to where we "start from" in the "source" matrix A , which is at $A_{iM, jM}$:

$$A_{\text{sub}}(0, 0) := A(iM, jM)$$

and so this means that i.e. we had supposed $i = 0, 1, \dots \frac{N_i^A}{M} - 1$. So consider

$$\begin{aligned} j &= 0, 1 \dots \frac{N_j^A}{M} - 1 \\ i &= i_y + j_I M = 0, 1, \dots N^A - 1 \\ j &= i_x + j_J M = 0, 1, \dots N^C - 1 \end{aligned}$$

and so the crucial step is seen as

$$C_{ij} = \sum_{k=0}^{N^B-1} A_{ik}B_{kj} = \sum_{j_K=0}^{\frac{N^B}{M}-1} \sum_{k=0}^{M-1} A_{i(k+j_K M)}B_{(k+j_K M),j}$$

I originally implemented matrix multiplication based on the CUDA C Programming Guide here at [github : ernestyalumni/-CompPhys/moreCUDA/matmultShare.cu](#) Indeed, the idea from the CUDA C Programming Guide is essentially the above - I will reiterate again, since it was important in Pöpl's presentation for TUM in HPC - Algorithms and Applications (cf. [Introduction to CUDA](#)): given

$$\begin{aligned} A &\in \text{Mat}_{\mathbb{R}}(N_i^A, N_j^A) \\ B &\in \text{Mat}_{\mathbb{R}}(N_i^B, N_j^B) \\ C &\in \text{Mat}_{\mathbb{R}}(N_i^C, N_j^C) \end{aligned}$$

Necessarily, for

$$\begin{aligned} AB &= C \\ (35) \quad A_{ik}B_{kj} &= C_{ij} \quad \forall \begin{matrix} i = 1, 2 \dots N_i^A \\ j = 1, 2, \dots N_j^B \end{matrix} \end{aligned}$$

we have to have

$$\begin{aligned} N_i^C &= N_i^A \\ N_j^C &= N_j^B \\ N_j^A &= N_i^B \end{aligned}$$

We sought thread warp coalescing, in conjunction with the so-called "row-major ordering" (also known as "row-major", "order") in the x -direction of thread block. Keep that in mind for the grid, block thread(s) assignment strategy.

16.1. **Numerical differentiation and interpolation (in C++).** cf. Chapter 3 “Numerical differentiation and interpolation”

Given 5 pts. $\{x_0, x_0 \pm h, x_0 \pm 2h\}$,

of Hjorth-Jensen (2015) [7].

This is how I understand it.

Consider the Taylor expansion for $f(x) \in C^\infty(\mathbb{R})$:

$$f(x) = f(x_0) + \sum_{j=1}^{\infty} \frac{f^{(j)}(x_0)}{j!} h^j$$

For $x = x_0 \pm h$,

$$f(x) = f(x_0 \pm h) = f(x_0) + \sum_{j=1}^{\infty} \frac{f^{(2j)}(x_0)}{(2j)!} h^{2j} \pm \sum_{j=1}^{\infty} \frac{f^{(2j-1)}(x_0)}{(2j-1)!} h^{2j-1}$$

Then

$$\begin{aligned} f(x_0 + 2^k h) - f(x_0 - 2^k h) &= 2 \sum_{j=1}^{\infty} \frac{f^{(2j-1)}(x_0)}{(2j-1)!} (x_0) 2^{k(2j-1)} h^{2j-1} = \\ &= 2 \left[f^{(1)}(x_0) 2^k h + \sum_{j=2}^{\infty} \frac{f^{(2j-1)}(x_0)}{(2j-1)!} 2^{k(2j-1)} h^{2j-1} \right] = \\ &= 2 \left[f^{(1)}(x_0) 2^k h + \frac{f^{(3)}(x_0)}{3!} 2^{k(3)} h^3 + \sum_{j=3}^{\infty} \frac{f^{(2j-1)}(x_0)}{(2j-1)!} 2^{k(2j-1)} h^{2j-1} \right] \end{aligned}$$

So for $k = 1$,

$$f(x_0 + h) - f(x_0 - h) = 2 \left[f^{(1)}(x_0) h + \sum_{j=1}^{\infty} \frac{f^{(2j+1)}(x_0)}{(2j+1)!} h^{2j+1} \right]$$

Now

$$\begin{aligned} f(x_0 + 2^k h) + f(x_0 - 2^k h) - 2f(x_0) &= \\ &= 2 \sum_{j=1}^{\infty} \frac{f^{(2j)}(x_0)}{(2j)!} 2^{2jk} h^{2j} = \\ &= 2 \left[\frac{f^{(2)}(x_0)}{2} 2^{2k} h^2 + \sum_{j=2}^{\infty} \frac{f^{(2j)}(x_0)}{(2j)!} 2^{2jk} h^{2j} \right] = \\ &= 2 \left[\frac{f^{(2)}(x_0)}{2} 2^{2k} h^2 + \frac{f^{(4)}(x_0)}{4!} 2^{4k} h^4 + \sum_{j=3}^{\infty} \frac{f^{(2j)}(x_0)}{(2j)!} 2^{2jk} h^{2j} \right] \end{aligned}$$

Thus for the case of $k = 1$,

$$f(x_0 + h) + f(x_0 - h) - 2f(x_0) = f^{(2)}(x_0) h^2 + 2 \sum_{j=2}^{\infty} \frac{f^{(2j)}(x_0)}{(2j)!} h^{2j}$$

$$\frac{f(x_0 + h) - f(x_0 - h)}{2h} = f^{(1)}(x_0) + \sum_{j=1}^{\infty} \frac{f^{(2j+1)}(x_0)}{(2j+1)!} h^{2j}$$

$$\frac{f(x_0 + h) + f(x_0 - h) - 2f(x_0)}{h^2} = f^{(2)}(x_0) + 2 \sum_{j=2}^{\infty} \frac{f^{(2(j+1))}(x_0)}{(2(j+1))!} h^{2j}$$

A pattern now emerges on how to include more calculations at points $x_0, x_0 \pm 2^k h$ so to obtain better accuracy $O(h^l)$. For instance,

$$f(x_0 + 2h) - f(x_0 - 2h) = 2[f^{(1)}(x_0) 2^1 h + \frac{f^{(3)}(x_0)}{3!} 2^3 h^3 + O(h^5)]$$

$$f(x_0 + h) - f(x_0 - h) = 2[f^{(1)}(x_0) h + \frac{f^{(3)}(x_0)}{3!} h^3 + O(h^5)]$$

$$\implies f'(x_0) = \frac{f(x_0 - 2h) - 8f(x_0 - h) + 8f(x_0 + h) - f(x_0 + 2h)}{12h} + O(h^4)$$

Hjorth-Jensen (2015) [7] argues, on pp. 46-47, that the additional evaluations are time consuming, to obtain further accuracy, so it's a balance.

To summarize, for $O(h^2)$ accuracy,

$$\frac{f(x_0 + h) - f(x_0 - h)}{2h} = f^{(1)}(x_0) + \sum_{j=1}^{\infty} \frac{f^{(2j+1)}(x_0)}{(2j+1)!} h^{2j} \quad O(h^2)$$

$$\frac{f(x_0 + h) + f(x_0 - h) - 2f(x_0)}{h^2} = f^{(2)}(x_0) + 2 \sum_{j=1}^{\infty} \frac{f^{(2j+2)}(x_0)}{(2j+2)!} h^{2j} \quad O(h^2)$$

17. INTERPOLATION

cf. 3.2 Numerical Interpolation and Extrapolation of Hjorth-Jensen (2015) [7]

$$y_0 = f(x_0)$$

Given $N + 1$ pts. $y_1 = f(x_1)$, x_i 's distinct (none of x_i values equal)

$$\vdots$$

$$y_N = f(x_N)$$

We want a polynomial of degree n s.t. $p(x) \in \mathbb{R}[x]$

$$p(x_i) = f(x_i) = y_i \quad i = 0, 1 \dots N$$

$$p(x) = a_0 + a_1(x - x_0) + \dots + a_i \prod_{j=0}^{i-1} (x - x_j) + \dots + a_N(x - x_0) \dots (x - x_{N-1}) = a_0 + \sum_{i=1}^N a_i \prod_{j=0}^{i-1} (x - x_j)$$

$$a_0 = f(x_0)$$

$$a_0 + a_1(x_1 - x_0) = f(x_1)$$

$$\vdots$$

$$a_0 + \sum_{i=1}^k a_i \prod_{j=0}^{i-1} (x_k - x_j) = f(x_k)$$

Hjorth-Jensen (2015) [7] mentions this Lagrange interpolation formula (I haven't found a good proof for it).

$$p_N(x) = \sum_{i=0}^N \prod_{k \neq i} \frac{x - x_k}{x_i - x_k} y_i$$

18. CLASSES (C++)

cf. [C++ Operator Overloading in expression](#)
Take a look at this link: [C++ Operator Overloading in expression](#). This point isn’t emphasized enough, as in Hjorth-Jensen (2015) [7]. This makes doing something like

$$d = a * c + d/b$$

work the way we expect. Kudos to user [fredoverflow](#) for his answer:
“The expression (`e_x*u_c`) is an rvalue, and references to non-const won’t bind to rvalues. Also, member functions should be marked `const` as well.”

18.1. What are lvalues and rvalues in C and C++? [C++ Rvalue References Explained](#)

Original definition of *lvalues* and *rvalues* from *C*:
lvalue - expression *e* that may appear on the left or on the right hand side of an assignment
rvalue - expression that can only appear on right hand side of assignment =.
Examples:

```
int a = 42;
int b = 43;
```

```
// a and b are both l-values
a = b; // ok
b = a; // ok
a = a * b; // ok
```

```
// a * b is an rvalue:
int c = a * b; // ok, rvalue on right hand side of assignment
a * b = 42; // error, rvalue on left hand side of assignment
```

In *C++*, this is still useful as a first, intuitive approach, but
lvalue - expression that refers to a memory location and allows us to take the address of that memory location via the `&` operator.
rvalue - expression that’s not a lvalue
So `&` and reference *functor* can’t act on rvalue’s.

18.2. **Functors (C++); C++ Functors; C++ class templates.** For categories **A**, **B**, consider trying to understand, wrap your mind around C++, especial C++11/14 style functors. The key *insight* is *composability*: use the mathematical property of **composition**.

(38)

$$\begin{array}{ccc} \mathbf{A} & \xrightarrow{F} & \mathbf{B} \\ \downarrow \langle \text{Type} \rangle & & \downarrow \langle \text{Type} \rangle \\ \text{Type} \circ \mathbf{A} & \xrightarrow{\langle \text{Type} \rangle \circ F} & \text{Type} \circ \mathbf{B} \end{array}$$

This webpage from K Hong helped with understanding C++11/14 style functors: [C++ Tutorial - Functors\(Function Objects\)](#) - 2017, cf. <http://www.bogotobogo.com/cplusplus/functors.php>
I implemented all of that in the webpage here: [github functors.cpp](#) , [ernestyalumni/CompPhys/Cpp/Cpp14/functors.cpp](#)
I will try to write a dictionary between math, i.e. mathematical formulation, and the class templates, structs.
I looked at pp. 213 of Conlon, pp. 513 of Rotman, and looked up keywords “functional.”
Consider the bilinear functional that results in a function, i.e. $\mathcal{C}^\infty(\mathbb{R})$.

i.e.

$$\begin{aligned} \mathbb{R} \times \mathbb{R} &\rightarrow C^\infty(\mathbb{R}) \\ \mathbb{R} \times \mathbb{R} &\rightarrow \text{Hom}_{\mathbb{R}}(\mathbb{R}, \mathbb{R}) \\ (a, b) &\mapsto f(x) = ax + b \end{aligned} \tag{39}$$

with

$$\text{Hom}_{\mathbb{R}}(\mathbb{R}, \mathbb{R}) \ni \{\mathbb{R} \xrightarrow{f} \mathbb{R}\}$$

Compare this directly to `class Line` in `functor.cpp`. Note that this is *class object working as a functor*:

```
class Line {
    double a;           // slope
    double b;           // y-intercept

public:
    Line(double slope = 1, double yintercept = 1) :
        a(slope), b(yintercept) { }
    double operator()(double x){
        return a*x + b;
    }
};
```

Now consider the use of C++ function object, but with non-type template, C++ templates:

$$\begin{aligned} \mathbb{R} &\rightarrow \text{Hom}_{\mathbb{R}}(\mathbb{R}, \mathbb{R}) \\ x &\mapsto f(y) = y + x \end{aligned} \tag{40}$$

But suppose $y \in \mathbb{R}^d$, e.g. $y_i \in \mathbb{R}, i = 0, 1, \dots, d - 1$.

$$\begin{aligned} \mathbb{R} &\rightarrow \text{Hom}_{\mathbb{R}}(\mathbb{R}^d, \mathbb{R}^d) \\ x &\mapsto f(y) = y + x \text{ or } (f(y))_i = y_i + x \quad \forall i = 0, 1, \dots, d - 1 \end{aligned} \tag{41}$$

So for the *class template*, to generalize \mathbb{R} to some choice of field \mathbb{K} , generalize \mathbb{R}^d to \mathbf{R} -module R .

$$\begin{aligned} \mathbb{K} &\rightarrow \text{Hom}_{\mathbb{K}}(\mathbb{K}, \mathbb{K}) \\ x &\mapsto (f(y))_i = y_i + x \quad \forall i = 0, 1, \dots, d - 1 \end{aligned} \tag{42}$$

And so the strategy is to generalize type by the class template (declaration), define the Hom from M to M by defining the Hom from \mathbb{K} to \mathbb{K} for each element of M .

Compare this directly to the code for `class Add` in `functor.cpp`:

```
template <typename T>
class Add
{
    T x;

public:
    Add(T xx) : x(xx) { }
    void operator()(T& e) const { e += x; }
};
...
```

`std::for_each(v2.begin(), v2.end(), Add<int>(10));`

`std::for_each(v2.begin(), v2.end(), Add<int>(*v2.begin()))`);

Notice how the construction of the Hom needs an input.

19. NUMERICAL INTEGRATION

19.0.1. *Trapezoid rule (or trapezoidal rule).* See [Integrate.ipynb](#).

From there, consider integration on $[a, b]$, considering $h := \frac{b-a}{N}$, and $N+1$ (grid) points, $\{a, a+h, a+2h, \dots, a+jh, \dots, a+Nh = b\}_{j=0\dots N}$.

Then $\frac{N}{2}$ pts. are our “ x_0 ”; x_0 ’s $= \{a+h, a+3h, \dots, a+(2j-1)h, \dots, a+(\frac{2N}{2}-1)h\}_{j=1\dots \frac{N}{2}}$.

Notice how we really need to care about if N is even or not. If N is not even, then we’d have to deal with the integration at the integration limits and choosing what to do.

Then

$$\begin{aligned} \int_a^b f(x)dx &= \sum_{j=1}^{N/2} \int_{a+(2j-1)h-h}^{a+(2j-1)h+h} f(x)dx = \sum_{j=1}^{N/2} \frac{h}{2} (2f(a+(2j-1)h) + f(a+2(j-1)h) + f(a+2jh)) = \\ &= h(f(a)/2 + f(a+h) + \dots + f(b-h) + \frac{f(b)}{2}) = h \left(\frac{f(a)}{2} + \sum_{j=1}^{N-1} f(a+jh) + \frac{f(b)}{2} \right) \end{aligned}$$

19.0.2. *Midpoint method or rectangle method.* .

Let $h := \frac{b-a}{N}$ be the step size. The grid is as follows:

$$\{a, a+h, \dots, a+jh, \dots, a+Nh = b\}_{j=0\dots N}$$

The desired midpoint values are at the following N points:

$$\{a + \frac{h}{2}, a + \frac{3}{2}h, \dots, a + \frac{(2j-1)h}{2}, \dots, a + \left(N - \frac{1}{2}\right)h\}_{j=1\dots N}$$

and so

$$(43) \quad \int_a^b f(x)dx \approx \sum_{j=1}^N f(x_j)h = \sum_{j=1}^N f\left(a + \frac{(2j-1)h}{2}\right)h$$

19.0.3. *Simpson rule.* The idea is to take the next “order” in the Lagrange interpolation formula, the second-order polynomial, and then we can rederive Simpson’s rule. The algebra is worked out in [Integrate.ipynb](#).

From there, then we can obtain Simpson’s rule,

$$\begin{aligned} \int_a^b f(x)dx &= \sum_{j=1}^{N/2} \int_{a+2(j-1)h}^{a+2jh} f(x)dx = \sum_{j=1}^{N/2} \frac{h}{3} (4f(a+(2j-1)h) + f(a+2(j-1)h) + f(a+2jh)) = \\ &= \frac{h}{3} \left[f(a) + f(b) + \sum_{j=1}^{N/2} 4f(a+(2j-1)h) + 2 \sum_{j=1}^{N/2-1} f(a+2jh) \right] \end{aligned}$$

19.1. **Gaussian Quadrature.** cf. Hjorth-Jensen (2015) [7], Section 5.3 Gaussian Quadrature, Chapter 5 Numerical Integration

20. RUNGE-KUTTA METHODS (RK)

cf. Hjorth-Jensen (2015) [7], Section 8.4 *More on finite difference methods, Runge-Kutta methods* and *wikipedia*, “Runge-Kutta methods,” https://en.wikipedia.org/wiki/Runge%E2%80%93Kutta_methods

While Runge-Kutta methods are useful initially for ordinary differential equations, remember that under certain (very general, in fact, for smooth manifolds even) conditions, vector fields admit integral lines and there you simply solve an system of linear ODEs.

21. PARTIAL DIFFERENTIAL EQUATIONS

21.0.1. *Explicit Scheme.* cf. Hjorth-Jensen (2015) [7], Section 10.2.1 Explicit Scheme

Consider

$$(44) \quad \begin{aligned} u &= u(t, x) \in C^\infty(M) = C^\infty(\mathbb{R} \times N) \\ \Delta u &= \frac{\partial u}{\partial t}(t, x) \end{aligned}$$

with initial conditions

$$u(0, x) = g(x) \quad \forall 0 < x < L_x \text{ or } x \in \Omega \subset N \text{ (in general)}$$

e.g. $L_x = 1$

and boundary conditions

$$\begin{aligned} u(t, 0) &= a(t) \quad t \geq 0 \\ u(t, L) &= b(t) \quad t \geq 0 \end{aligned}$$

Consider the act of discretization as a transformation or a functor:

$$(45) \quad \begin{aligned} \frac{\partial u}{\partial t} &= \frac{u(t+\Delta t, x) - u(t, x)}{\Delta t} + O(\Delta t) \xrightarrow{\text{discretize}} \frac{u(t_j + \Delta t, x_i) - u(t_j, x_i)}{\Delta t} \\ \frac{\partial^2 u}{\partial (x^i)^2} &\approx \frac{u(t, x+\Delta x) - 2u(t, x) + u(t, x-\Delta x)}{(\Delta x)^2} \xrightarrow{\text{discretize}} \frac{u(t_j, x_i + \Delta x) - 2u(t_j, x_i) + u(t_j, x_i - \Delta x)}{(\Delta x)^2} \end{aligned}$$

$$(46) \quad \implies u(t_j + \Delta t, x_i) = \frac{\Delta t}{(\Delta x)^2} u(t_j, x_i + \Delta x) + \left(1 - \frac{2\Delta t}{(\Delta x)^2}\right) u(t_j, x_i) + \frac{\Delta t}{(\Delta x)^2} u(t_j, x_i - \Delta x)$$

Discretize the initial conditions:

$$(47) \quad u(0, x) = g(x) \xrightarrow{\text{discretize}} u(0, x_i) = g(x_i)$$

and so, for the first step,

$$(48) \quad u(\Delta t, x_i) = \frac{\Delta t}{(\Delta x)^2} g(x_i + \Delta x) + \left(1 - \frac{2\Delta t}{(\Delta x)^2}\right) g(x_i) + \frac{\Delta t}{(\Delta x)^2} g(x_i - \Delta x)$$

It would appear to be instructive to show what discretize is doing, as a commutative diagram:

$$(49) \quad \begin{array}{ccc} \mathbb{R} \times N & \xrightarrow{\text{discretize}} & \mathbb{Z}^+ \times (\{0 \dots L_x - 1\} \times \{0 \dots L_y - 1\} \times \{0 \dots L_z - 1\}) \subset \mathbb{Z}^+ \times \mathbb{Z}^d \\ \downarrow & & \downarrow \\ C^\infty(\mathbb{R} \times N) & \xrightarrow{\text{discretize}} & C^\infty(\mathbb{Z}^+ \times (\{0 \dots L_x - 1\} \times \{0 \dots L_y - 1\} \times \{0 \dots L_z - 1\})) \subset C^\infty(\mathbb{Z}^+ \times \mathbb{Z}^d) \end{array}$$

Then there's this so-called "sparse" (I think this means that there are a lot more zeros as values for the entries in a matrix than there are nonzero values), tridiagonal (diagonal and next to the diagonal, diagonals) matrices representation of the time-evolution transformation/operator. I'll call this transformation over to this matrix representation, this functor, *matricer*.

$$(50) \quad C^\infty(\mathbb{Z}^+ \times (\{0 \dots L_x - 1\} \times \{0 \dots L_y - 1\} \times \{0 \dots L_z - 1\})) \xrightarrow{\text{matricer}} \mathbb{Z}^+ \times \mathbb{R}^{L_x L_y L_z} = \mathbb{Z}^+ \times \text{Mat}_{\mathbb{R}}(L_x, L_y, L_z)$$

For the boundary conditions,

$$\begin{array}{ll} u(t, 0) = a(t) & t \geq 0 \\ u(t, L) = b(t) & t \geq 0 \end{array} \xrightarrow{\text{discretize}} \begin{array}{ll} u(t, 0) = a(t) & \\ u(t, x_{L_x-1}) = b(t) & \end{array}$$

For 1-dim. case, $V_j \in \text{Mat}_{\mathbb{R}}(L_x)$ (vector or "column" matrix)

$$V_j = \begin{bmatrix} u(t_j, x_2) \\ u(t_j, x_3) \\ \vdots \\ u(t_j, x_{L_x-3}) \end{bmatrix}$$

and so the "time-evolution" operator/transformation is

$$(51) \quad \hat{A} \in \text{Mat}_{\mathbb{R}}(L_x - 4, L_x - 4) = \text{End}(\mathbb{R}^{L_x-4}, \mathbb{R}^{L_x-4})$$

$$\hat{A} = \begin{bmatrix} \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} & 0 & 0 & \dots & 0 \\ 0 & \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} & 0 & \dots & 0 \\ & & & \ddots & & & \\ 0 & 0 & \dots & 0 & \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} \end{bmatrix}$$

Note that in the specialized 1-dim. case where $a(t) = b(t) = 0$ (boundary conditions for both ends is of value 0), then we can, in this specialized case, define the matrix \hat{A} to be

$$(52) \quad \hat{A} \in \text{Mat}_{\mathbb{R}}(L_x - 2, L_x - 2) = \text{End}(\mathbb{R}^{L_x-2}, \mathbb{R}^{L_x-2})$$

$$\hat{A} = \begin{bmatrix} 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} & 0 & 0 & \dots & 0 & 0 \\ \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} & 0 & 0 & \dots & 0 \\ 0 & \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} & 0 & \dots & 0 \\ & & & \ddots & & & \\ 0 & 0 & \dots & 0 & \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} & \frac{\Delta t}{(\Delta x)^2} \\ 0 & 0 & 0 & \dots & 0 & \frac{\Delta t}{(\Delta x)^2} & 1 - 2\frac{\Delta t}{(\Delta x)^2} \end{bmatrix}$$

e.g.

$$g(x) = \sin\left(\frac{\pi}{l_x}x\right)$$

with an analytic solution of

$$u(t, x) = \sin\left(\frac{\pi}{l_x}x\right) \exp\left(-\left(\frac{\pi}{l_x}\right)^2 t\right)$$

It was bizarre to me that in Hjorth-Jensen (2015) [7], Section 10.2.1 Explicit Scheme, on pp. 307, Hjorth-Jensen went through a lengthy and thorough explanation of this "matricer" operation, i.e. doing the time-evolution with a matrix on a vector of values at grid points, and yet in the pseudo-code, essentially, there is no trace of that matrix! It's essentially a local "stencil" operation. What the heck?

I present the "matrix form" code in my github repository: '[diffusion1dexplicit.cpp](#)'. To be explicit, the code follows the previous writeup, with its notation, essentially one-to-one.

21.0.2. *Implicit scheme.* cf. Hjorth-Jensen (2015) [7], Section 10.2.2 Implicit Scheme

Consider

$$(53) \quad \begin{array}{ll} \text{backwards formula :} & \frac{\partial u}{\partial t}(t, x) \approx \frac{u(t_j, x_i) - u(t_j - \Delta t, x_i)}{\Delta t} \text{ or even} \\ \text{midpoint approximations :} & \frac{\partial u}{\partial t}(t, x) \approx \frac{u(t_j + \Delta t, x_i) - u(t_j - \Delta t, x_i)}{2\Delta t} \end{array}$$

Consider the same spatial discretization as before for the Laplacian:

$$\frac{\partial^2 u}{\partial (x^i)^2} \approx \frac{u(t, x + \Delta x) - 2u(t, x) + u(t, x - \Delta x)}{(\Delta x)^2} \xrightarrow{\text{discretize}} \frac{u(t_j, x_i + \Delta x) - 2u(t_j, x_i) + u(t_j, x_i - \Delta x)}{(\Delta x)^2}$$

and so for the backwards formula case,

$$(54) \quad \frac{u(t_j, x_i) - u(t_j - \Delta t, x_i)}{\Delta t} = \frac{u(t_j, x_i + \Delta x) - 2u(t_j, x_i) + u(t_j, x_i - \Delta x)}{(\Delta x)^2}$$

$$u(t_j - \Delta t, x_i) = -\frac{\Delta t}{(\Delta x)^2}u(t_j, x_i + \Delta x) + \left(1 + \frac{2\Delta t}{(\Delta x)^2}\right)u(t_j, x_i) - \frac{\Delta t}{(\Delta x)^2}u(t_j, x_i - \Delta x)$$

resulting in the backwards time-evolution matrix \hat{A} (keeping in mind the special boundary condition of 0 value for u at both ends, for the sake of a simplified discussion):

$$(55) \quad \hat{A} \in \text{Mat}_{\mathbb{R}}(L_x - 2, L_x - 2) = \text{End}(\mathbb{R}^{L_x-2}, \mathbb{R}^{L_x-2})$$

$$\hat{A} = \begin{bmatrix} 1 + 2\frac{\Delta t}{(\Delta x)^2} & -\frac{\Delta t}{(\Delta x)^2} & 0 & 0 & \dots & 0 \\ -\frac{\Delta t}{(\Delta x)^2} & 1 + 2\frac{\Delta t}{(\Delta x)^2} & -\frac{\Delta t}{(\Delta x)^2} & 0 & \dots & 0 \\ & & & \ddots & & \\ 0 & 0 & \dots & -\frac{\Delta t}{(\Delta x)^2} & 1 + 2\frac{\Delta t}{(\Delta x)^2} & -\frac{\Delta t}{(\Delta x)^2} \\ 0 & 0 & 0 & \dots & -\frac{\Delta t}{(\Delta x)^2} & 1 + 2\frac{\Delta t}{(\Delta x)^2} \end{bmatrix}$$

$$\hat{A}u^j = u^{j-1}$$

and so

$$\hat{A}^{-1}u^{j-1} = u^j$$

21.1. **Crank-Nicolson method.** Hjorth-Jensen (2015) [7], Section 10.2.3 Crank Nicholson scheme has a write up about the Crank-Nicolson method, but the derivation is unclear (and sloppy, in that after the Taylor expansions, he says that the terms magically add up to the desired result, and the "approximation" notation is vacuous in that nothing new was conveyed). Rather, look at [Crank Nicolson Scheme for the Heat Equation](#) for a clearer derivation, that drives home the point of looking at the time between time steps.

Consider the following Taylor expansions about $t^{1/2} := t + \frac{\Delta t}{2}$.

$$u(t + \Delta t, x) = u\left(t + \frac{\Delta t}{2} + \frac{\Delta t}{2}, x\right) \equiv u(t^{1/2} + \frac{\Delta t}{2}, x) = u(t^{1/2}, x) + \frac{\Delta t}{2} \frac{\partial u}{\partial t}(t^{1/2}, x) + \frac{1}{2} \left(\frac{\Delta t}{2}\right)^2 \frac{\partial^2 u}{\partial t^2}(t^{1/2}, x) + O((\Delta t)^3)$$

$$u(t, x) = u\left(t + \frac{\Delta t}{2} - \frac{\Delta t}{2}, x\right) \equiv u(t^{1/2} - \frac{\Delta t}{2}, x) = u(t^{1/2}, x) - \frac{\Delta t}{2} \frac{\partial u}{\partial t}(t^{1/2}, x) + \frac{1}{2} \left(\frac{\Delta t}{2}\right)^2 \frac{\partial^2 u}{\partial t^2}(t^{1/2}, x) + O((\Delta t)^3)$$

$$u(t + \Delta t, x) - u(t, x) = \Delta t \frac{\partial u}{\partial t}(t^{1/2}, x) + O((\Delta t)^3)$$

$$\implies \frac{\partial u}{\partial t}(t^{1/2}, x) = \frac{u(t + \Delta t, x) - u(t, x)}{\Delta t}$$

with $O((\Delta t)^2)$ order of accuracy.

To approximate

$$\frac{\partial^2 u}{\partial x^2}(t + \frac{\Delta t}{2}, x) \equiv \frac{\partial^2 u}{\partial x^2}(t^{1/2}, x)$$

use average of second, centered differences for $\frac{\partial^2 u}{\partial x^2}(t + \Delta t, x)$ and $\frac{\partial^2 u}{\partial x^2}(t, x)$.

$$\frac{\partial^2 u}{\partial x^2}(t + \frac{\Delta t}{2}, x) \approx \frac{1}{2} \left[\frac{u(t + \Delta t, x + \Delta x) - 2u(t + \Delta t, x) + u(t + \Delta t, x - \Delta x)}{(\Delta x)^2} + \frac{u(t, x + \Delta x) - 2u(t, x) + u(t, x - \Delta x)}{(\Delta x)^2} \right]$$

Then for

$$\frac{\partial u}{\partial t}(t^{1/2}, x) = C_0 \Delta u(t^{1/2}, x) \xrightarrow{discretize}$$

$$u(t + \Delta t, x) - u(t, x) = \frac{1}{2} C_0 \frac{\Delta t}{(\Delta x)^2} (u(t + \Delta t, x + \Delta x) - 2u(t + \delta t, x) + u(t + \Delta t, x - \Delta x)) + \frac{1}{2} C_0 \frac{\Delta t}{(\Delta x)^2} u(t, x + \Delta x) + \frac{-\Delta t}{(\Delta x)^2} C_0 u(t, x) + \frac{1}{2} \frac{\Delta t}{(\Delta x)^2} C_0 u(t, x - \Delta x)$$

Let

$$\alpha := \frac{\Delta t}{(\Delta x)^2}$$

Then

$$(56) \quad \frac{-1}{2} \alpha u(t + \Delta t, x + \Delta x) + (1 + \alpha) u(t + \Delta t, x) - \frac{\alpha}{2} u(t + \Delta t, x - \Delta x) = \frac{\alpha}{2} u(t, x + \Delta x) + (1 - \alpha) u(t, x) + \frac{\alpha}{2} u(t, x - \Delta x)$$

In general

$$(57) \quad \begin{aligned} & \frac{-\alpha}{2} C_0 u(t + \Delta t, x + \Delta x) + (1 + C_0 \alpha) u(t + \Delta t, x) - \frac{C_0 \alpha}{2} u(t + \Delta t, x - \Delta x) = \\ & = \frac{C_0 \alpha}{2} u(t, x + \Delta x) + (1 - C_0 \alpha) u(t, x) + \frac{C_0 \alpha}{2} u(t, x - \Delta x) \end{aligned}$$

This scheme necessitates a matrix representation. In matrix form,

$$(58) \quad \begin{aligned} & \begin{bmatrix} 1 + C_0 \alpha & -\frac{\alpha C_0}{2} & 0 & \dots & 0 & 0 & \dots & 0 \\ -\frac{C_0 \alpha}{2} & 1 + C_0 \alpha & -\frac{\alpha C_0}{2} & 0 & 0 & \dots & 0 & \\ 0 & -\frac{C_0 \alpha}{2} & 1 + C_0 \alpha & -\frac{C_0 \alpha}{2} & 0 & \dots & 0 & \\ & & & \ddots & & & & \\ 0 & 0 & \dots & 0 & \frac{-\alpha}{2} & 1 + C_0 \alpha & \frac{-\alpha}{2} & \\ 0 & 0 & \dots & 0 & 0 & \frac{-\alpha}{2} & 1 + C_0 \alpha & \end{bmatrix} u_i^{t+\Delta t} = \\ & = \begin{bmatrix} 1 - C_0 \alpha & \frac{C_0 \alpha}{2} & 0 & 0 & 0 & \dots & 0 & \\ \frac{C_0 \alpha}{2} & 1 - C_0 \alpha & \frac{C_0 \alpha}{2} & 0 & 0 & \dots & 0 & \\ 0 & \frac{C_0 \alpha}{2} & 1 - C_0 \alpha & \frac{C_0 \alpha}{2} & 0 & \dots & 0 & \\ & & & \ddots & & & & \\ 0 & 0 & 0 & \dots & \frac{C_0 \alpha}{2} & 1 - C_0 \alpha & \frac{C_0 \alpha}{2} & \\ 0 & 0 & 0 & \dots & 0 & \frac{C_0 \alpha}{2} & 1 - C_0 \alpha & \end{bmatrix} u_i^t \\ & \implies \hat{B} u_i^{t+\Delta t} = \hat{A} u_i^t \text{ or} \\ & u_i^{t+\Delta t} = \hat{B}^{-1} \hat{A} u_i^t \end{aligned}$$

21.2. **Jacobi method, SOR method, for the Laplace and Poisson equation.** 3.1 **Poisson's Equation and Relaxation Methods** of 410-505 Physics had a good, online, clear explanation of Jacobi method and improvements, namely the Successive Over Relaxation (SOR) method, applied to Laplace and Poisson equation, with clearly labelled diagrams: <http://www.physics.buffalo.edu/phy410-505/2011/topic3/app1/index.html>

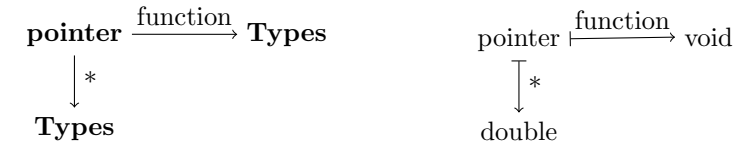
22. CALL BY REFERENCE - CALL BY VALUE, CALL BY REFERENCE (IN C AND IN C++)

cf. pp. 58, 2.10 Pointers Ch. 2 Scientific Programming in C, Fitzpatrick [6] `printfact3.c`, `printfact3.c`

pass pointer, pass by reference, call by pointer, call by reference

In C:

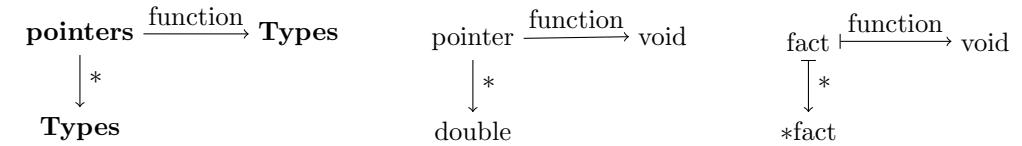
- *function prototype* -



\implies
`void factorial(double *)`

where for factorial, it's just your choice of name for *function*.

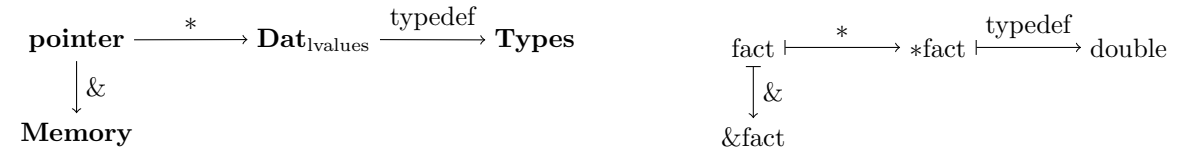
- *function definition* -



\implies

`void function(double *fact) { ... }`

Inside the function definition,

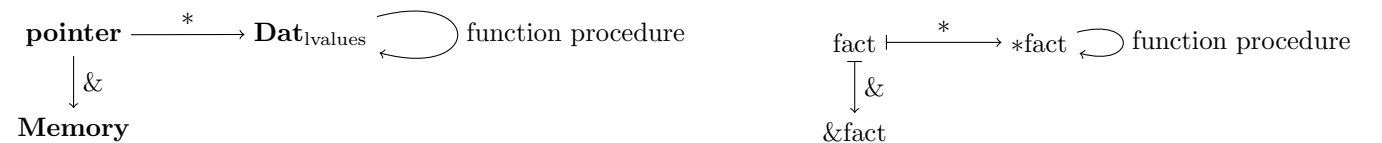


and so, for instance, in the function definition, you can do things like this:

```
*fact = 1
*fact *= (double) n
```

and so notice that from `*fact = 1`, `*fact` is a lvalue.

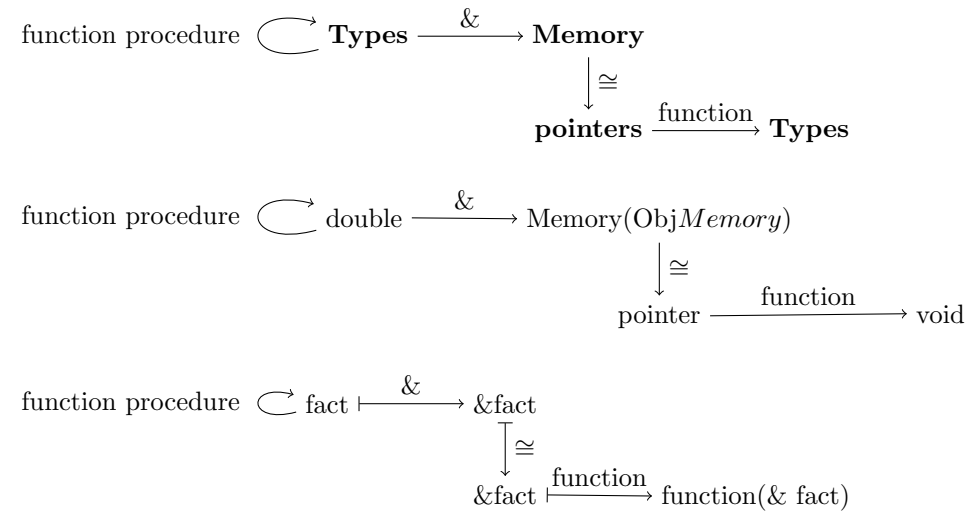
– *function procedure*



\implies

`*fact *= (double) n`

- “Using” the function, function “instantiation”, “calling” the function, i.e. “running” the function



where, again simply note the notation, that we’re using *function* and *factorial*, *fact* for *nameofpointer*, interchangeably: see [printfact3.c](#) for the example I’m referring to.

Again, in *C*, consider a *pointer to a function* passed to another function as an argument. Take a look at [passfunction.c](#) simultaneously.

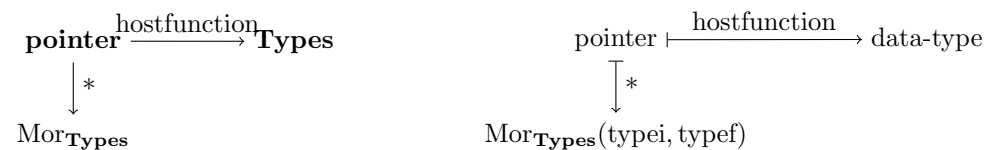
- *function prototype* -



\Rightarrow

`void hostfunction(double (*)(double))`

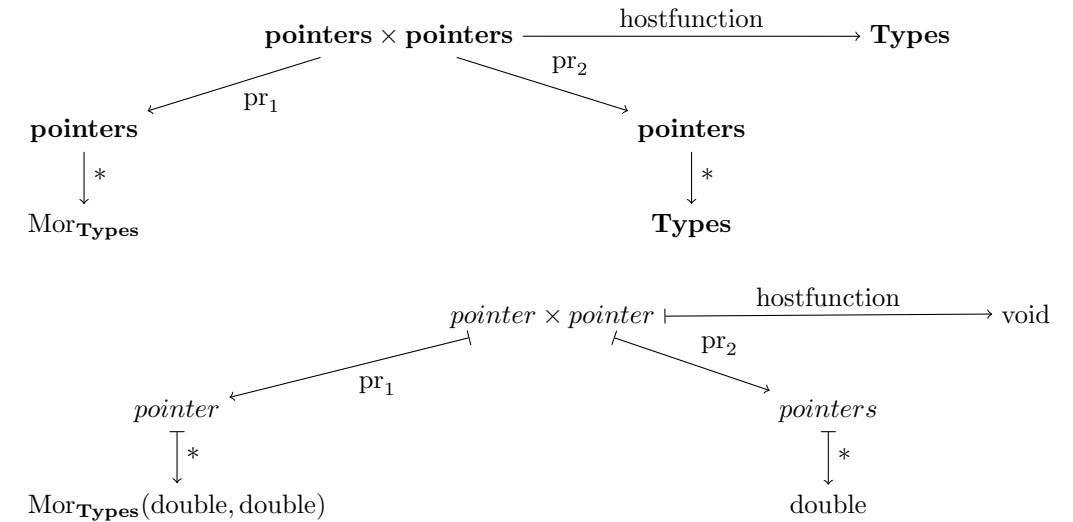
We could further generalize this syntax, simply for syntax and notation sake, as such:



\Rightarrow

`data-type hostfunction(typef (*)(typei))`

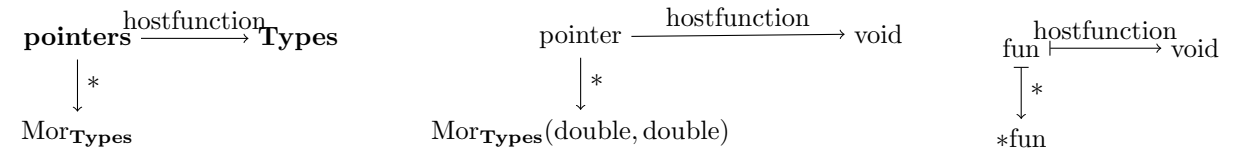
For practice, consider more than 1 argument in our function, and the other argument, for practice, is a pointer, we’re “passing by reference.”



\Rightarrow

`void hostfunction(double (*)(double), double *)`

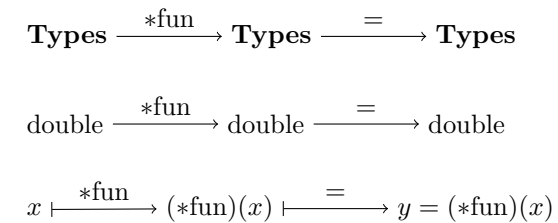
- *function definition*



\Rightarrow

`void hostfunction(double (*fun)(double)) { ... }`

- *Inside* the function definition,



\Rightarrow

`y = (*fun)(x)`

- “Using” the function - the *actual* syntax for “passing” a function into a function is interesting (peculiar?): you only need the *name* of the function.

Let’s quickly recall how a function is prototyped, “declared” (or, i.e., defined), and used:

– *function prototype* -

$$\begin{array}{ccc} \mathbf{Types} & \xrightarrow{\text{fun1}} & \mathbf{Types} \\ \\ \text{double} & \xrightarrow{\text{fun1}} & \text{double} \end{array}$$

⇒

`double fun1(double)`

– *function definition* -

$$\begin{array}{ccc} \mathbf{Types} & \xrightarrow{\text{fun1}} & \mathbf{Types} \\ \\ \text{double} & \xrightarrow{\text{fun1}} & \text{double} \end{array}$$

$$z \mapsto 3.0z * z - z (= 3z^2 - z)$$

⇒

`double fun1(double z) { ... }`

– Using function - `fun1(z)`

and so

$$\text{fun1} \in \text{Mor}_{\mathbf{Types}}(\text{double}, \text{double})$$

And so again, it’s interesting in terms of syntax that all you need is the *name* of the function to pass into the arguments of the “host function” when using the host function:

$$\text{Mor}_{\mathbf{Types}} \xrightarrow{\text{hostfunction}} \mathbf{Types}$$

$$\text{Mor}_{\mathbf{Types}}(\text{double}, \text{double}) \xrightarrow{\text{hostfunction}} \text{void}$$

$$\text{fun1} \xrightarrow{\text{hostfunction}} \text{hostfunction}(\text{fun1})$$

⇒

`hostfunction(fun1)`

22.0.1. *C++ extensions, or how C++ pass by reference (pass a pointer to argument) vs. C.* Recall how C passes by reference, and look at Fitzpatrick [6], pp. 83-84 for the `square` function:

- *function prototype* -

$$\begin{array}{ccc} \text{pointer} & \xrightarrow{\text{square}} & \mathbf{Types} \\ \downarrow * & & \\ \mathbf{Types} & & \end{array} \qquad \begin{array}{ccc} \text{pointer} & \xrightarrow{\text{square}} & \text{void} \\ \downarrow * & & \\ \text{double} & & \end{array}$$

⇒

`void square(double *)`

- *function definition* -

$$\begin{array}{ccc} \text{pointers} & \xrightarrow{\text{square}} & \mathbf{Types} \\ \downarrow * & & \\ \mathbf{Types} & & \end{array} \qquad \begin{array}{ccc} \text{pointer} & \xrightarrow{\text{function}} & \text{void} \\ \downarrow * & & \\ \text{double} & & \end{array} \qquad \begin{array}{ccc} y & \xrightarrow{\text{square}} & \text{void} \\ \downarrow * & & \\ *y & & \end{array}$$

⇒

`void square(double *y) { ... }`

Inside the function definition,

$$\text{pointer} \xrightarrow{*} \mathbf{Dat}_{\text{values}} \xrightarrow{\text{typedef}} \mathbf{Types} \qquad y \xrightarrow{*} *y \xrightarrow{\text{typedef}} \text{double}$$

and so, for instance, in the function definition, you can do things like this:

$$*y = x*x$$

- “Using” the function, function “instantiation”, “calling” the function, i.e. “running” the function

$$\begin{array}{ccc} \text{function procedure} & \hookrightarrow & \mathbf{Types} \xrightarrow{\&} \mathbf{Memory} \\ & & \downarrow \cong \\ & & \text{pointers} \xrightarrow{\text{square}} \mathbf{Types} \end{array}$$

$$\begin{array}{ccc} \text{function procedure} & \hookrightarrow & \text{double} \xrightarrow{\&} \text{Memory}(\text{ObjMemory}) \\ & & \downarrow \cong \\ & & \text{pointer} \xrightarrow{\text{square}} \text{void} \end{array}$$

$$\begin{array}{ccc} \text{function procedure} & \hookrightarrow & \text{res} \xrightarrow{\&} \&\text{res} \\ & & \downarrow \cong \\ & & \&\text{res} \xrightarrow{\text{square}} \text{square}(\&\text{res}) \end{array}$$

22.0.2. *C++ syntax for dealing with passing pointers (and arrays) into functions.* However, in *C++*, a lot of the dereferencing `*` and referencing `&` is not explicitly said so in the syntax. In this syntax, passing by reference is indicated by prepending the `&` ampersand to the variable name, in function declaration (prototype and definition). We don’t have to explicitly deference the argument in the function (it’s done behind the scene) and syntax-wise (it seems), we only have to refer to the argument by regular local name.

Indeed, the syntax appears “shortcutted” greatly:

- *function prototype* -

$$\text{pointer} \times \mathbf{Types} \xrightarrow{\text{function}} \mathbf{Types} \qquad \text{pointer}, \text{double} \xrightarrow{\text{function}} \text{void}$$

⇒

`void function(double &)`

- *function definition* -

$$\text{pointers} \times \mathbf{Types} \xrightarrow{\text{square}} \mathbf{Types} \qquad \text{pointer, double} \xrightarrow{\text{function}} \text{void} \qquad \&z, y \vdash \xrightarrow{\text{function}} \text{function(double \&y)}$$

⇒

```
void function(double &y) { ... }
```

Inside the function definition,

$$\text{double} \xrightarrow{\text{End(double, double)}} \text{double} \qquad y \xrightarrow{\text{End(double, double)}} y = x * x$$

and so, for instance, in the function definition, you can do things like this:

```
y = x*x
```

with no deferencing needed.

- “Using” the function, function “instantiation”, “calling” the function, i.e. “running” the function

$$\mathbf{Types} \xrightarrow{\text{function}} \mathbf{Types} \qquad \text{double} \xrightarrow{\text{function}} \text{void} \qquad \text{res} \vdash \xrightarrow{\text{function}} \text{function(res)}$$

22.0.3. *C++ note on arrays.* For dealing with arrays, Stroustrup (2013) [8], on pp. 12 of Chapter 1 The Basics, Section 1.8 Pointers, Arrays, and References, does the following:

- *array declaration* -

```
type a[n]; // type[n]; array of n type's
```

- “Using” arrays in function prototypes, i.e. passing into arguments of functions for *function prototypes*

```
data-type function( type * arrayname)
```

- “Using” arrays when “using” functions, i.e. passing into arguments when a function is “called” or “executed”

```
function( arrayname )
```

Fitzpatrick [6] mentions using `inline` for short functions, no more than 3 lines long, because of memory cost of calling a function.

22.0.4. *Need a CUDA, C, C++, IDE? Try Eclipse!* This website has a clear, lucid, and pedagogical tutorial for using Eclipse: [Creating Your First C++ Program in Eclipse](#). But it looks like I had to pay. Other than the well-written tips on the webpage, I looked up stackexchange for my Eclipse questions (I had difficulty with the Eclipse documentation).

Others, like myself, had questions on how to use an IDE like Eclipse when learning CUDA, and “building” (is that the same as compiling?) and running only single files.

My workflow: I have a separate, in my file directory, folder with my github repository clone that’s local.

I start a New Project, CUDA Project, in Eclipse. I type up my single file (I right click on the `src` folder and add a ‘Source File’). I build it (with the Hammer, Hammer looking icon; yes there are a lot of new icons near the top) and it runs. I can then run it again with the Play, triangle, icon.

I found that if I have more than 1 (2 or more) file in the `src` folder, that requires the `main` function, it won’t build right.

So once a file builds and it’s good, I, in Terminal, `cp` the file into my local github repository. Note that from there, I could use the `nvcc` compiler to build, from there, if I wanted to.

Now with my file saved (for example, `helloworldkernel.cu`), then I can delete it, without fear, from my, say, `cuda-workplace`, from the right side, “C/C++ Projects” window in Eclipse.

23. ON CUDA BY EXAMPLE

Take a look at 3.2.2 A Kernel Call, a Hello World in CUDA C, with a simple kernel, on pp. 23 of Sanders and Kandrot (2010) [9] and on github, `[helloworldkernel.cu]`(<https://github.com/ernestyalumni/CompPhys/blob/master/CUDA-By-Example/helloworldkernel.cu>). Let’s work out the functor interpretation for practice.

- *function definition* -

$$\mathbf{Types} \xrightarrow{\text{kernel}} \mathbf{Types} \\ \text{void} \xrightarrow{\text{kernel}} \text{void}$$

where `kernel` ∈ `__global__`

⇒

```
__global__ void kernel(void) { }
```

CUDA C adds the `__global__` qualifier to standard C to *alert the compiler that the function, kernelfunction*, should be compiled to run on the *device*, not the host (pp. 24 [9]).

- “Using”, “calling”, “running” function -

$$<<<>>>: (n_{\text{block}}, n_{\text{threads}}) \times \text{kernelfunction} \mapsto \text{kernelfunction} <<< n_{\text{block}}, n_{\text{threads}} >>> \in \text{End}(\text{Dat}_{\mathbf{Types}})$$

$$<<<>>>: \mathbb{N}^+ \times \mathbb{N}^+ \times \text{Mor}_{\text{GPU}} \rightarrow \text{End}(\text{Dat}_{GPU})$$

⇒

```
kernel<<<1,1>>>();
```

cf. 3.2.3 Passing Parameters of Sanders and Kandrot (2010) [9]

Taking a look at `[add-passb.cu]`(<https://github.com/ernestyalumni/CompPhys/blob/master/CUDA-By-Example/add-passb.cu>), let’s work out the functor interpretation of `cudaMalloc`, `cudaMemcpy`.

In `main`, “declaring” a pointer:

```
int *dev_c
```

⇐

$$\text{pointers} \xrightarrow{*} \mathbf{Dat}_{\text{lvalues}} \xrightarrow{\text{typedef}} \mathbf{Types}$$

$$\text{dev_c} \vdash \xrightarrow{*} *dev_c \vdash \xrightarrow{\text{typedef}} \text{int}$$

We can also do, note, the `sizeof` function (which is a well-defined mapping, for once) on `ObjTypes`:

$$\text{pointers} \xrightarrow{*} \mathbf{Dat}_{\text{lvalues}} \xrightarrow{\text{typedef}} \mathbf{Types} \xrightarrow{\text{sizeof}} \mathbb{N}^+$$

$$\text{dev_c} \vdash \xrightarrow{*} *dev_c \vdash \xrightarrow{\text{typedef}} \text{int} \vdash \xrightarrow{\text{sizeof}} \text{sizeof(int)}$$

Consider what Sanders and Kandrot says about the pointer to the pointer that (you want to) holds the address of the newly allocated memory. [9] Consider this diagram:

$$\begin{aligned}
& \mathbf{pointers} \xrightarrow{*} \mathbf{pointers} \xrightarrow{*} \mathbf{Types} \\
& \mathbf{pointer} \xrightarrow{*} \mathbf{pointer} \xrightarrow{*} \text{void} \\
& \&\mathbf{dev_c} \xrightarrow{*} *(&\mathbf{dev_c}) \xrightarrow{*} (\text{void } *)(&\mathbf{dev_c})
\end{aligned}$$

I propose that what `cudaMalloc` does (actually) is the following:

$$\begin{aligned}
& \mathbf{Memory}_{\text{GPU}} \xrightarrow{\text{cudaMalloc}} \mathbf{pointers} \xrightarrow{*} \mathbf{pointers} \xrightarrow{*} \mathbf{Types} \\
& \quad \downarrow * \\
& \mathbf{pointers}_{\text{GPU}} \xrightarrow{*} \mathbf{Types} \\
(59) \quad & \text{Memory address}_{\text{GPU}} \xrightarrow{\text{cudaMalloc}} \&\mathbf{dev_c} \xrightarrow{*} *(&\mathbf{dev_c}) \xrightarrow{*} (\text{void } *)(&\mathbf{dev_c}) \\
& \quad \downarrow * \\
& \mathbf{dev_c} \xrightarrow{*} *\mathbf{dev_c}
\end{aligned}$$

`dev_c` is now a *device pointer*, available to kernel functions on the GPU.

Syntax-wise, we can relate this diagram to the corresponding function “usage”:

$$\mathbf{pointers} \times \mathbb{N}^+ \xrightarrow{\text{cudaMalloc}} \mathbf{cudaError_r}$$

$$((\text{void } *)(&\mathbf{dev_c}), (\text{sizeof}(\text{int}))) \xrightarrow{\text{cudaMalloc}} \mathbf{cudaSuccess} \text{ (for example)}$$

\implies

`cudaMalloc((void**)&dev_c, sizeof(int))`

For practice, consider now `cudaMemcpy` in the functor interpretation, and its definition as such:

`cudaMemcpy` is a “functor category”, s.t. we equip the functor `cudaMemcpy` with a collection of objects $\text{Obj}_{\text{cudaMemcpy}}$, s.t., for example, `cudaMemcpyDevicetoHost` $\in \text{Obj}_{\text{cudaMemcpy}}$, where

$$(\text{cudaMemcpy}(-, -, n_{\text{thread}}, \text{cudaMemcpyDevicetoHost}) : \mathbf{Memory}_{\text{GPU}} \rightarrow \mathbf{Memory}_{\text{CPU}}) \in \text{Hom}(\mathbf{Memory}_{\text{GPU}}, \mathbf{Memory}_{\text{CPU}})$$

where $\text{Obj}_{\mathbf{Memory}_{\text{GPU}}} \equiv$ collection of all possible memory (addresses) on GPU.

It should be noted that, syntax-wise, $\&c \in \text{Obj}_{\mathbf{Memory}_{\text{CPU}}}$ and $\&c$ belongs in the “first slot” of the arguments for `cudaMemcpy`, whereas `dev_c` $\in \mathbf{pointers}_{\text{GPU}}$ a *device pointer*, is “passed in” to the “second slot” of the arguments for `cudaMemcpy`.

24. THREADS, BLOCKS, GRIDS

cf. Chapter 5 Thread Cooperation, Section 5.2. Splitting Parallel Blocks of Sanders and Kandrot (2010) [9].

Consider first a 1-dimensional block.

- `threadIdx.x` $\Leftarrow M_x \equiv$ number of threads per block in x -direction. Let $j_x = 0 \dots M_x - 1$ be the index for the thread. Note that $1 \leq M_x \leq M_x^{\max}$, e.g. $M_x^{\max} = 1024$, max. threads per block
- `blockIdx.x` $\Leftarrow N_x \equiv$ number of blocks in x -direction. Let $i_x = 0 \dots N_x - 1$
- `blockDim` stores number of threads along each dimension of the block M_x .

Then if we were to “linearize” or “flatten” in this x -direction,

$$k = j_x + i_x M_x$$

where k is the k th thread. $k = 0 \dots N_x M_x - 1$.

Take a look at [heatttexture1.cu](#) which uses the GPU texture memory. Look at how `threadIdx/blockIdx` is mapped to pixel position.

As an exercise, let’s again rewrite the code in mathematical notation:

- `threadIdx.x` $\Leftarrow j_x, 0 \leq j_x \leq M_x - 1$
- `blockIdx.x` $\Leftarrow i_x, 0 \leq i_x \leq N_x - 1$
- `blockDim.x` $\Leftarrow M_x$, number of threads along each dimension (here dimension x) of a block, $1 \leq M_x \leq M_x^{\max} = 1024$
- `gridDim.x` $\Leftarrow N_x, 1 \leq N_x$

resulting in

- $k_x = j_x + i_x M_x \implies$

```
int x = threadIdx.x + blockIdx.x * blockDim.x ;
```
- $k_y = j_y + i_y M_y \implies$

```
int y = threadIdx.y + blockIdx.y * blockDim.y ;
```

and so for a “flattened” thread index $J \in \mathbb{N}$,

$$J = k_x + N_x \cdot M_x \cdot k_y$$

\implies

$$\text{offset} = x + y * \text{blockDim.x} * \text{gridDim.x} ;$$

Suppose vector is of length N . So we *need* N parallel threads to launch, in total.

e.g. if $M_x = 128$ threads per block, $N/128 = N/M_x$ blocks to get our total of N threads running.

Wrinkle: integer division! e.g. if $N = 127$, $\frac{N}{128} = 0$.

Solution: consider $\frac{N+127}{128}$ blocks. If $N = l \cdot 128 + r, l \in \mathbb{N}, r = 0 \dots 127$.

$$\begin{aligned}
\frac{N+127}{128} &= \frac{l \cdot 128 + r + 127}{128} = \frac{(l+1)128 + r - 1}{128} = \\
&= l + 1 + \frac{r-1}{128} = \begin{cases} l & \text{if } r = 0 \\ l+1 & \text{if } r = 1 \dots 127 \end{cases}
\end{aligned}$$

$$\begin{aligned}
\frac{N+(M_x-1)}{M_x} &= \frac{l \cdot M_x + r + M_x - 1}{M_x} = \frac{(l+1)M_x + r - 1}{M_x} = \\
&= l + 1 + \frac{r-1}{M_x} = \begin{cases} l & \text{if } r = 0 \\ l+1 & \text{if } r = 1 \dots M_x - 1 \end{cases}
\end{aligned}$$

So $\frac{N+(M_x-1)}{M_x}$ is the smallest multiple of M_x greater than or equal to N , so $\frac{N+(M_x-1)}{M_x}$ **blocks are needed or more than needed to run a total of N threads.**

Problem: Max. grid dim. in 1-direction is 65535, $\equiv N_i^{\max}$.

So $\frac{N+(M_x-1)}{M_x} = N_i^{\max} \implies N = N_i^{\max} M_x - (M_x - 1) \leq N_i^{\max} M_x$. i.e. number of threads N is limited by $N_i^{\max} M_x$.

Solution.

- number of threads per block in x -direction $\equiv M_x \implies \mathbf{blockDim.x}$

- number of blocks in grid $\equiv N_x \implies \text{gridDim.x}$
- $N_x M_x$ total number of threads in x -direction. Increment by $N_x M_x$. So next scheduled execution by GPU at the $k = N_x M_x$ thread.

Sanders and Kandrot (2010) [9] made an important note, on pp. 176-177 Ch. 9 Atomics of Section 9.4 Computing Histograms, an important *rule of thumb* on the number of blocks.

First, consider N^{threads} total threads. The extremes are either N^{threads} threads on a single block, or N^{threads} blocks, each with a single thread.

Sanders and Kandrot gave this tip:

number of blocks, i.e. $\text{gridDim.x} \leftarrow N_x \sim 2 \times$ number of GPU multiprocessors, i.e. twice the number of GPU multiprocessors. In the case of my GeForce GTX 980 Ti, it has 22 Multiprocessors.

24.1. global thread Indexing: 1-dim., 2-dim., 3-dim. Consider the problem of *global thread indexing*. This was asked on the NVIDIA Developer’s board (cf. [Calculate GLOBAL thread Id](#)). Also, there exists a “cheatsheet” (cf. [CUDA Thread Indexing Cheatsheet](#)). Let’s consider a (mathematical) generalization.

Consider again (cf. [24](#)) the following notation:

- $\text{threadIdx.x} \leftarrow i_x, 0 \leq i_x \leq M_x - 1$, $i_x \in \{0 \dots M_x - 1\} \equiv I_x$, of “cardinal length/size” of $|I_x| = M_x$
- $\text{blockIdx.x} \leftarrow j_x, 0 \leq j_x \leq N_x - 1$, $j_x \in \{0 \dots N_x - 1\} \equiv J_x$, of “cardinal length/size” of $|J_x| = N_x$
- $\text{blockDim.x} \leftarrow M_x$
- $\text{gridDim.x} \leftarrow N_x$

Now consider formulating the various cases, of a grid of dimensions from 1 to 3, and blocks of dimensions from 1 to 3 (for a total of 9 different cases) mathematically, as the [CUDA Thread Indexing Cheatsheet](#) did, similarly:

- *1-dim. grid of 1-dim. blocks.* Consider $J_x \times I_x$. For $j_x \in J_x, i_x \in I_x$, then $k_x = j_x M_x + i_x, k_x \in \{0 \dots N_x M_x - 1\} \equiv K_x$. The condition that k_x be a valid global thread index is that K_x has equal cardinality or size as $J_x \times I_x$, i.e.

$$|J_x \times I_x| = |K_x|$$

(this must be true). This can be checked by checking the most extreme, maximal, case of $j_x = N_x - 1, i_x = M_x - 1$:

$$k_x = j_x M_x + i_x = (N_x - 1)M_x + M_x - 1 = N_x M_x - 1$$

and so k_x ranges from 0 to $N_x M_x - 1$, and so $|K_x| = N_x M_x$.

Summarizing all of this in the following manner:

$$J_x \times I_x \longrightarrow K_x \equiv K^{N_x M_x} = \{0 \dots N_x M_x - 1\}$$

$$(j_x, i_x) \longmapsto k_x = j_x M_x + i_x$$

For the other cases, this generalization we’ve just done is implied.

- *1-dim. grid of 2-dim. blocks*

$$J_x \times (I_x \times I_y) \longrightarrow K^{N_x M_x M_y} \equiv \{0 \dots N_x M_x M_y - 1\}$$

$$(j_x, (i_x, i_y)) \longmapsto k = j_x M_x M_y + (i_x + i_y M_x) = j_x |I_x \times I_y| + (i_x + i_y M_x) \in \{0 \dots N_x M_x M_y - 1\}$$

The “most extreme, maximal” case that can be checked to check that the “cardinal size” of $K^{N_x M_x M_y}$ is equal to $J_x \times (I_x \times I_y)$ is the following, and for the other cases, will be implied (unless explicitly written or checked out):

$$k = j_x M_x M_y + (i_x + i_y M_x) = (N_x - 1)M_x M_y + ((M_x - 1) + (M_y - 1)M_x) = (N_x M_x M_y - 1)$$

The thing to notice is this emerging, general pattern, what could be called a “global view” of understanding the threads and blocks model of the GPU (cf. [njuffa’s answer](#)):

total number of threads = block index (Id) · total number of threads per block + thread index on the block

But as we’ll see, that’s not the only way of “flattening” the index, or transforming into a 1-dimensional index.

- *1-dim. grid of 3-dim. blocks*

$$J_x \times (I_x \times I_y \times I_z) \longrightarrow K^{N_x M_x M_y M_z}$$

$$(j_x, (i_x, i_y, i_z)) \longmapsto k = j_x (M_x M_y M_z) + (i_x + i_y M_x + i_z M_x M_y) \in \{0 \dots N_x M_x M_y M_z - 1\}$$

- *2-dim. grid of 1-dim. blocks*

$$(J_x \times J_y) \times I_x \longrightarrow L^{N_x N_y} \times I_x \longrightarrow K^{N_x N_y M_x}$$

$$((j_x, j_y), i_x) \longmapsto ((j_x + N_x j_y), i_x) \longmapsto k = (j_x + N_x j_y) \cdot M_x + i_x \in \{0 \dots N_x N_y M_x - 1\}$$

- *2-dim. grid of 2-dim. blocks*

$$(J_x \times J_y) \times (I_x, I_y) \longrightarrow L^{N_x N_y} \times (I_x, I_y) \longrightarrow K^{N_x N_y M_x}$$

$$((j_x, j_y), (i_x, i_y)) \longmapsto ((j_x + N_x j_y), (i_x, i_y)) \longmapsto k = (j_x + N_x j_y) \cdot M_x M_y + i_x + M_x i_y$$

But this *isn’t the only way of obtaining* a “flattened index.” Exploit the commutativity and associativity of the Cartesian product:

$$J_x \times J_y \times I_x \times I_y = (J_x \times I_x) \times (J_y \times I_y) \longrightarrow K^{N_x M_x} \times K^{N_y M_y} \longrightarrow K^{N_x N_y M_x M_y}$$

$$((j_x, j_y, i_x, i_y) = ((j_x, i_x), (j_y, i_y)) \longmapsto (i_x + M_x j_x, i_y + M_y j_y) \equiv (k_x, k_y) \longmapsto k = k_x + k_y N_x M_x = (i_x + M_x j_x) + (i_y + M_y j_y) M_x N_x$$

Indeed, checking the “maximal, extreme” case,

$$k = k_x + k_y N_x M_x = M_x N_x - 1 + (M_y N_y - 1)(N_x M_x) = M_y M_y N_x M_x - 1$$

and so k ranges from 0 to $M_y M_y N_x M_x - 1$.

- *3-dim. grid of 3-dim. blocks*

$$(J_x \times J_y \times J_z) \times (I_x \times I_y \times I_z) = \longrightarrow K^{N_x M_x} \times K^{N_y M_y} \times K^{N_z M_z} \longrightarrow K^{N_x N_y N_z M_x M_y M_z} \\ = (J_x \times I_x) \times (J_y \times I_y) \times (J_z \times I_z)$$

$$((j_x, j_y, j_z), (i_x, i_y, i_z)) = (i_x + M_x j_x, i_y + M_y j_y, i_z + M_z j_z) \equiv (j_x, i_x), (j_y, i_y), (j_z, i_z) \longmapsto (i_x + M_x j_x, i_y + M_y j_y, i_z + M_z j_z) \equiv (k_x, k_y, k_z) \longmapsto k = k_x + k_y N_x M_x + k_z N_x M_x N_y M_y$$

Indeed, checking the “extreme, maximal” case for k :

$$k = k_x + k_y N_x M_x + k_z N_x M_x N_y M_y = (N_x M_x - 1) + (N_y M_y - 1)N_x M_x + (N_z M_z - 1)N_x M_x N_y M_y = N_x N_y N_z M_x M_y M_z - 1$$

24.2. (CUDA) Constant Memory. cf. Chapter 6 Constant Memory of Sanders and Kandrot (2010) [9]

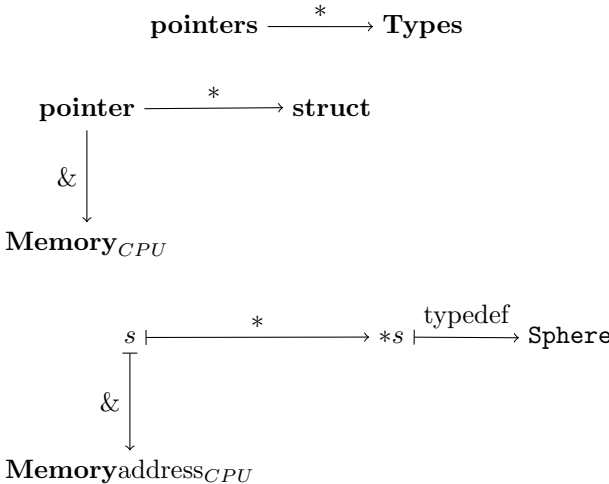
Refer to the ray tracing examples in Sanders and Kandrot (2010) [9], and specifically, here: [raytrace.cu](#), [rayconst.cu](#).

Without constant memory, then this had to be done:

- *definition* (in the code) - Consider **struct** as a subcategory of **Types** since **struct** itself is a category, equipped with objects and functions (i.e. methods, modules, etc.).
So for **struct**, $\text{Objstruct} \ni \text{Sphere.} \implies$

```
struct sphere { ... }
```

- Usage, “instantiation”, i.e. creating, or “making” it (the **struct**):



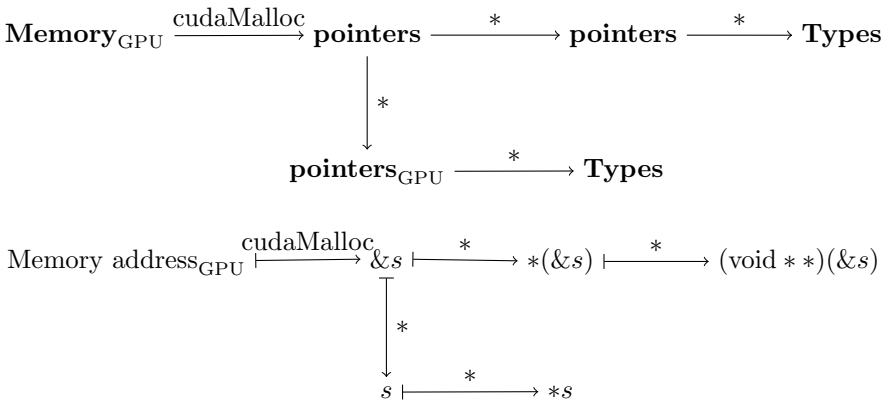
\implies

```
Sphere *s
```

Recalling Eq. 59, for SPHERES == 40 (i.e. for example, 40 spheres)

```
cudaMalloc((void **) &s, sizeof(Sphere)*SPHERES)
```

\Leftarrow



and syntax-wise,

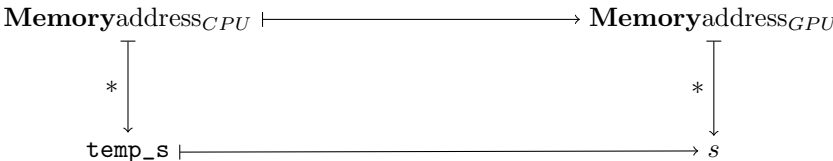
```
pointers  $\times \mathbb{N}^+ \xrightarrow{\text{cudaMalloc}}$  cudaError_r
```

```
((void **)(s), sizeof(Sphere) * SPHERES)  $\xrightarrow{\text{cudaMalloc}}$  cudaSuccess (for example)
```

Now consider

```
cudaMemcpy(s, temp_s, sizeof(Sphere) * SPHERES, cudaMemcpyHostToDevice)
```

```
cudaMemcpy(s, temps, sizeof(Sphere) * SPHERES, cudaMemcpyHostToDevice)  
MemoryCPU  $\longrightarrow$  MemoryGPU
```



The lesson then is this, in light of how long ray tracing takes with constant memory and without constant memory - **cudaMemcpy** between host to device, CPU to GPU, is a costly operation. Here, in this case, we’re copying from the host memory to memory on the GPU. It copies to a global memory on the GPU.

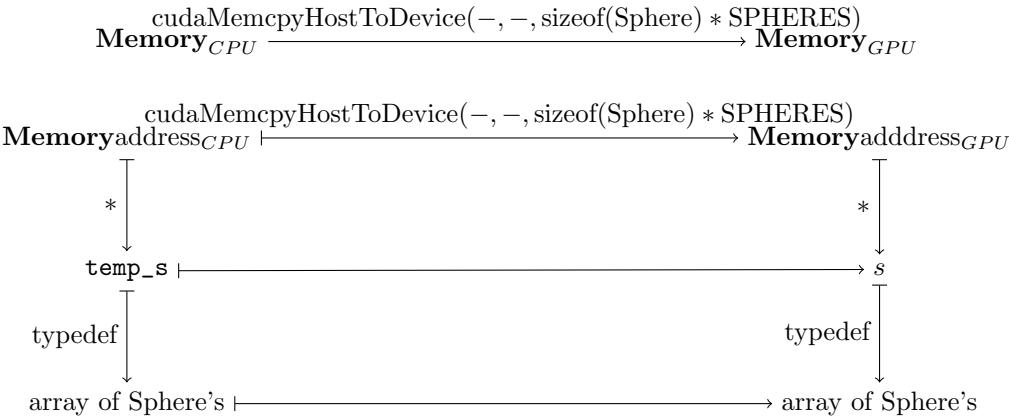
Now, using **constant memory**, we no longer need to do **cudaMalloc**, allocate memory on the GPU, for **s**, pointer to a **Sphere**. Instead, we have

```
--constant-- Sphere s[SPHERES];
```

In this particular case, we want it to have global scope.

Note, it is still on host memory.

Notice that



So notice that we have a bijection, and on one level, we can think of the bijection from **temp_s**, an array of Sphere’s to **s**, an array of Sphere’s. So notice that the types and memory size of **temp_s** and **s** must match.

And for this case, that’s all there is to *constant memory*. What’s going on involves the so-called *warp*, a collection of threads, “woven together” and get executed in lockstep. NVIDIA hardware broadcasts a single memory read to each half-warp. “If every thread in a half-warp requests data from the same address in constant memory, your GPU will generate only a single

read request and subsequently broadcast the data to every thread.” (cf. Sanders and Kandrot (2010) [9]). Furthermore, “the hardware can aggressively cache the constant data on the GPU.”

24.3. (CUDA) Texture Memory.

24.4. Do (smooth) manifolds admit a triangulation? Topics in Geometric Topology (18.937) [Piecewise Linear Topology \(Lecture 2\)](#)

Part 6. Computational Fluid Dynamics (CFD); Computational Methods

25. ON COMPUTATIONAL METHODS FOR AEROSPACE ENGINEERING, VIA DARMOFAL, SPRING 2005

Notes to follow along Darmofal (2005) [10]

25.1. On Lecture 1, Numerical Integration of Ordinary Differential Equations. For the 1-dim. case,

$$m_p \frac{du}{dt} = m_p g - D(u)$$

Recall the velocity vector field $u = u(t, x) \in \mathfrak{X}(\mathbb{R} \times \mathbb{R})$. This is *not* what we want in this case; we want for particles the tangent bundle.

$$D = D(u) = \frac{1}{2} \rho_g \pi a^2 u^2 C_D(\text{Re})$$

$$\text{Re} = \frac{2\rho_g u a}{\mu_g}$$

$$C_D = \frac{24}{\text{Re}} + \frac{6}{1 + \sqrt{\text{Re}}} + 0.4$$

Darmofal (2005) [10] then made a brief aside/note on linearization.

Consider perturbation method (linearization)

$$u(t) = u_0 + \tilde{u}(t)$$

e.g. constant (in time).

If $\frac{du}{dt} = f(u, t)$,

$$\frac{d\tilde{u}}{dt} = f(u_0 + \tilde{u}, t) = f(u_0, t) + \left. \frac{\partial f}{\partial u} \right|_{u_0, 0} \tilde{u} + \left. \frac{\partial f}{\partial t} \right|_{u_0, 0} t + \mathcal{O}(t^2, \tilde{u}t, \tilde{u}^2)$$

$$a = 0.01 \text{ m}, \rho_p = 917 \text{ kg/m}^3 \quad \rho_g = 0.9 \text{ kg/m}^3$$

$$m_p = \rho_p \frac{4}{3} \pi a^3 = 0.0038 \text{ kg}$$

$$\mu_g = 1.69 \times 10^{-5} \text{ kg/(m sec)}$$

$$g = 9.8 \text{ m/s}^2$$

In the 3-dim. case,

$$m_p \frac{d\mathbf{u}}{dt} = m_p g - D(u) \frac{\mathbf{u}}{|\mathbf{u}|}$$

Consider curve $x : \mathbb{R} \rightarrow N = \mathbb{R}$
 $x(t) \in \mathbb{R}$, $u(t) \equiv \frac{dx}{dt} \in \Gamma(TN) = \Gamma(T\mathbb{R})$

$$\frac{du}{dt} = g - \frac{D(u)}{m_p}$$

$$\frac{du}{dt} = (u(t + \Delta t) - u(t)) \frac{1}{\Delta t} + \mathcal{O}(\Delta t)$$

25.2. Multi-step methods generalized. This subsection corresponds to [Lecture 3: Convergence of Multi-Step Methods](#), but is a further generalization to the presented multi-step methods.

The problem to solve, the ODE to compute out, is

$$(60) \quad \frac{du}{dt}(t) = f(u(t), t)$$

Make the following ansatz:

$$(61) \quad \frac{du}{dt}(t) = \sum_{\nu=0}^N \frac{1}{h} C_\nu u(t - \nu h) = \sum_{\xi=1}^P \beta_\xi f(u(t - \xi h), t - \xi h)$$

Do the Taylor expansion:

$$\begin{aligned} \sum_{\nu=0}^N \frac{1}{h} C_\nu \left[u(t) + \left(\frac{du}{dt} \right)(t) \cdot (-\nu h) + \sum_{j=2}^n \frac{u^{(j)}(t)}{j!} (-\nu h)^j + \mathcal{O}(h^n) \right] &= \sum_{\xi=1}^P \beta_\xi \frac{du}{dt}(t - \xi h) = \\ &= \sum_{\xi=1}^P \beta_\xi \left[\frac{du}{dt} + \sum_{j=2}^n \frac{u^{(j)}(t)}{j!} (-\xi h)^j + \mathcal{O}(h^n) \right] \end{aligned}$$

25.3. Convection (Discretized). While I am following Lecture 7 of Darmofal (2005) [10], I will generalize to a “foliated, spatial” (smooth) manifold N , parametrized by time $t \in \mathbb{R}$, $\mathbb{R} \times N$, with $\dim N = n = 1, 2$ or 3 and to *CUDA* C/C++ parallel programming.

Consider n -form $m \in \Omega^N(\mathbb{R} \times N)$, $\dim N = n$. Then

$$(62) \quad \begin{aligned} \frac{d}{dt} m &= \frac{d}{dt} \int_V \rho \text{vol}^n = \int_V \mathcal{L}_{\frac{\partial}{\partial t} + \mathbf{u}} \rho \text{vol}^n = \int_V \frac{\partial \rho}{\partial t} \text{vol}^n + \mathbf{d}i_{\mathbf{u}} \rho \text{vol}^n = \int_V \left(\frac{\partial \rho}{\partial t} + \text{div}(\rho \mathbf{u}) \right) \text{vol}^n = \\ &= \int_V \frac{\partial \rho}{\partial t} \text{vol}^n + \int_{\partial V} \rho i_{\mathbf{u}} \text{vol}^n = \dot{m} \end{aligned}$$

where recall

$$\text{div} : \mathfrak{X}(\mathbb{R} \times N) \rightarrow C^\infty(\mathbb{R} \times N)$$

$$\text{div}(\rho \mathbf{u}) = \frac{1}{\sqrt{g}} \frac{\partial(\sqrt{g} u^i \rho)}{\partial x^i}$$

25.3.1. 1-dimensional case for Convection from mass (scalar) conservation. Consider Cell i , between $x_{i-\frac{1}{2}}$ and $x_{i+\frac{1}{2}}$, i.e. $[x_{i-\frac{1}{2}}, x_{i+\frac{1}{2}}] \subset \mathbb{R}$. In this case, Eq. [62](#), for mass conservation with sources, becomes

$$\int_V \frac{\partial \rho}{\partial t} \text{vol}^n + \int_{\partial V} \rho i_{\mathbf{u}} \text{vol}^n = \int_V \frac{\partial \rho}{\partial t} dx + \int_{\partial V} \rho u^i = \int_{x_L}^{x_R} \frac{\partial \rho}{\partial t} dx + (\rho(x_R)u(x_R) - \rho(x_L)u(x_L)) = \frac{d}{dt} \int_{x_L}^{x_R} \rho(x) dx$$

In the case of $\frac{d}{dt} m = 0$, on a single cell i ,

$$\int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \frac{\partial \rho}{\partial t} dx + \rho(x)u(x)|_{x_{i+\frac{1}{2}}} - \rho(x)u(x)|_{x_{i-\frac{1}{2}}} = 0$$

This is one of the first main approximations Darmofal (2005) [10] makes, in Eq. 7.10, Section 7.3 Finite Volume Method for Convection, for the *finite volume method*:

$$(63) \quad \overline{m}_i := \frac{1}{\Delta x_i} \int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \rho(x) dx$$

where $\Delta x_i \equiv x_{i+\frac{1}{2}} - x_{i-\frac{1}{2}}$.

And so

$$(64) \quad \Delta x_i \frac{\partial}{\partial t} \overline{m}_i + \rho(x)u(x)|_{x_{i+\frac{1}{2}}} - \rho(x)u(x)|_{x_{i-\frac{1}{2}}} = 0$$

We want to discretize this equation also in time.

Consider as first approximation,

$$(65) \quad \overline{m}(x, t) = \overline{m}_i(t) \quad \forall x_{i-\frac{1}{2}} < x < x_{i+\frac{1}{2}}$$

Consider then initial time t , time step Δt .

25.3.2. *1-dimensional “Upwind” Interpolation for Finite Volume.* This is the “major” approximation for the so-called “Upwind” interpolation approximation:

$$(66) \quad \rho(x_{i+\frac{1}{2}}, t + \Delta t) = \begin{cases} \overline{m}_i(t) & \text{if } u(x_{i+\frac{1}{2}}, t) > 0 \\ \overline{m}_{i+1}(t) & \text{if } u(x_{i+\frac{1}{2}}, t) < 0 \end{cases}$$

Then use the so-called “forward” time approximation for $\frac{d}{dt}\overline{m}_i(t)$:

$$\Delta x_i \frac{\overline{m}_i(t + \Delta t) - \overline{m}_i(t)}{\Delta t} + (\rho u)(t, x_{i+\frac{1}{2}}) - (\rho u)(t, x_{i-\frac{1}{2}}) = 0$$

Darmofal (2005) [10] didn’t make this explicit in Lecture 7, but in the approximation for $\rho(x_{i+\frac{1}{2}}, t + \Delta t)$, Eq. 66, it’s supposed that it’s valid at time t : $\rho(x_{i+\frac{1}{2}}, t) \approx \rho(x_{i+\frac{1}{2}}, t + \Delta t)$, since it’s the value of ρ for time moving forward from t (this is implied in Darmofal’s code `convect1d`

$$\rho(x_{i+\frac{1}{2}}, t)u(x_{i+\frac{1}{2}}, t) = \begin{cases} \overline{m}_i(t)u(x_{i+\frac{1}{2}}, t) & \text{if } u(x_{i+\frac{1}{2}}, t) > 0 \\ \overline{m}_{i+1}(t)u(x_{i+\frac{1}{2}}, t) & \text{if } u(x_{i+\frac{1}{2}}, t) < 0 \end{cases}$$

Then

$$(67) \quad \begin{aligned} & \frac{\Delta x_i}{\Delta t} (\overline{m}_i(t + \Delta t) - \overline{m}_i(t)) + \\ & + \begin{cases} \overline{m}_i(t)u(x_{i+\frac{1}{2}}, t) & \text{if } u(x_{i+\frac{1}{2}}, t) > 0 \\ \overline{m}_{i+1}(t)u(x_{i+\frac{1}{2}}, t) & \text{if } u(x_{i+\frac{1}{2}}, t) < 0 \end{cases} - \\ & - \begin{cases} \overline{m}_{i-1}(t)u(x_{i-\frac{1}{2}}, t) & \text{if } u(x_{i-\frac{1}{2}}, t) > 0 \\ \overline{m}_i(t)u(x_{i-\frac{1}{2}}, t) & \text{if } u(x_{i-\frac{1}{2}}, t) < 0 \end{cases} = \\ & = 0 \end{aligned}$$

A note on 1-dimensional gridding: Consider total length $L_0 \in \mathbb{R}^+$.

For N^{cells} total cells in x -direction. $i = 0 \dots N^{\text{cells}} - 1$.

$$\begin{aligned} x_{i-\frac{1}{2}} &= i\Delta x & i &= 0, 1 \dots N^{\text{cells}} - 1 \\ x_{i+\frac{1}{2}} &= (i+1)\Delta x & i &= 0, 1 \dots N^{\text{cells}} - 1 \\ x_i &= x_{i-\frac{1}{2}} + \frac{x_{i+\frac{1}{2}} - x_{i-\frac{1}{2}}}{2} = \frac{x_{i+\frac{1}{2}} + x_{i-\frac{1}{2}}}{2} = (2i+1)\frac{\Delta x}{2} & i &= 0, 1 \dots N^{\text{cells}} - 1 \end{aligned}$$

At this point, instead of what is essentially the so-called “Upwind Interpolation”, which Darmofal is doing in Lecture 7 of Darmofal (2005) [10], and on pp. 76, Chapter 4 Finite Volume Methods, Subsection 4.4.1 Upwind Interpolation (UDS) of Ferziger and Peric (2002) [11], which is essentially a zero-order approximation, let’s try to do better.

Consider the interval $[x_{i-\frac{1}{2}}, x_{i+\frac{1}{2}}] \subset \mathbb{R}$.

For the 1-dimensional case of (pure) convection,

$$\int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \frac{\partial \rho(t, x)}{\partial t} dx + \rho(t, x_{i+\frac{1}{2}})u(t, x_{i+\frac{1}{2}}) - \rho(t, x_{i-\frac{1}{2}})u(t, x_{i-\frac{1}{2}}) = \frac{d}{dt} \int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \rho(x) dx$$

Given $\rho(t, x_{i-\frac{1}{2}}), \rho(t, x_{i+\frac{1}{2}}) \in \mathbb{R}$, do (polynomial) interpolation:

$$\begin{aligned} \mathbb{R} \times \mathbb{R} &\xrightarrow{\text{interpolation}} \mathbb{R}[x] \equiv \mathcal{P}_{n=1}(\mathbb{R}) \\ \rho(t, x_{i-\frac{1}{2}}), \rho(t, x_{i+\frac{1}{2}}) &\mapsto \frac{(x - x_{i-\frac{1}{2}})\rho(t, x_{i+\frac{1}{2}}) - (x - x_{i+\frac{1}{2}})\rho(t, x_{i-\frac{1}{2}})}{h} = \rho_{n=1}(t, x) \end{aligned}$$

where $h \equiv x_{i+\frac{1}{2}} - x_{i-\frac{1}{2}}$ and $\mathcal{P}_{n=1}(\mathbb{R})$ is the set of all polynomials of order $n = 1$ over field \mathbb{R} (real numbers).

In general,

$$\begin{aligned} \mathbb{R} \times \mathbb{R} &\xrightarrow{\text{interpolation}} \mathbb{R}[x] \equiv \mathcal{P}_{n=1}(\mathbb{R}) \\ \rho(t, x_L), \rho(t, x_R) &\mapsto \frac{(x - x_L)\rho(t, x_R) - (x - x_R)\rho(t, x_L)}{(x_R - x_L)} = \rho_{n=1}(t, x) \end{aligned}$$

We interchange the operations of integration and partial derivative - I (correct me if I’m wrong) give two possible reasons why we can do this: the spatial manifold N is fixed in time t , and if the grid cell itself is fixed in time, then the partial derivative in time can be moved out of the integration limits.

So, interchanging $\int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} dx$ and $\frac{\partial}{\partial t}$:

$$\int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \frac{\partial \rho(t, x)}{\partial t} dx = \frac{\partial}{\partial t} \int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \rho(t, x) dx$$

So then

$$\implies \frac{\partial}{\partial t} \int_{x_{i-\frac{1}{2}}}^{x_{i+\frac{1}{2}}} \rho_{n=1}(t, x) = \frac{\partial}{\partial t} (\rho(t, x_{i+\frac{1}{2}}) + \rho(t, x_{i-\frac{1}{2}})) \frac{\Delta x}{2}$$

where $\Delta x = x_{i+\frac{1}{2}} - x_{i-\frac{1}{2}}$.

In general,

$$\frac{\partial}{\partial t} \int_{x_L}^{x_R} \rho_{n=1}(t, x) = \frac{\partial}{\partial t} (\rho(t, x_R) + \rho(t, x_L)) \frac{(x_R - x_L)}{2}$$

Then, discretizing,

$$(68) \quad \begin{aligned} \implies & \left[(\rho(t + \Delta t, x_{i+\frac{1}{2}}) + \rho(t + \Delta t, x_{i-\frac{1}{2}})) - (\rho(t, x_{i+\frac{1}{2}}) + \rho(t, x_{i-\frac{1}{2}})) \right] \frac{\Delta x}{2} \left(\frac{1}{\Delta t} \right) + \rho(t, x_{i+\frac{1}{2}})u(t, x_{i+\frac{1}{2}}) - \rho(t, x_{i-\frac{1}{2}})u(t, x_{i-\frac{1}{2}}) = \\ & = \dot{m}_{[x_{i-\frac{1}{2}}, x_{i+\frac{1}{2}}]}(t) \end{aligned}$$

To obtain $\rho(t, x_{i-\frac{1}{2}})$, consider

$$\frac{\partial \rho}{\partial t} + \text{div}(\rho u) = \frac{d\rho}{dt} = 0$$

which is valid at every point on N .

Consider for $\dim N = 1$,

$$\frac{\partial \rho}{\partial t}(t, x) + \frac{\partial(\rho u)}{\partial x}(t, x)$$

Now, we want $x = x_{i-\frac{1}{2}}$.

Consider

$$\frac{\partial \rho(t, x_{i-\frac{1}{2}})}{\partial t} \approx \frac{\rho(t + \Delta t, x_{i-\frac{1}{2}}) - \rho(t, x_{i-\frac{1}{2}})}{\Delta t}$$

Next, consider the (polynomial) interpolation for the $\frac{\partial(\rho u)}{\partial x}(t, x)$ term:

$$\begin{aligned} \mathbb{R} \times \mathbb{R} \times \mathbb{R} &\xrightarrow{\text{interpolate}} \mathbb{R}[x] \equiv \mathcal{P}_{n=2}(\mathbb{R}) \\ \rho(t, x_{i-\frac{3}{2}})u(t, x_{i-\frac{3}{2}}), \rho(t, x_{i-\frac{1}{2}})u(t, x_{i-\frac{1}{2}}), \rho(t, x_{i+\frac{1}{2}})u(t, x_{i+\frac{1}{2}}) &\xrightarrow{\text{interpolate}} (\rho u)_{n=2}(t, x) \end{aligned}$$

Thus, we can calculate, by plugging into,

$$\frac{\partial(\rho u)_{n=2}(t, x_{i-\frac{1}{2}})}{\partial x}$$

In general, for

$$\mathbb{R} \times \mathbb{R} \times \mathbb{R} \xrightarrow{\text{interpolate}} \mathbb{R}[x] \equiv \mathcal{P}_{n=2}(\mathbb{R})$$

$$\rho(t, x_{LL})u(t, x_{LL}), \rho(t, x_L)u(t, x_L), \rho(t, x_R)u(t, x_R) \xrightarrow{\text{interpolate}} (\rho u)_{n=2}(t, x)$$

we have

$$\frac{\partial(\rho u)_{n=2}(t, x_L)}{\partial x} = \frac{1}{(x_L - x_{LL})(x_L - x_R)(x_{LL} - x_R)}.$$

$$\cdot \left((x_L - x_{LL})^2 (\rho u)(x_R) + (x_L - x_{LL})(x_{LL} - x_R)(\rho u)(x_L) - (x_L - x_R)^2 (\rho u)(x_{LL}) + (x_L - x_R)(x_{LL} - x_R)(\rho u)(x_L) \right)$$

Thus,

$$(69) \quad \begin{aligned} & \rho(t + \Delta t, x_{i-\frac{1}{2}}) - \rho(t, x_{i-\frac{1}{2}}) + \frac{\partial(\rho u)_{n=2}}{\partial x}(t, x_{i-\frac{1}{2}})\Delta t = 0 \text{ or} \\ \implies & \rho(t + \Delta t, x_{i-\frac{1}{2}}) = \rho(t, x_{i-\frac{1}{2}}) - \frac{\partial(\rho u)_{n=2}}{\partial x}(t, x_{i-\frac{1}{2}})\Delta t \end{aligned}$$

Now a note on the 1-dimensional grid, “gridding”: for cell $i = 0, \dots, N^{\text{cell}} - 1$, N^{cell} cells total in the x -direction, then

$$\begin{aligned} x_{i-\frac{1}{2}} &= ih \\ x_{i-\frac{1}{2}} &= (i+1)h \end{aligned}$$

and so $x_{i+\frac{1}{2}} - x_{i-\frac{1}{2}} = h$, meaning the cell width or cell size is h .

Thus, in summary,

$$(70) \quad \begin{aligned} \rho(t + \Delta t, x_{i-\frac{1}{2}}) &= \rho(t, x_{i-\frac{1}{2}}) - (\rho(t, x_{i+\frac{1}{2}})u(t, x_{i+\frac{1}{2}}) - \rho(t, x_{i-\frac{3}{2}})u(t, x_{i-\frac{3}{2}})) \left(\frac{1}{2h} \right) \Delta t \\ \left[(\rho(t + \Delta t, x_{i+\frac{1}{2}}) + \rho(t + \Delta t, x_{i-\frac{1}{2}})) - (\rho(t, x_{i+\frac{1}{2}}) + \rho(t, x_{i-\frac{1}{2}})) \right] \frac{h}{2} \left(\frac{1}{\Delta t} \right) &+ \rho(t, x_{i+\frac{1}{2}})u(t, x_{i+\frac{1}{2}}) - \rho(t, x_{i-\frac{1}{2}})u(t, x_{i-\frac{1}{2}}) = \\ &= \dot{m}_{[x_{i-\frac{1}{2}}, x_{i+\frac{1}{2}}]}(t) \end{aligned}$$

If one was to include Newtonian gravity, consider this general expression for the time derivative of the momentum flux Π :

$$(71) \quad \begin{aligned} \Pi &= \int_{B(t)} \rho u^i \text{vol}^n \otimes e_i \\ \dot{\Pi} &= \int_{B(t)} \frac{\partial(\rho u^i)}{\partial t} \text{vol}^n \otimes e_i + \int_{B(t)} d(\rho u^i i_u \text{vol}^n) \otimes e_i = \int_{B(t)} \frac{\partial(\rho u^i)}{\partial t} \text{vol}^n \otimes e_i + \int_{\partial B(t)} \rho u^i i_u \text{vol}^n \otimes e_i \end{aligned}$$

In 1-dim.,

$$\dot{\Pi} = \int_{B(t)} \frac{\partial(\rho u)}{\partial t} dx + \int_{\partial B} \rho u^2 = \int_B \frac{GM dm}{r^2} = GM \int_B \frac{\rho \text{vol}^n}{r^2} = GM \int_B \frac{\rho dx}{(R-x)^2}$$

Considering a first-order polynomial interpolation for $\rho, \rho_{n=1}$,

$$\frac{\partial}{\partial t}((\rho u)(t, x_{i+\frac{1}{2}}) + (\rho u)(t, x_{i-\frac{1}{2}})) \frac{h}{2} + \rho u^2(t, x_{i+\frac{1}{2}}) - \rho u^2(t, x_{i-\frac{1}{2}}) = GM \int \frac{\rho_{n=2} dx}{(R-x)^2}$$

Note that we need another equation, at $x = x_{i-\frac{1}{2}}$, similar to above:

$$\frac{\partial(\rho u)}{\partial t} + \frac{\partial(\rho u^2)}{\partial x} = \frac{GM \rho}{(R-x)^2}$$

$$\implies \rho u(t + \Delta t, x_{i-\frac{1}{2}}) - \rho u(t, x_{i-\frac{1}{2}}) + (\rho u^2(t, x_{i+\frac{1}{2}}) - \rho u^2(t, x_{i-\frac{3}{2}})) \left(\frac{1}{2h} \right) \Delta t = \Delta t \int GM \frac{\rho dx}{(R-x)^2}$$

As a recap, the 1-dimensional setup is as follows:

$$\mathbb{R} \times N = \mathbb{R} \times \mathbb{R} \xrightarrow{\text{discretization}} \mathbb{Z} \times \mathbb{Z}$$

$$(t, x) \xrightarrow{\text{discretization}} (t_0 + (\Delta t)j, x_{i-\frac{1}{2}} = ih), \quad i, j \in \mathbb{Z}$$

Initial conditions for $\rho \in C^\infty(\mathbb{R} \times \mathbb{R})$: $\rho(t_0, x) \in C^\infty(\mathbb{R} \times \mathbb{R})$.

Choices for $u \in \mathfrak{X}(\mathbb{R} \times \mathbb{R})$:

- $u(t, x) = u(x)$ (i.e. time-independent velocity vector field)
- $u(t, x)$ determined by Newtonian gravity (that’s an external force on the fluid)

25.3.3. *Note on 1-dimensional gridding.* For, $[0, 1] \subset \mathbb{R}$

N cells,

Then $1/N = \Delta x$. Then consider

$$x_j = j\Delta x \quad j = 0, 1, \dots, N$$

25.4. **2-dim. and 3-dim. “Upwind” interpolation for Finite Volume.** I build on Lecture 7 of Darmofal (2005) [10].

Consider a rectangular grid.

Consider cell C_{ij}^2 , $i = 0 \dots N_x - 1$, $j = 0 \dots N_y - 1$. Then there’s $N_x \cdot N_y$ total cells, N_x cells in x -direction .

N_y cells in y -direction

There are 2 possibilities: rectangles of all the same size, with width l^x and length l^y each, or each rectangle for each cell C_{ij}^2 is different, of dimensions $l_i^x \times l_j^y$.

Consider cells centered at $x_{2i+1} = l^x \frac{(2i+1)}{2} = \sum_{k=0}^{i-1} l_k^x + \frac{l_i^2}{2}$.

On the “left” sides, $x_{2i} = l^x i = \sum_{k=0}^{i-1} l_k^x$

“right” sides, $x_{2(i+1)} = l^x(i+1) = \sum_{k=0}^i l_k^x$.

So cells are centered at

$$(x_{2i+1}, y_{2j+1}) = (l^x \frac{(2i+1)}{2}, l^y \frac{(2j+1)}{2}) = \left(\sum_{k=0}^{i-1} l_k^x + \frac{l_i^x}{2}, \sum_{k=0}^{j-1} l_k^y + \frac{l_j^y}{2} \right)$$

So this cell C_{ij}^2 , a 2-(cubic) simplex has 4 1-(cubic) simplices (edges): so 1-(cubic) simplices $\{C_{i\pm 1, j}^1, C_{i, j\pm 1}^1\}$

The center of these simplices are the following:

$$x_{C_{i+1, j}^1} = (x_{2i+1+1}, y_{2j+1}) = (l^x(i+1), l^y \frac{(2j+1)}{2}) = \left(\sum_{k=0}^i l_k^x, \sum_{k=0}^{j-1} l_k^y + \frac{l_j^y}{2} \right) \text{ so then}$$

$$x_{C_{i\pm 1, j}^1} = (x_{2i+1\pm 1}, y_{2j+1}) = (l^x \left(\frac{2i+1\pm 1}{2} \right), l^y \frac{(2j+1)}{2}) = \left(\sum_{k=0}^{\frac{2i-1\pm 1}{2}} l_k^x, \sum_{k=0}^{j-1} l_k^y + \frac{l_j^y}{2} \right)$$

$$x_{C_{i, j\pm 1}^1} = (x_{2i+1}, y_{2j+1\pm 1}) = (l^x \left(\frac{2i+1}{2} \right), l^y \frac{(2j+1\pm 1)}{2}) = \left(\sum_{k=0}^{i-1} l_k^x + \frac{l_i^x}{2}, \sum_{k=0}^{\frac{2j-1\pm 1}{2}} l_k^y \right)$$

We want the flux. So for

$$\bar{\rho}_{ij} := \frac{1}{l_i^x l_j^y} \int_{C_{ij}^2} \rho \text{vol}^2$$

then the flux through 1-(cubic) simplices (faces), $\int \rho i_{\mathbf{u}} \text{vol}^2$,

$$\int_{C_{i+1,j}^1} \rho i_{\mathbf{u}} \text{vol}^2 = \begin{cases} l_j^y \bar{\rho}_{ij} u^x(x_{C_{i+1,j}^1}) & \text{if } u^x(x_{C_{i+1,j}^1}) > 0 \\ l_j^y \bar{\rho}_{i+1,j} u^x(x_{C_{i+1,j}^1}) & \text{if } u^x(x_{C_{i+1,j}^1}) < 0 \end{cases}$$

$$\int_{C_{i-1,j}^1} \rho i_{\mathbf{u}} \text{vol}^2 = \begin{cases} -l_j^y \bar{\rho}_{i-1,j} u^x(x_{C_{i-1,j}^1}) & \text{if } u^x(x_{C_{i-1,j}^1}) > 0 \\ -l_j^y \bar{\rho}_{i,j} u^x(x_{C_{i-1,j}^1}) & \text{if } u^x(x_{C_{i-1,j}^1}) < 0 \end{cases}$$

Likewise,

$$\int_{C_{i,j+1}^1} \rho i_{\mathbf{u}} \text{vol}^2 = \begin{cases} l_i^x \bar{\rho}_{ij} u^y(x_{C_{i,j+1}^1}) & \text{if } u^y(x_{C_{i,j+1}^1}) > 0 \\ l_i^x \bar{\rho}_{i,j+1} u^y(x_{C_{i,j+1}^1}) & \text{if } u^y(x_{C_{i,j+1}^1}) < 0 \end{cases}$$

and so on.

25.4.1. *3-dim. “Upwind” interpolation for finite volume.* For a rectangular prism (cubic), for cell C_{ijk}^3 , $i = 0 \dots N_x - 1$, $j = 0 \dots N_y - 1$, $k = 0 \dots N_z - 1$, $N_x \cdot N_y \cdot N_z$ total cells. Cells centered at

$$(x_{2i+1}, y_{2j+1}, z_{2k+1}) = (l^x \frac{(2i+1)}{2}, l^y \frac{(2j+1)}{2}, l^z \frac{(2k+1)}{2}) = \left(\sum_{l=0}^{i-1} l_l^x + \frac{l_i^x}{2}, \sum_{l=0}^{j-1} l_l^y + \frac{l_j^y}{2}, \sum_{l=0}^{k-1} l_l^z + \frac{l_k^z}{2} \right)$$

For the 3-(cubic) simplex, C_{ijk}^3 , it has 6 2-(cubic) simplices (faces). So for C_{ijk}^3 , consider $\{C_{i\pm 1,jk}^2, C_{ij\pm 1,k}^2, C_{ijk\pm 1}^2\}$. The center of these faces, such as for $C_{i\pm 1,jk}^2$, $x_{C_{i\pm 1,jk}^2}$, for instance,

$$x_{C_{i\pm 1,jk}^2} = (x_{2i+1\pm 1}, y_{2j+1}, z_{2k+1}) = (l^x \left(\frac{2i+1\pm 1}{2} \right), l^y \frac{(2j+1)}{2}, l^z \frac{(2k+1)}{2}) = \left(\sum_{l=0}^{\frac{2i-1\pm 1}{2}} l_l^x, \sum_{l=0}^{j-1} l_l^y + \frac{l_j^y}{2}, \sum_{l=0}^{k-1} l_l^z + \frac{l_k^z}{2} \right)$$

We want the flux. So for

$$\bar{\rho}_{ijk} := \frac{1}{l_i^x l_j^y l_k^z} \int_{C_{ijk}^3} \rho \text{vol}^3$$

then the flux through 2-(cubic) simplices (faces), $\int \rho i_{\mathbf{u}} \text{vol}^3$,

$$\int_{C_{i+1,jk}^2} \rho i_{\mathbf{u}} \text{vol}^3 = \begin{cases} l_j^y l_k^z \bar{\rho}_{ijk} u^x(x_{C_{i+1,jk}^2}) & \text{if } u^x(x_{C_{i+1,jk}^2}) > 0 \\ l_j^y l_k^z \bar{\rho}_{i+1,jk} u^x(x_{C_{i+1,jk}^2}) & \text{if } u^x(x_{C_{i+1,jk}^2}) < 0 \end{cases}$$

and so on.

To reiterate the so-called “upwind” interpolation method, in generality, recall that we are taking this equation:

$$\int_{C_{ij}^n} \frac{\partial \rho}{\partial t} \text{vol}^n + \int_{\partial C_{ij}^n} \rho u_{\mathbf{u}} \text{vol}^n = \dot{M}_{ij}$$

and discretizing it to obtain

$$\frac{\partial}{\partial t} \bar{\rho}_{ij} |\text{vol}^n| + \int_{\partial C_{ij}^n} \rho i_{\mathbf{u}} \text{vol}^n = \dot{M}_{ij}$$

$$\implies \frac{\partial}{\partial t} \bar{\rho}_{ij} = \frac{-1}{|\text{vol}^n|} \int_{\partial C_{ij}^n} \rho i_{\mathbf{u}} \text{vol}^n + \frac{1}{|\text{vol}^n|} \dot{M}_{ij}$$

26. FINITE DIFFERENCE

References/Links that I used:

- [Chapter 6 The finite difference method](#), by Pascal Frey
- [Numerical Methods for Partial Differential Equations](#) by Volker John
- [Wikipedia “Finite Difference”](#). Wikipedia has a section on Difference operators which appears powerful and general, but I haven’t understood how to apply it. In fact, see my jupyter notebook on the [CompPhys](#) github, [finitediff.ipynb](#) on how to calculate the coefficients in arbitrary (differential) order, and (error) order (of precision, error, i.e. $\mathcal{O}(h^p)$) for finite differences, approximations of derivatives.

From [finitediff.ipynb](#), I derived this formula

$$f'(x) = \frac{1}{h} \sum_{\nu=1}^3 C_{\nu} \cdot (f(x + \nu h) - f(x - \nu h)) + \mathcal{O}(h^7) \text{ for}$$

$$(72) \quad \begin{aligned} C_1 &= \frac{3}{4} \\ C_2 &= \frac{-3}{20} \\ C_3 &= \frac{1}{60} \end{aligned}$$

26.1. **Finite Difference with Shared Memory (CUDA C/C++)**. References/Links that I used:

- [Finite Difference Methods in CUDA C++, Part 2](#), by Dr. Mark Harris
- [GPU Computing with CUDA Lecture 3 - Efficient Shared Memory Use](#), Christopher Cooper of Boston University, August, 2011. UTFSM, Valparaíso, Chile.

cf. [Finite Difference Methods in CUDA C++, Part 2](#), by Dr. Mark Harris

In x -derivative, $\frac{\partial f}{\partial x}$, \forall thread block, $(j_x, j_y) \in \{0 \dots N_x - 1\} \times \{0 \dots N_y - 1\}$. $m_x \times s_{\text{Pencils}}$ elements \in tile e.g. $64 \times s_{\text{Pencils}}$.

In y -derivative, $\frac{\partial f}{\partial y}$, (x, y) -tile of $s_{\text{Pencils}} \times 64 = s_{\text{Pencils}} \times m_y$.

Likewise, $\frac{\partial f}{\partial z} \rightarrow (x, z)$ -tile of $s_{\text{Pencils}} \times 64 = s_{\text{Pencils}} \times m_z$.

Consider for the y derivative, the code for `__global__ void derivative_y(*f, *d_f)` ([finitediff.cu](#)):

```
int i <- i = j_x M_x + i_x ∈ {0...N_x M_x - 1} (since j_x M_x + i_x = (M_x - 1) + (N_x - 1) M_x, i.e. the “maximal” case)
int j <- j = i_y ∈ {0...M_y - 1}
int k <- k = j_y ∈ {0...N_y - 1}
int si { <- si = i_x ∈ {0...M_x - 1} }
int sj <- sj = j + 4 ∈ {4...M_y + 3}. Notice that r = 4. Then generalize to s_j = j + r ∈ {r, ..., M_y + r - 1}
int globalIdx <- l = k m_x m_y + j m_x + i ∈ {0, ..., (N_y - 1) m_x m_y + (M_y - 1) m_x + N_x M_x - 1} since
```

$$(N_y - 1) m_x m_y + (M_y - 1) m_x + N_x M_x - 1$$

`s_f[sj][si] <- s_f[sj][si] ≡ (s_f)_{s_j, s_i} = f(l), $l \in \{0 \dots (N_y - 1) m_x m_y + (M_y - 1) m_x + N_x M_x - 1\}$ with`

$$s_f \in \text{Mat}_{\mathbb{R}}(M_y + r, M_x)$$

If $j < 4$, $j < r$, $j = s_j - r \in \{0 \dots r - 1\}$ and so

$$(s_f)_{(s_j - r), s_i} = (s_f)_{s_j + m_y - 1 - r, s_i}$$

$$\iff$$

$$\{0, \dots, r - 1\} \times \{0 \dots M_x - 1\} \leftarrow \{m_y - 1, \dots, M_y + m_y - 2\} \times \{0 \dots M_x - 1\}$$

Then, the actual approximation method:

$$\frac{\partial f}{\partial y}(l) = \frac{\partial f}{\partial y}(i, j, k) = \sum_{\nu=1}^r c_{\nu} ((s_f)_{s_j + \nu, s_i} - (s_f)_{s_j - \nu, s_i})$$

The shared memory tile here is

```
__shared__ float s_f[m_y+8][sPencils]  $\Leftarrow s_f \in \text{Mat}_{\mathbb{R}}(m_y + 2r, s_{\text{Pencil}})$ 
```

By using the shared memory tile, each element from global memory is read only once. (cf. [Finite Difference Methods in CUDA C++, Part 2](#), by Dr. Mark Harris)

Consider expanding the number of pencils in the shared memory tile, e.g. 32 pencils.

Harris says that “with a 1-to-1 mapping of threads to elements where the derivative is calculated, a thread block of 2048 threads would be required.” Consider then letting each thread calculate the derivative for multiple points.

So Harris uses a thread block of $32 \times 8 \times 1 = 256$ threads per block, and have each thread calculate the derivative at 8 points, as opposed to a thread block of $4 * 64 * 1 = 256$ thread block, with each thread calculate the derivative at only 1 point.

Perfect coalescing is then regained.

[GPU Computing with CUDA Lecture 3 - Efficient Shared Memory Use](#), Christopher Cooper

26.2. **Note on finite-difference methods on the shared memory of the device GPU, in particular, the pencil method, that attempts to improve upon the double loading of boundary “halo” cells (of the grid).** cf. **Finite**

Difference Methods in CUDA C++, Part 1, by Dr. Mark Harris

Take a look at the code [finite.difference.cu](#). The full code is there. In particular, consider how it launches blocks and threads in the kernel function (and call) `__global__ derivative_x, derivative_y, derivative_z`. `setDerivativeParameters` has the arrays containing `dim3` “instantiations” that have the grid and block dimensions, for x-,y-,z-derivatives and for “small” and “long pencils”. Consider “small pencils” for now. The relevant code is as follows:

```
grid[0][0] = dim3(my / sPencils , mz, 1);
block[0][0] = dim3(mx, sPencils , 1);

grid[0][1] = dim3(my / lPencils , mz, 1);
block[0][1] = dim3(mx, sPencils , 1);

grid[1][0] = dim3(mx / sPencils , mz, 1);
block[1][0] = dim3(sPencils , my, 1);

grid[1][1] = dim3(mx / lPencils , mz, 1);
// we want to use the same number of threads as above,
// so when we use lPencils instead of sPencils in one
// dimension, we multiply the other by sPencils/lPencils
block[1][1] = dim3(lPencils , my * sPencils / lPencils , 1);

grid[2][0] = dim3(mx / sPencils , my, 1);
block[2][0] = dim3(sPencils , mz, 1);

grid[2][1] = dim3(mx / lPencils , my, 1);
block[2][1] = dim3(lPencils , mz * sPencils / lPencils , 1);
```

Let $N_i \equiv$ total number of cells in the grid in the i th direction, $i = x, y, z$. N_i corresponds to `m*` in the code, e.g. N_x is `mx`. Note that in this code, what seems to be attempted is calculating the derivatives of a 3-dimensional grid, but using only 2-dimensions on the memory of the device GPU. In my experience, with the NVIDIA GeForce GTX 980 Ti, the maximum number of threads per block in the z -direction and the maximum number of blocks that can be launched in the z -direction is severely limited compared to the x and y directions (use `cudaGetDeviceProperties`, or run the code [queryb.cu](#); I find

(73)

```
Max threads per block: 1024
Max thread dimensions: (1024, 1024, 64)
Max grid dimensions:   (2147483647, 65535, 65535)
).
```

Let M_i be the number of threads on a block in the i th direction. Let N_i^{threads} be the total number of threads in the i th direction on the grid, i.e. the number of threads in the i th grid-direction. $i = x, y$. This is *not* the desired grid dimension N_i . Surely, for a desired grid of size $N_x \times N_y \times N_z \equiv N_x N_y N_z$, then a total of $N_x N_y N_z$ threads are to be computed. Denote $s_{\text{pencil}} \in \mathbb{Z}^+$ to be `sPencils`; example value is $s_{\text{pencil}} = 4$. Likewise, denote $l_{\text{pencil}} \in \mathbb{Z}^+$ to be `lPencils`; example value is $l_{\text{pencil}} = 32 > s_{\text{pencil}} = 4$.

Then, for instance the small pencil case, for the x -derivative, we have

(74)

$$\begin{aligned} &\text{grid dimensions } (N_y/s_{\text{pencil}}, N_z, 1) \\ &\text{block dimensions } (N_x, s_{\text{pencil}}, 1) \end{aligned}$$

Then the total number of threads launched in each direction, x and y , is

$$\begin{aligned} N_x^{\text{threads}} &= \frac{N_y}{s_{\text{pencil}}} N_x \\ N_y^{\text{threads}} &= \frac{N_z}{s_{\text{pencil}}} \end{aligned}$$

While it is true that the total number of threads computed matches our desired grid:

$$N_x^{\text{threads}} \cdot N_y^{\text{threads}} = \frac{N_y}{s_{\text{pencil}}} N_x N_z s_{\text{pencil}} = N_x N_y N_z$$

take a look at the block dimensions that were demanded in Eq. ??, $(N_x, s_{\text{pencil}}, 1)$. The total number of threads to be launched in this block is $N_x \cdot s_{\text{pencil}}$. Suppose $N_x = 1920$. Then easily $N_x \cdot s_{\text{pencil}} >$ allowed maximum number of threads per block. In my case, this number is 1024.

Likewise for the case of x -direction, but with long pencils. The blocks and threads to be launched on the grid and blocks for the kernel function (`derivative_x`) is

(75)

$$\begin{aligned} &\text{grid dimensions } (N_y/l_{\text{pencil}}, N_z, 1) \\ &\text{block dimensions } (N_x, s_{\text{pencil}}, 1) \end{aligned}$$

The total number of threads to be launched in each block is also $N_x \cdot s_{\text{pencil}}$ and for large N_x , this could easily exceed the maximum number of threads per block allowed.

Also, be aware that the shared memory declaration is

```
__shared__ float s_f[sPencils][mx+8]
```

N_x (i.e. `mx`) can be large and we’re requiring a 2-dim. array of size $(N_x + 8) * s_{\text{pencil}}$ of floats, for each block. As, from Code listing [73](#), much more blocks can be launched than threads on a block, and so trying to launch more blocks could possibly be a better solution.

27. MAPPING SCALAR (DATA) TO COLORS; DATA VISUALIZATION

Links I found useful:
Taku Komura has good lectures on visualization with computers; it was heavily based on using VTK, but I found the principles and overview he gave to be helpful: here’s [Lecture 6 Scalar Algorithms: Colour Mapping](#). (Komura’s teaching in the UK, hence spelling “colour”)
Good article on practical implementation of a rainbow: <https://www.particleincell.com/2014/colormap/>, i.e. [Converting Scalars to RGB Colormap](#).
I will formulate the problem mathematically (and clearly).
What we want is this, a bijection:

(76)

$$\begin{aligned} \mathbb{R} &\longrightarrow [0, 255]^3 \equiv RGB \\ \\ [0, 1) &\longrightarrow RGB \\ \\ U \subset \mathbb{R} &\longrightarrow [0, 1) \longrightarrow RGB \\ \\ x \in U &\longmapsto f = \frac{x - \text{minval}}{\text{maxval} - \text{minval}} \longmapsto (r, g, b) \end{aligned}$$

with

$$\begin{aligned} \text{minval} &:= \min_{x \in U} x \\ \text{maxval} &:= \max_{x \in U} x \end{aligned} \text{ and}$$

$$r, g, b \in [0, \dots, 255] \subset \mathbb{Z}$$

Let n = number of “mapping segments” or “segments” (matplotlib terminology). e.g. $n = 5$.
Consider $\frac{1}{n}$, e.g. $\frac{1}{n} = \frac{1}{5} = 0.20$.
Let

(77)

$$y := \lfloor 255(nf - \lfloor nf \rfloor) \rfloor \in [0, 255]$$

Then

(78)

$$(r, g, b) = \begin{cases} (255, y, 0) & \text{if } \lfloor nf \rfloor = 0 \text{ or } 0 \leq nf < 1 \\ (255 - y, 255, 0) & \text{if } \lfloor nf \rfloor = 1 \text{ or } 1 \leq nf < 2 \\ (0, 255, y) & \text{if } \lfloor nf \rfloor = 2 \text{ or } 2 \leq nf < 3 \\ (0, 255 - y, 255) & \text{if } \lfloor nf \rfloor = 3 \text{ or } 3 \leq nf < 4 \\ (y, 0, 255) & \text{if } \lfloor nf \rfloor = 4 \text{ or } 4 \leq nf < 5 \\ (255, 0, 255) & \text{if } \lfloor nf \rfloor = 5 \end{cases}$$

To understand how this color mapping is implemented in matplotlib, take a look at `class matplotlib.colors.LinearSegmentedColormap(name, segmentdata, N=256, gamma=1.0)` of [colors - Matplotlib 1.5.3 documentation](#) ,
and look at
row i: x y0 y1
/
/
row i+1: x y0 y1

http://scipy.github.io/old-wiki/pages/Cookbook/Matplotlib/Show_colormaps and <http://stackoverflow.com/questions/16834861/create-own-colormap-using-matplotlib-and-plot-color-scale>, for more examples of creating color maps, color bars.

28. ON GRIEBEL, DORNSEIFER, AND NEUNHOEFFER’S *Numerical Simulation in Fluid Dynamics: A Practical Introduction*

Griebel, Dornseifer, and Neunhoefffer (1997) [[12](#)]
See also [Software of Research group of Prof. Dr. M. Griebel, Institute für Numerische Simulation](#) <http://wissrech.ins.uni-bonn.de/research/software/>

28.1. **Boundary conditions.** cf. Sec. 2.1. The Mathematical Mode: The Navier-Stokes Equations, Ch. 2 The Mathematical Description of Flows, Griebel, Dornseifer, and Neunhoefffer (1997) [[12](#)], pp. 12-13

Let
 $\varphi_n \equiv$ component of velocity orthogonal to boundary (in exterior normal direction)
 $\varphi_t \equiv$ component of velocity parallel to boundary (in tangential direction)

derivatives in normal direction:

$$\begin{aligned} \frac{\partial \varphi_n}{\partial n} \\ \frac{\partial \varphi_t}{\partial n} \end{aligned}$$

Let fixed boundary $\Gamma := \partial\Omega$. Consider

- (1) *No-slip condition.*
No fluid penetrates boundary.
fluid is at rest there; i.e.

(79)

$$\begin{aligned} \varphi_n(x, y) &= 0 \\ \varphi_t(x, y) &= 0 \end{aligned}$$

- (2) *Free-slip condition.*
No fluid penetrates boundary.
Contrary to no-slip condition,
there’s no frictional losses at boundary, i.e.

(80)

$$\begin{aligned} \varphi_n(x, y) &= 0 \\ \frac{\partial \varphi_t}{\partial n}(x, y) &= 0 \end{aligned}$$

Free-slip condition often imposed along line or plane of symmetry in problem, thereby reducing size of domain, where flow needs to be comprised by half.

cf. 2.2.2. Conservation of Momentum, Ch. 2 The Mathematical Description of Flows, Griebel, Dornseifer, and Neunhoefffer (1997) [[12](#)], pp. 16

For incompressible fluids,

$$\rho(\mathbf{x}, t) = \rho_\infty = \text{const}$$

(81)

$$\frac{\partial \mathbf{u}}{\partial t} + u^j \frac{\partial \mathbf{u}}{\partial x^j} + \frac{1}{\rho_\infty} \text{grad} p = \frac{\mu}{\rho_\infty} \Delta \mathbf{u} + \mathbf{g}$$

with

dynamic viscosity μ

kinematic viscosity $\nu = \frac{\mu}{\rho_\infty}$

28.1.1. *Dynamic Similarity of Flows.* cf. 2.3 Dynamic Similarity of Flows, Ch. 2 The Mathematical Description of Flows, 4 1-(cubic) simplices (edges)

Griebel, Dornseifer, and Neunhoeffler (1997) [12], pp. 17

For incompressible flows,

$$(82) \quad \begin{aligned} (x^i)^* &:= \frac{x^i}{L} \\ t^* &:= \frac{u_\infty t}{L} \\ (u^i)^* &:= \frac{u^i}{u_\infty} \\ p^* &:= \frac{p - p_\infty}{\rho_\infty u_\infty^2} \end{aligned}$$

$$(83) \quad \begin{aligned} \frac{\partial \mathbf{u}^*}{\partial t^*} \left(\frac{u_\infty}{L/u_\infty} \right) + \frac{u_\infty^2}{L} (u^j)^* \frac{\partial \mathbf{u}^*}{\partial (x^j)^*} + \frac{1}{\rho_\infty} \frac{\rho_\infty u_\infty^2}{L} \text{grad}^* p^* &= \frac{\mu}{\rho_\infty} \frac{1}{L^2} u_\infty \Delta^* (\mathbf{u})^* + \mathbf{g} \implies \\ \frac{\partial \mathbf{u}^*}{\partial t^*} + (u^j)^* \frac{\partial \mathbf{u}^*}{\partial (x^j)^*} + \text{grad}^* p^* &= \frac{\mu}{\rho_\infty u_\infty L} \Delta^* \mathbf{u}^* + \frac{L}{u_\infty^2} \mathbf{g} \end{aligned}$$

$$(84) \quad \begin{aligned} \text{Re} &:= \frac{\rho_\infty u_\infty L}{\mu} && \text{(Reynolds number)} \\ \text{Fr} &:= \frac{u_\infty}{\sqrt{L \|\mathbf{g}\|}} && \text{(Froude number)} \end{aligned}$$

Compare this Eq. 83, derived with dynamic similarity, to Eq. (2.2a) of Griebel, Dornseifer, and Neunhoeffler (1997) [12], and my version of the dimensionless momentum conservation equation:

$$(85) \quad \frac{\partial \mathbf{u}}{\partial t} + u^j \frac{\partial \mathbf{u}}{\partial x^j} + \text{grad} p = \frac{1}{\text{Re}} \Delta \mathbf{u} + \mathbf{g}^* \quad (\text{incompressible})$$

However, Griebel, Dornseifer, and Neunhoeffler (1997) [12] writes it as

$$(86) \quad \begin{aligned} \frac{\partial \mathbf{u}}{\partial t} + \frac{\partial u^j \mathbf{u}}{\partial x^j} + \text{grad} p &= \frac{1}{\text{Re}} \Delta \mathbf{u} + \mathbf{g}^* \\ \text{div}(\mathbf{u}) &= 0 \end{aligned}$$

as, separately, a *momentum equation* and *continuity equation*. Griebel, Dornseifer, and Neunhoeffler (1997) [12] ends up implementing this version and so I needed to keep in mind the continuity equation condition.

28.2. Discretization of the Navier-Stokes Equations. cf. 3.1.2 Discretization of the Navier-Stokes Equations, Ch. 3 The Numerical Treatment of the Navier-Stokes Equations, Griebel, Dornseifer, and Neunhoeffler (1997) [12], pp. 26

28.2.1. *Gridding (revisited); staggered grid.* Consider again C_{ij}^2 a 2-(cubic) simplex.

$$\begin{array}{ccc} i = 1, 2, \dots, L_x & \xrightarrow{\text{Griebel, et.al's notation}} & i = 1, 2, \dots, i_{\max} \\ j = 1, 2, \dots, L_y & & j = 1, 2, \dots, j_{\max} \end{array}$$

Assume rectangles of all same size, width δx , length δy .

To clarify (or drive home the point), cell (i, j) , C_{ij}^2 occupies

$$\begin{aligned} [(i-1)\delta x, i\delta x] \times [(j-1)\delta y, j\delta y] & \quad \forall i = 1, 2, \dots, L_x \\ & \quad j = 1, 2, \dots, L_y \end{aligned}$$

cell centers:

$$x_{i-0.5, j-0.5} \equiv (x_{i-0.5, j-0.5}) = ((i-0.5)\delta x, (j-0.5)\delta y)$$

$$\begin{aligned} C_{i, j-0.5}^1 &= \{i\delta x\} \times [(j-0.5)\delta y, j\delta y] & C_{i-0.5, j}^1 &= [(i-0.5)\delta x, i\delta x] \times \{j\delta y\} \\ C_{i-1, j-0.5}^1 &= \{(i-1)\delta x\} \times [(j-0.5)\delta y, j\delta y] & C_{i-0.5, j-1}^1 &= [(i-0.5)\delta x, i\delta x] \times \{(j-1)\delta y\} \end{aligned}$$

(i, j) assigned to pressure at cell center, u^x velocity at right edge, u^y -velocity at upper edge of cell, i.e.

$$\begin{aligned} p : (i, j) &\mapsto p_{i, j} \mapsto ((i-0.5)\delta x, (j-0.5)\delta y) & \forall (i, j) &\in \{1, 2, \dots, L_x\} \times \{1, 2, \dots, L_y\}, \text{ so } (i, j) \mapsto x_{ij} \in \Omega \\ u^x : (i, j) &\mapsto u_{i, j}^x \mapsto (i\delta x, (j-0.5)\delta y) & \forall (i, j) &\in \{1, 2, \dots, L_x\} \times \{1, 2, \dots, L_y\}, \text{ so } (i, j) \mapsto x_{ij} \in \Omega \\ u^y : (i, j) &\mapsto u_{i, j}^y \mapsto ((i-0.5)\delta x, j\delta y) & \forall (i, j) &\in \{1, 2, \dots, L_x\} \times \{1, 2, \dots, L_y\}, \text{ so } (i, j) \mapsto x_{ij} \in \Omega \end{aligned}$$

where domain Ω cell (i, j) , $i = 1 \dots L_x$. $[(i-1)\delta x, i\delta x] \times [(j-1)\delta y, j\delta y]$.

$$j = 1 \dots L_y$$

To reiterate,

$$(i-1, j-0.5) \quad \begin{array}{ccc} (i-0.5, j) & (i-0.5, j-0.5) & (i, j-0.5) \end{array} \xleftarrow{\text{discretization}} \begin{array}{ccc} ((i-1)\delta x, (j-0.5)\delta y) & ((i-0.5)\delta x, j\delta y) & ((i-0.5)\delta x, (j-0.5)\delta y) \\ (i-0.5, j-1) & & ((i-0.5)\delta x, (j-1)\delta y) \end{array} \quad \begin{array}{c} (i\delta x, (j-0.5)\delta y) \end{array}$$

28.2.2. Discretization of the Spatial Derivatives; Treatment of the Spatial Derivatives. cf. 3.1.1 Simple Discretization Formulas, Ch. 3 The Numerical Treatment of the Navier-Stokes Equations, Griebel, Dornseifer, and Neunhoeffler (1997) [12].

$$\begin{aligned} \text{forward difference} & \quad \left[\frac{du}{dx} \right]_i^r := \frac{u(x_{i+1}) - u(x_i)}{\delta x} \\ \text{backward difference} & \quad \left[\frac{du}{dx} \right]_i^l := \frac{u(x_i) - u(x_{i-1})}{\delta x} \\ \text{central difference} & \quad \left[\frac{du}{dx} \right]_i^c := \frac{u(x_{i+1}) - u(x_{i-1})}{2\delta x} \end{aligned}$$

28.2.3. *Stability problems, unphysical oscillations.* Upwind difference or upwinding

$$(87) \quad \left[\frac{du}{dx} \right]_i^{\text{up}} := \frac{(1+\epsilon)(u_i - u_{i-1}) + (1-\epsilon)(u_{i+1} - u_i)}{2\delta x} \quad \text{with } \epsilon := \text{sign}(k)$$

cf. Eq. (3.9) Griebel, Dornseifer, and Neunhoeffler (1997) [12]

where k is in $\frac{d^2 u}{dx^2} + k \frac{du}{dx} = f$ in Ω

Consider the weighted average of both.

$$\begin{aligned} & \gamma \in [0, 1] \\ & \gamma \cdot \text{upwind difference} + (1-\gamma) \cdot \text{central difference} \\ & \gamma \cdot \left[\frac{du}{dx} \right]_i^{\text{up}} + (1-\gamma) \left[\frac{du}{dx} \right]_i^c \end{aligned}$$

donor-cell scheme. Consider $\frac{d(ku)}{dx}$, $k \in C^\infty(\mathbb{R})$.

e.g. $\mathbb{R} \xrightarrow{\text{discretization}} \mathbb{Z}$. Consider $x_i \equiv i\delta x$, $i \in \mathbb{Z}$.

Consider $k_l := k_{i-0.5}$

$$k_r := k_{i+0.5}$$

So then

$$(88) \quad \left[\frac{d(ku)}{dx} \right]_i^{dc} := \frac{k_r u_r - k_l u_l}{\delta x}$$

cf. Eq. (3.11) Griebel, Dornseifer, and Neunhoffer (1997) [12]

And so defining

$$(89) \quad \begin{aligned} u_r &:= \begin{cases} u_i & k_r > 0 \\ u_{i+1} & k_r < 0 \end{cases} \\ u_l &:= \begin{cases} u_{i-1} & k_l > 0 \\ u_i & k_l < 0 \end{cases} \end{aligned}$$

can be rewritten as

$$\begin{aligned}
(90) \quad \left[\frac{d(ku)}{dx} \right]_i^{dc} &= \frac{1}{2\delta x} ((k_r - |k_r|)u_{i+1} + (k_r + |k_r| - k_l + |k_l|)u_i + (-k_l - |k_l|)u_{i-1}) = \\
&= \frac{1}{2\delta x} (k_r(u_i + u_{i+1}) - k_l(u_{i-1} + u_i) + |k_r|(u_i - u_{i+1}) - |k_l|(u_{i-1} - u_i))
\end{aligned}$$

cf. Eq. (3.12), pp. 25, Griebel, Dornseifer, and Neunhoffer (1997) [12].

Consider terms such as

$$\frac{\partial(u^j \mathbf{u})}{\partial x^j}$$

Take the average:

$$(91) \quad \begin{aligned} \left[\frac{\partial(u^x u^y)}{\partial y} \right]_{i,j} &:= \frac{1}{\delta y} \left(\frac{(u_{i,j}^y + u_{i+1,j}^y)}{2} \frac{(u_{i,j}^x + u_{i,j+1}^x)}{2} - \frac{(u_{i,j-1}^y + u_{i+1,j-1}^y)}{2} \frac{(u_{i,j-1}^x + u_{i,j}^x)}{2} \right) \\ \left[\frac{\partial((u^x)^2)}{\partial x} \right]_{i,j} &:= \frac{1}{\delta x} \left(\left(\frac{u_{i,j} + u_{i+1,j}}{2} \right)^2 - \left(\frac{u_{i-1,j} + u_{i,j}}{2} \right)^2 \right) \end{aligned}$$

To understand these formulas, **take a look at Fig. 3.6. on pp. 28** of Griebel, Dornseifer, and Neunhoffer (1997) [12]. Continuity equation.

$$(92) \quad \operatorname{div} \mathbf{u} = 0$$

cf. Eq. (2.2c)

$$\text{div} \mathbf{u} = 0 \xrightarrow{\text{discretization}} \begin{cases} \left[\frac{\partial u}{\partial x} \right]_{i,j} := \frac{u_{i,j} - u_{i-1,j}}{\delta x} \\ \left[\frac{\partial u^y}{\partial y} \right]_{i,j} := \frac{u_{i,j}^y - u_{i,j-1}^y}{\delta y} \end{cases}$$

Also note this result that'll be used for the Poisson equation describing pressure p :

$$(93) \quad \boxed{\text{divgrad} p = \Delta p = \text{div} \left(-\frac{\partial(u^j \mathbf{u})}{\partial x^j} + \frac{1}{\text{Re}} \Delta \mathbf{u} + \mathbf{g}^* \right)}$$

where

$$\xrightarrow{\text{div}\mathbf{u}} \text{div}\left(\frac{\partial\mathbf{u}}{\partial t}\right) = \frac{\partial}{\partial t}\text{div}\mathbf{u} = 0$$

was used.

28.2.4. F, G terms (which include central difference, c , and donor-cell, dc , methods). Page 29 of cf. 3.1.2 Discretization of the Navier-Stokes Equations, Ch. 3 The Numerical Treatment of the Navier-Stokes Equations, Griebel, Dornseifer, and Neunhoffer (1997) [12] is *gold* for understanding the implementation Griebel, Dornseifer, and Neunhoffer had used and how to implement the computation or calculation of F, G .

\forall cell (i, j) , $i = 1 \dots i_{\max} - 1$, $j = 1 \dots j_{\max}$, for u at the midpoint of the *right edge* of the cell, then from Eq. (3.19)a,

$$(94) \quad \left[\frac{\partial(u^2)}{\partial x} \right]_{i,j} := \frac{1}{\delta x} \left(\left(\frac{u_{ij} + u_{i+1j}}{2} \right)^2 - \left(\frac{u_{i-1j} + u_{ij}}{2} \right)^2 \right) + \\ + \gamma \frac{1}{\delta x} \left(\frac{|u_{ij} + u_{i+1j}|}{2} \frac{(u_{ij} - u_{i+1j})}{2} - \frac{|u_{i-1j} + u_{ij}|}{2} \frac{(u_{i-1j} - u_{ij})}{2} \right)$$

Indeed, in this case

$$k_r := \frac{u_{ij} + u_{i+1j}}{2}$$

$$k_l := \frac{u_{i-1j} + u_{ij}}{2}$$

and Eq. 90 tells us (cf. Eq. 3.12 of Griebel, Dornseifer, and Neunhoffer (1997) [12]), which again is

$$\left[\frac{d(ku)}{dx} \right]_i^{dc} = \frac{1}{2\delta x} (k_r(u_i + u_{i+1}) - k_l(u_{i-1} + u_i) + |k_r|(u_i - u_{i+1}) - |k_l|(u_{i-1} - u_i))$$

and so in this case

$$\begin{aligned} \left[\frac{\partial(u^2)}{\partial x} \right]_{ij}^{dc} &= \frac{1}{\delta x} \left[\left(\frac{u_{ij} + u_{i+1j}}{2} \right) \left(\frac{u_{ij} + u_{i+1j}}{2} \right) - \left(\frac{u_{i-1j} + u_{ij}}{2} \right) \left(\frac{u_{i-1j} + u_{ij}}{2} \right) + \frac{|u_{ij} + u_{i+1j}|}{2} \frac{(u_{ij} - u_{i+1j})}{2} + \frac{|u_{i-1j} + u_{ij}|}{2} \frac{(u_{i+1j} - u_{ij})}{2} \right] = \\ &= \left[\frac{\partial(u^2)}{\partial x^2} \right]_{ij}^c + \frac{1}{\delta x} \left(\frac{|u_{ij} + u_{i+1j}|}{2} \frac{(u_{ij} - u_{i+1j})}{2} + \frac{|u_{i-1j} + u_{ij}|}{2} \frac{(u_{i+1j} - u_{ij})}{2} \right) \end{aligned}$$

Consider Fig. 3.6 on pp. 28 of Griebel, Dornseifer, and Neunhoffer (1997) [12], “Values required for the discretization of the u -momentum equation.

$$\begin{array}{ccccc}
& & u_{ij+1} & & \\
& v_{ij} & & v_{i+1j} & \\
u_{i-1j} & & u_{ij} & & u_{i+1j} \\
& v_{ij-1} & & v_{i+1j-1} & \\
& & u_{ij-1} & & \\
& & & & \\
& & v_{ij+1} & & \\
& u_{i-1j+1} & & u_{ij+1} & \\
v_{i-1j} & & v_{ij} & & v_{i+1j} \\
& u_{i-1j} & & u_{ij} & \\
& & v_{ij-1} & &
\end{array}
\Longrightarrow
\begin{array}{ccccc}
& & u_{ij+1}^x & & \\
& & \mathbf{u}_{ij} & & \mathbf{u}_{i+1j} \\
u_{i-1j}^x & & \mathbf{u}_{ij-1} & & u_{i+1j-1}^y \\
& & & & \\
& & & & \\
& & u_{i-1j+1}^x & & \mathbf{u}_{ij+1} \\
& & \mathbf{u}_{i-1j} & & \mathbf{u}_{ij} \\
& & & & u_{ij-1}^y \\
& & & & u_{i+1j}^y
\end{array}$$

$$(95) \quad \begin{aligned} F &:= (u^x)^n + \delta t \left[\frac{1}{\text{Re}} \Delta u^x - \frac{\partial(u^j u^x)}{\partial x^j} + (g^*)^x \right] \\ G &:= (u^y)^n + \delta t \left[\frac{1}{\text{Re}} \Delta u^y - \frac{\partial(u^j u^y)}{\partial x^j} + (g^*)^y \right] \end{aligned}$$

So. Compute $u^{(n+1)}$, $v^{(n+1)}$ according to (3.34), (3.35). cf. pp. 34, 3.2.2 The Discrete Momentum Equations, Griebel, Dornseifer, and Neunhoffer (1997) [12].

$$(96) \quad u_{i,j}^{(n+1)} = F_{i,j}^{(n)} - \frac{\delta t}{\delta x} (p_{i+1,j}^{(n+1)} - p_{i,j}^{(n+1)}) \quad \begin{array}{l} i = 1 \dots i_{\max} - 1 \\ j = 1 \dots j_{\max} \end{array}$$

cf. (3.34) Griebel, Dornseifer, and Neunhoeffler (1997) [12]

$$(97) \quad v_{i,j}^{(n+1)} = G_{i,j}^{(n)} - \frac{\delta t}{\delta y} (p_{i,j+1}^{(n+1)} - p_{i,j}^{(n+1)}) \quad \begin{array}{l} i = 1 \dots i_{\max} \\ j = 1 \dots j_{\max} - 1 \end{array}$$

cf. (3.34) Griebel, Dornseifer, and Neunhoeffler (1997) [12]
with F, G , cf. (3.29), and with

F discretized at right edge of cell (i, j)
 G discretized at upper edge of cell (i, j)

which is

$$(98) \quad F_{i,j} := u_{i,j} + \delta t \left(\frac{1}{\text{Re}} \left(\left[\frac{\partial^2 u}{\partial x^2} \right]_{i,j} + \left[\frac{\partial^2 v}{\partial y^2} \right]_{i,j} \right) - \left[\frac{\partial(u^2)}{\partial x} \right]_{i,j} - \left[\frac{\partial(uv)}{\partial y} \right]_{i,j} + g_x \right) \quad \begin{array}{l} i = 1 \dots i_{\max} - 1 \\ j = 1 \dots j_{\max} \end{array}$$

cf. Eq. (3.36) Griebel, Dornseifer, and Neunhoeffler (1997) [12], and

$$(99) \quad G_{i,j} := v_{i,j} + \delta t \left(\frac{1}{\text{Re}} \left(\left[\frac{\partial^2 v}{\partial x^2} \right]_{i,j} + \left[\frac{\partial^2 v}{\partial y^2} \right]_{i,j} \right) - \left[\frac{\partial(uv)}{\partial x} \right]_{i,j} - \left[\frac{\partial(v^2)}{\partial y} \right]_{i,j} + g_y \right) \quad \begin{array}{l} i = 1 \dots i_{\max} \\ j = 1 \dots j_{\max} - 1 \end{array}$$

cf. Eq. (3.37) Griebel, Dornseifer, and Neunhoeffler (1997) [12].

Consider where the set of cells C_{ij} intersect or overlap that are needed for $u_{i,j}^{n+1}$ and $v_{i,j}^{(n+1)}$.

$$\{(i, j) \mid \begin{array}{l} i = 1 \dots i_{\max} - 1 \\ j = 1 \dots j_{\max} - 1 \end{array}\} \iff u_{i,j}^{(n+1)} = ((u^x)_{i,j}^{(n+1)}, (u^y)_{i,j}^{(n+1)})$$

For Eq. (3.36), (3.37) of Griebel, Dornseifer, and Neunhoeffler (1997) [12], which are quoted for the implementation or algorithm,

$$(100) \quad F_{i,j} := u_{i,j} + \delta t \left(\frac{1}{\text{Re}} \left(\left[\frac{\partial^2 u}{\partial x^2} \right]_{i,j} + \left[\frac{\partial^2 v}{\partial y^2} \right]_{i,j} \right) - \left[\frac{\partial(u^2)}{\partial x} \right]_{i,j} - \left[\frac{\partial(uv)}{\partial y} \right]_{i,j} + g_x \right) \quad \begin{array}{l} i = 1 \dots i_{\max} - 1 \\ j = 1 \dots j_{\max} \end{array}$$

cf. Eq. (3.36) Griebel, Dornseifer, and Neunhoeffler (1997) [12], and

$$(101) \quad G_{i,j} := v_{i,j} + \delta t \left(\frac{1}{\text{Re}} \left(\left[\frac{\partial^2 v}{\partial x^2} \right]_{i,j} + \left[\frac{\partial^2 v}{\partial y^2} \right]_{i,j} \right) - \left[\frac{\partial(uv)}{\partial x} \right]_{i,j} - \left[\frac{\partial(v^2)}{\partial y} \right]_{i,j} + g_y \right) \quad \begin{array}{l} i = 1 \dots i_{\max} \\ j = 1 \dots j_{\max} - 1 \end{array}$$

cf. Eq. (3.37) Griebel, Dornseifer, and Neunhoeffler (1997) [12].

Eq. (3.38) of Griebel, Dornseifer, and Neunhoeffler (1997) [12] is essentially the discretization of the Poisson equation (that takes advantage of the fact that we're dealing with the incompressible case):

$$(102) \quad \Delta p = \frac{1}{\delta t} \text{div}((F, G)) \xrightarrow{\text{discretization}}$$

$$(103) \quad \frac{p_{i+1,j}^{(n+1)} - 2p_{i,j}^{(n+1)} + p_{i-1,j}^{(n+1)}}{(\delta x)^2} + \frac{p_{i,j+1}^{(n+1)} - 2p_{i,j}^{(n+1)} + p_{i,j-1}^{(n+1)}}{(\delta y)^2} = \frac{1}{\delta t} \left(\frac{F_{i,j}^{(n)} - F_{i-1,j}^{(n)}}{\delta x} + \frac{G_{i,j}^{(n)} - G_{i,j-1}^{(n)}}{\delta y} \right) \quad \begin{array}{l} i = 1 \dots i_{\max} \\ j = 1 \dots j_{\max} \end{array}$$

cf. Eq. (3.38) of Griebel, Dornseifer, and Neunhoeffler (1997) [12], pp. 35, 3.2.3. The Poisson Equation for the Pressure.

28.2.5. *Poisson equation for pressure (for this incompressible case), and towards SOR (successive Over Relaxation) method.*

$$(104) \quad \frac{\epsilon_i^E (p_{i+1,j}^{(n+1)} - p_{i,j}^{(n+1)}) - \epsilon_i^W (p_{i,j}^{(n+1)} - p_{i-1,j}^{(n+1)})}{(\delta x)^2} + \frac{\epsilon_j^N (p_{i,j+1}^{(n+1)} - p_{i,j}^{(n+1)}) - \epsilon_j^S (p_{i,j}^{(n+1)} - p_{i,j-1}^{(n+1)})}{(\delta y)^2} =$$

$$= \frac{1}{\delta t} \left(\frac{F_{i,j}^{(n)} - F_{i-1,j}^{(n)}}{\delta x} + \frac{G_{i,j}^{(n)} - G_{i,j-1}^{(n)}}{\delta y} \right)$$

$$\begin{array}{l} i = 1 \dots i_{\max} \\ j = 1 \dots j_{\max} \end{array}$$

where

$$\epsilon_i^W := \begin{cases} 0 & i = 1 \\ 1 & i > 1 \end{cases}$$

$$\epsilon_i^E := \begin{cases} 1 & i < i_{\max} \\ 0 & i = i_{\max} \end{cases}$$

$$\epsilon_j^S := \begin{cases} 0 & j = 1 \\ 1 & j > 1 \end{cases}$$

$$\epsilon_j^N := \begin{cases} 1 & j < j_{\max} \\ 0 & j = j_{\max} \end{cases}$$

cf. Eq. (3.43) Griebel, Dornseifer, and Neunhoeffler (1997) [12].

e.g., for $1 < i < i_{\max}$, and $1 < j < j_{\max}$,

$$\frac{(p_{i+1,j}^{(n+1)} - p_{i,j}^{(n+1)}) - (p_{i,j}^{(n+1)} - p_{i-1,j}^{(n+1)})}{(\delta x)^2} + \frac{(p_{i,j+1}^{(n+1)} - p_{i,j}^{(n+1)}) - (p_{i,j}^{(n+1)} - p_{i,j-1}^{(n+1)})}{(\delta y)^2} =$$

$$= \frac{1}{\delta t} \left(\frac{F_{i,j}^{(n)} - F_{i-1,j}^{(n)}}{\delta x} + \frac{G_{i,j}^{(n)} - G_{i,j-1}^{(n)}}{\delta y} \right)$$

Indeed it takes into account the discretized boundary conditions, which was obtained from the so-called *Chorin projection method* (which is based on the Hodge-Helmholtz decomposition).

$$(105) \quad \begin{array}{lll} p_{0,j} = p_{i,j}; & p_{imax+1,j} = p_{imax,j}, & j = 1 \dots j_{\max} \\ p_{i,0} = p_{i,1}; & p_{i,jmax+1} = p_{i,jmax}, & i = 1 \dots i_{\max} \end{array}$$

cf. Eq. (3.41) Griebel, Dornseifer, and Neunhoeffler (1997) [12].

28.2.6. *Successive Over Relaxation (SOR) method.* $\forall it = 1 \dots it_{\max}$

$$i = 1 \dots i_{\max},$$

$$j = 1 \dots j_{\max}$$

$$(106) \quad p_{i,j}^{it+1} := (1 - \omega) p_{i,j}^{it} +$$

$$+ \frac{\omega}{\left(\frac{\epsilon_i^E + \epsilon_i^W}{(\delta x)^2} + \frac{\epsilon_j^N + \epsilon_j^S}{(\delta y)^2} \right)} \cdot \left(\frac{\epsilon_i^E p_{i+1,j}^{it} + \epsilon_i^W p_{i-1,j}^{it+1}}{(\delta x)^2} + \frac{\epsilon_j^N p_{i,j+1}^{it} + \epsilon_j^S p_{i,j-1}^{it+1}}{(\delta y)^2} - \text{rhs}_{ij} \right)$$

cf. Eq. (3.44) Griebel, Dornseifer, and Neunhoeffler (1997) [12].

28.2.7. *Implementation, routines, algorithms for incompressible Navier-Stokes equations solver.* Consider the serial version of and so the incompressible Navier-Stokes equations solver with finite difference:

- $t := 0, n := 0$
- initial values of u, v, p i.e. \mathbf{u}, p
- While $t < t_{\text{end}}$
 - select δt (according to (3.50) if stepsize control is used)
 - set boundary values for u, v
 - Compute $F^{(n)}$ and $G^{(n)}$ according to (3.36), (3.37)
 - Compute RHS of pressure Eq. (3.38)
 - Set $it := 0$
 - While $it < it_{\text{max}}$ and $\|r^{it}\| > \text{eps}$ (resp., $\|r^{it}\| > \text{eps} \|p^0\|$)
 - * Perform an SOR cycle according to (3.44)
 - * Compute the residual norm for the pressure equation, $\|r^{it}\|$
 - * $it := it + 1$
 - Compute $u^{(n+1)}$ and $v^{(n+1)}$ according to (3.34), (3.35)
 - $t := t + \delta t$
 - $n := n + 1$

Algorithm 1. Base version (or serial version), pp. 40, of Griebel, Dornseifer, and Neunhoeffler (1997) [12].

Compare this with the parallelized version:

- $t := 0, n := 0$
- initial values of u, v, p i.e. \mathbf{u}, p
- While $t < t_{\text{end}}$
 - select δt (according to (3.50) if stepsize control is used)
 - set boundary values for u, v
 - Compute $F^{(n)}$ and $G^{(n)}$ according to (3.36), (3.37)
 - Compute RHS of pressure Eq. (3.38)
 - Set $it := 0$
 - While $it < it_{\text{max}}$ and $\|r^{it}\| > \text{eps}$ (resp., $\|r^{it}\| > \text{eps} \|p^0\|$)
 - * Perform an SOR cycle according to (3.44)
 - * Exchange the pressure values in the boundary strips
 - * Compute the partial residual and send these to the master process
 - * *Master process* computes the residual norm of the pressure equation $\|r^{it}\|$ and broadcasts it to all processes
 - * $it := it + 1$
 - Compute (update) $u^{(n+1)}$ and $v^{(n+1)}$ according to (3.34), (3.35)
 - $t := t + \delta t$
 - $n := n + 1$

Algorithm 3. Parallel version, pp. 115 of Griebel, Dornseifer, and Neunhoeffler (1997) [12]

28.2.8. *Lid-Driven Cavity.* cf. 5.1. Lid-Driven Cavity. Ch. 5 Example Applications, Griebel, Dornseifer, and Neunhoeffler (1997) [12].

On $S_N \subset \partial\Omega \subset \Omega \subset \mathbb{R}^2$ (“northern” N boundary of domain Ω , which is a submanifold of Ω or of \mathbb{R}^2 ,

$$(107) \quad \begin{aligned} u^x(x) &= \bar{u} \\ u^y(x) &= 0 \end{aligned} \quad \forall x \in S_N$$

For the *discretization*, consider the staggered grid and how u^x or i.e. the x -component of velocity field \mathbf{u} , u , is at the center of each edge (or i.e. face), and by convention, the cell index (i, j) corresponds to the “right” face. So

$$\begin{array}{ccc} u_{i-1, j_{\text{max}}+1}^x & & u_{i, j_{\text{max}}+1}^x \\ & u_{i-\frac{1}{2}, j_{\text{max}}+\frac{1}{2}}^x & \\ u_{i-1, j_{\text{max}}}^x & & u_{i, j_{\text{max}}}^x \end{array}$$

$$(108) \quad \begin{aligned} u_{i, j_{\text{max}}+\frac{1}{2}}^x &= \frac{u_{i, j_{\text{max}}+1}^x + u_{i, j_{\text{max}}}^x}{2} = \bar{u} \text{ or} \\ u_{i, j_{\text{max}}+1}^x &= 2\bar{u} - u_{i-1, j_{\text{max}}}^x; \quad i = 1 \dots i_{\text{max}} \end{aligned}$$

Part 7. Finite Element; Finite Element Method, Finite Element Analysis; Finite Element Exterior Calculus

cf. Lecture 11 “Method of Weighted Residuals”, Darmofal (2005) [10]

cf. Ch. 23 of “Electrostatics via Finite Elements” from Landau, Páez, and Bordeianu (2015) [15].

$$(109) \quad \phi_i(x) = \begin{cases} 0 & \text{for } x < x_{i-1} \text{ or } x > x_{i+1} \\ \frac{x-x_{i-1}}{h_{i-1}} & \text{for } x_{i-1} \leq x \leq x_i \\ \frac{x_{i+1}-x}{h_i} & \text{for } x_i \leq x \leq x_{i+1} \end{cases}$$

Consider

$$*\mathbf{d}\phi(x) \in \Omega^{d-1}(N)$$

e.g. $d = 3, d - 1 = 2$.

Consider $\omega \in \Omega^p(N)$, in general, $f \in C^\infty(N)$, in general.

$$\begin{aligned} \int_{\Omega} d(f\omega) &= \int_{\Omega} d(f\omega_{i_1 \dots i_p}) dx^{i_1} \wedge \dots \wedge dx^{i_p} = \int_{\Omega} \frac{\partial(f\omega_{i_1 \dots i_p})}{\partial x^d} dx^j \wedge dx^{i_1} \wedge \dots \wedge dx^{i_p} = \int_{\Omega} (d\omega)f + \int_{\Omega} (df) \wedge \omega = \\ &= \int_{\partial\Omega} f\omega \end{aligned}$$

$\dim\Omega = p + 1$.

$$(110) \quad \implies \int_{\Omega} f(d\omega) + \int_{\Omega} (df) \wedge \omega = \int_{\partial\Omega} f\omega$$

So if $\Omega = -*\mathbf{d}U$; $U \in C^\infty(N)$, $E = -dU \in \Omega^1(N) = \Gamma(T^*N)$, $f = \Phi$

$$- \int_{\Omega} (\mathbf{d}* \mathbf{d})U(x)\Phi(x) = - \int_{\partial\Omega} (*\mathbf{d}U)\Phi(x) + \int_{\Omega} \mathbf{d}\Phi \wedge *\mathbf{d}U = \int_{\Omega} 4\pi\rho(x)\Phi \text{vol}^d$$

Writing out the components, which is true in full generality,

$$\begin{aligned} \mathbf{d}U &= \frac{\partial U}{\partial x^j} dx^j \\ *\mathbf{d}U &= \frac{\sqrt{g}}{(d-1)!} \epsilon_{i_1, i_2 \dots i_d} g^{ij} \frac{\partial U}{\partial x^j} dx^{i_2} \wedge \dots \wedge dx^{i_d} \end{aligned}$$

Then, insightfully,

$$\mathbf{d}\Phi \wedge *\mathbf{d}U = \frac{\partial \Phi}{\partial x^{i_1}} g^{i_1 j} \frac{\partial U}{\partial x^j} \text{vol}^d \equiv \langle \text{grad}\Phi, \text{grad}U \rangle$$

$$(111) \quad \implies - \int_{\partial\Omega} (*\mathbf{d}U)\Phi(x) + \int_{\Omega} \mathbf{d}\Phi \wedge *\mathbf{d}U = - \int_{\partial\Omega} (*\mathbf{d}U)\Phi(x) + \int_{\Omega} \langle \text{grad}\Phi, \text{grad}U \rangle = \int_{\Omega} 4\pi\rho(x)\Phi \text{vol}^d$$

$\forall l = 0, 1, \dots N-1$

$$\int_{\Omega} 4\pi\rho(x)\phi_l(x)\text{vol}^d = \int_{x_{i-1}}^{x_i} 4\pi\rho(x) \frac{x-x_{i-1}}{h_{i-1}} dx + \int_{x_i}^{x_{i+1}} 4\pi\rho(x) \frac{x_{i+1}-x}{h_i} dx$$

cf. Lecture 11, Method of Weighted Residuals, **lect11.pdf**, Darmofal (2005) [10].

From heat equation,

$$(112) \quad \text{div}(k\text{grad}T) = -q$$

or in components

$$\frac{1}{\sqrt{g}}\frac{\partial}{\partial x^k}\left(\sqrt{g}k\frac{\partial T}{\partial x^l}g^{kl}\right)=-q$$

In 1-dim.,

$$\frac{\partial}{\partial x^k}\left(k\frac{\partial T}{\partial x^k}\right)=-q$$

over $\Omega = \left[-\frac{L}{2}, \frac{L}{2}\right]$.
e.g. Ex. 11.1 (Steady heat diffusion),
Suppose $L = 2$,
thermal conductivity $k = 1$,
heat source $q(x) = 50e^x$.
 $T(\pm 1) = 100$.
Integrate twice:

$$\begin{aligned} kT'' &= -50e^x \\ T' &= \frac{-50e^x}{k} + A_0 \\ T &= \frac{-50e^x}{k} + A_0x + B_0 \\ \frac{-50e}{k} + A_0 + B_0 &= 100 \\ \frac{-50e^{-1}}{k} - A_0 + B_0 &= 100 \\ B_0 &= 100 + \frac{50}{k} \cosh 1 \\ A_0 &= \frac{50}{k} \sinh 1 \end{aligned}$$

$$T = \frac{-50e^x}{k} + \frac{50}{k} \sinh 1x + 100 + \frac{50}{k} \cosh 1$$

A common approach is to approximate the solution with a series of weighted functions.
cf. 11.2. The Method of Weighted Residuals of Darmofal (2005) [10], Lecture 11, `lect11.pdf`.
Consider

$$\int_{-1}^1 w(x)R(\overline{T},x)dx$$

Choose N weight functions $w_j(x)$ for $1 \leq j \leq N$,
and setting N weighted residuals to 0

$$(113) \qquad R_j(\overline{T}) = \int_{-1}^1 w_j(x)R(\overline{T},x)dx \equiv \text{ weighted residual for } w_j$$

determine appropriate weight functions,
Galerkin method is to set weight functions euqal to functions used to approximate solution

$$w_j(x) = \phi_j(x) \qquad (\text{Galerkin})$$

e.g. 1-dim. heat diffusion:

$$\begin{aligned} w_1(x) &= (1-x)(1+x) \\ w_2(x) &= x(1-x)(1+x) \end{aligned}$$

$$\begin{aligned} R_1(\overline{T}) &= \int_{-1}^1 w_1(x)R(\overline{T},x)dx = \int_{-1}^1 (1-x)(1+x)(-2\alpha_1 - 6\alpha_2x + 50e^x)dx = \frac{-8}{3}\alpha_1 + 200e^{-1} = 0 \\ R_2(\overline{T}) &= \int_{-1}^1 w_2(x)R(\overline{T},x)dx = \int_{-1}^1 x(1-x)(1+x)(-2\alpha_1 - 6\alpha_2x + 50e^x)dx = \frac{-8}{3}\alpha_2 + 100e^{-1} - 1200e^{-1} = 0 \end{aligned}$$

and so, I would say (EY : 20170204)

$$(114) \qquad R_j(\overline{T}) = 0 \mapsto \alpha_j$$

cf. 12.3. 1-D Linear Elements and the Nodal Basis of Darmofal (2005) [10], Lecture 12, The Finite Element Method for One-Dimensional Diffusion `lect12.pdf`.

28.2.9. *nodal basis*. N elements, $N + 1$ degrees of freedom.

$$\begin{aligned} \tilde{T}(x) &= \sum_{i=1}^{N+1} a_i\phi_i(x) \text{ or } \tilde{T}(x) = \sum_{i=0}^N a_i\phi_i(x) \\ \text{if } \tilde{T}(x) &= \sum_{i=1}^{N+1} \tilde{T}(x_i)\phi_i(x) \text{ or } \tilde{T}(x) = \sum_{i=0}^N \tilde{T}(x_i)\phi_i(x) \end{aligned}$$

iff

$$\tilde{T}(x_j) = \sum_{i=0}^N a_i\phi_i(x_j) = \sum_{i=0}^N a_i\delta_{ij} = a_j$$

Now

$$\phi_i(x) = \begin{cases} 0 & \text{for } x < x_{i-1} \\ \frac{x-x_{i-1}}{\Delta x_{i-1}} & \text{for } x_{i-1} < x < x_i \\ \frac{x_{i+1}-x}{\Delta x_i} & \text{for } x_i < x < x_{i+1} \\ 0 & \text{for } x > x_{i+1} \end{cases}$$

But, if we only have “nodal points” or grid points, or given points on \mathbb{R} to evaluate \tilde{T} of $\{x_0,x_1,\dots x_N\}$ (for $N + 1$ points),
then how can we define the following basis functions?

$$\begin{aligned} \phi_0(x) &= \begin{cases} \frac{x_1-x}{\Delta x_0} & \text{for } x_0 < x < x_1 \\ 0 & \text{for } x > x_1 \end{cases} \\ \phi_1(x) &= \begin{cases} 0 & \text{for } x < x_0 \\ \frac{x-x_0}{\Delta x_0} & \text{for } x_0 < x < x_1 \\ \frac{x_2-x}{\Delta x_1} & \text{for } x_1 < x < x_2 \\ 0 & \text{for } x > x_2 \end{cases} \\ \phi_N(x) &= \begin{cases} 0 & \text{for } x < x_{N-1} \\ \frac{x-x_{N-1}}{\Delta x_{N-1}} & \text{for } x_{N-1} < x < x_N \\ \frac{x_{N+1}-x}{\Delta x_N} & \text{for } x_N < x < x_{N+1} \\ 0 & \text{for } x > x_{N+1} \end{cases} \\ \phi_{N+1}(x) &= \begin{cases} 0 & \text{for } x < x_N \\ \frac{x-x_N}{\Delta x_N} & \text{for } x_N < x < x_{N+1} \end{cases} \end{aligned}$$

Consider defining the residual $R = R(\tilde{T},x)$ as

$$R(\tilde{T},x) := \text{div}(k\text{grad}T) + q$$

and consider multiplying it by so-called “weights”, Φ , which is set to be ϕ_j ’s.

$$\begin{aligned} \int_{\Omega} d * (kdT) \Phi &= \int_{\Omega} -q \Phi \text{vol}^d \implies \int_{\Omega} (d * (kdT) + q \text{vol}^d) \Phi = 0 \\ &\implies \int_{\partial\Omega} *(kdT) \Phi 0 \int_{\Omega} \langle \text{grad} \Phi, k \text{grad} T \rangle + \int_{\Omega} q \Phi \text{vol}^d \\ &\quad - \int_{\Omega} \langle \text{grad} \phi_j, k \text{grad} T \rangle + \int_{\mathcal{O}_j} q \phi_j \text{vol}^d = 0 \end{aligned}$$

If $\Phi = \phi_j(x)$, s.t. $\phi_j(x) = 0$ on $\partial\Omega$,

e.g. $\mathcal{O}_j = [x_{j-1}, x_{j+1}]$
if

$$\phi_j(x) = \begin{cases} 0 & \text{for } x < x_{i-1} \\ \frac{x - x_{i-1}}{\Delta x_{i-1}} & \text{for } x_{i-1} < x < x_i \\ \frac{x_{i+1} - x}{\Delta x_i} & \text{for } x_i < x < x_{i+1} \\ 0 & \text{for } x > x_{i+1} \end{cases}$$

$$\int_{x_{i-1}}^{x_i} \frac{k \text{grad} T}{\Delta x_{i-1}} \text{vol}^d - \int_{x_i}^{x_{i+1}} \frac{k \text{grad} T}{\Delta x_i} \text{vol}^d + \int_{\mathcal{O}_j} q \phi_j \text{vol}^d = 0$$

Suppose $\tilde{T}(x) = \sum_{i=1}^{N+1} a_i \phi_k(x)$,

$$\begin{aligned} &\int_{x_{i-1}}^{x_i} \frac{k}{\Delta x_{i-1}^2} (a_i - a_{i-1}) - \int_{x_i}^{x_{i+1}} \frac{k}{\Delta x_i^2} (-a_i + a_{i+1}) + \int_{\mathcal{O}_j} q \phi_j \text{vol}^d = 0 = \\ &= \frac{a_i - a_{i-1}}{\Delta x_{i-1}^2} \int_{x_{i-1}}^{x_i} k - \frac{a_{i+1} - a_i}{\Delta x_i^2} \int_{x_i}^{x_{i+1}} k + \int_{\mathcal{O}_j} q \phi_j \text{vol}^d = 0 = k \frac{a_i - a_{i-1}}{\Delta x_{i-1}} - k \frac{a_{i+1} - a_i}{\Delta x_i} + \int_{\mathcal{O}_j} q \phi_j \text{vol}^d = 0 \end{aligned}$$

If $q = 50e^x$, and consider that

$$\int 50e^x \phi_j \text{vol}^d = \frac{50}{\Delta x_{i-1}} (xe^x - e^x - x_{i-1}e^x)$$

Part 8. CUB

29. CUB

29.1. **Striped arrangement.** Recall our notation

$$i_x \in \{0, 1, \dots, M_x - 1\} \subset \mathbb{Z} \iff \text{threadIdx.x}$$

with M_x corresponding to `blockDim.x`

Consider

$$\begin{aligned} F : \{0, 1, \dots, M_x - 1\} \subset \mathbb{Z} &\rightarrow \mathbb{K}^d \\ F(i_x) &= (F^{(0)}(i_x), F^{(d)}(i_x) \dots F^{(d)}(i_x)) \end{aligned}$$

with $d \iff$ items per thread, or `ITEMS_PER_THREAD`.

5.3 Flexible data arrangement across threads, Striped arrangement mentions a *logical stride* $S_x \in \mathbb{Z}^+ \iff \text{BLOCK_THREADS}$.

It goes on to say (CUB documentation) “thread i owns items $(i), (i + \text{block-threads}), \dots (i + \text{block-threads} * (\text{items-per-thread} - 1))$ ”.

Denote this `block-threads * items-per-thread`, stripped arrangement, as $S_x d$.

What is this `BLOCK_THREADS`? The thread block size in threads, from template parameters for the **cub LoadDirectStriped** documentation. It wasn’t obvious to me at first, until reading the documentation on its template parameters.

So

$$S_x = M_x$$

Is this enforced somehow? What happens if $S_x > 1024$, the hardware limit on number of threads on a single block?

So we are asked to consider

$$i_x, M_x, d \mapsto \{i_x, i_x + M_x, \dots, i_x + M_x l^x, \dots, i_x + M_x(d - 1)\}_{l^x=0,1,\dots,d-1}$$

Consider the “maximal” case:

$$i_x = M_x - 1, M_x, d \mapsto \{M_x - 1, 2M_x - 1, \dots, M_x d - 1\}$$

So we have $M_x d$ total values to consider for this single (thread) block.

In reality, what’s really going on is the flatten functor, necessitated by just how C/C++ doesn’t have multi-dimensional arrays.

$$\begin{aligned} (115) \quad F : \{0, 1, \dots, M_x - 1\} \subset \mathbb{Z} &\rightarrow \mathbb{K}^d \mapsto F : \{0, 1 \dots M_x d - 1\} \subset \mathbb{Z} \rightarrow \mathbb{K}^1 \\ F \in L(\{0, 1 \dots M_x - 1\} \subset \mathbb{K}^d) &\rightarrow L(\{0, 1 \dots M_x d - 1\}, \mathbb{K}) \end{aligned}$$

with $M_x \leq 1024$ (hardware enforced)

30. THEANO’S SCAN

cf. **theano scan tutorial**

30.1. Example 3: Reusing outputs from the previous iterations. For input $X : \mathbb{Z}^+ \rightarrow R - \text{Module}$, e.g. R -Module such $t \mapsto X(t)$ as $\mathbb{R}^d, V, \text{Mat}_{\mathbb{R}}(N_1, N_2), \tau_s^r(V)$, and a function f that acts at each iteration, i.e. $\forall t = 0, 1, \dots, T$,

$$\begin{aligned} f : R - \text{Module} \times R - \text{Module} &\rightarrow R - \text{Module} \\ (X_1, X_0) &\mapsto X_1 + X_0 \end{aligned}$$

, then we want to express

$$f(X(t), X(t - 1)) = X(t) + X(t - 1) \quad \forall t = 0, 1 \dots T - 1,$$

In the end, we should get

$$X \in (R - \text{Module})^T \xrightarrow{\text{scan}} Y \in (R - \text{Module})^T$$

$Y \equiv \text{output}$

In summary, the dictionary between the mathematics and the Python theano code for scan seems to be the following:

$$\begin{aligned} (116) \quad f : R - \text{Module} \times R - \text{Module} &\rightarrow R - \text{Module} \\ F(X(t), X(t - 1)) &\mapsto X(t) \iff \text{Python function (object) or Python lambda expression} \mapsto \text{scan(fn=} \end{aligned}$$

$$(X(0), X(1), \dots, X(T - 1)) \in (R - \text{Module})^T \iff \text{scan(sequences=} \end{array}$$

$$X(-1) \in R - \text{Module} \iff \text{scan(outputs_info=[\quad])}$$

30.2. **Example 4 : Reusing outputs from multiple past iterations.** $\forall t = 0, 1, \dots T-1, T \iff \mathbf{n_steps} = T$,

Consider

$$(117) \quad F : (R - \text{Module})^k \rightarrow R - \text{Module} \\ F(X(t-k), X(t-(k-1)), \dots X(t-1)) = X(t) \iff \mathbf{fn} \in \text{Python function (object)}$$

If $k = 1$, we'll need to be given $X(0) \in R - \text{Module}$. Perhaps consider $\forall t = -k, -(k-1), \dots -1, 0, 1 \dots T-1$ ("in full").

For $k > 1$, we'll need to be given (or declare) $\{X(-k), X(-(k-1)), \dots X(-1)\}$.

So for $k = 1$, $X(-1) \in R - \text{Module}$ needed \iff e.g. `T.scalar()` if $R - \text{Module} = \mathbb{R}$.

for $k > 1$, $(X(-k), X(-(k-1)), \dots X(-1)) \in (R - \text{Module})^k \iff$ e.g. `T.vector()`, into ''initial'' of a Python dict, if $R - \text{Module} = \mathbb{R}$.

Also, for $k > 1$,

$$(-k, -(k-1), \dots -1) \iff \mathbf{taps} = [-k, -(k-1), \dots -1] (\text{a Python list})$$

scan, essentially, does this:

$$(118) \quad (X(-k), X(-(k-1)), \dots X(-1)) \mapsto (X(0), X(1) \dots X(T-1)) \\ F(X(t-k), X(t-(k-1)) \dots X(t-1)) = X(t), \quad \forall t = 0, 1, \dots T-1$$

given $F : (R - \text{Module})^k \rightarrow R - \text{Module}$, with $T = \mathbf{n_steps}$.

Part 9. Finite State Machines, State Machines; Embedded Systems

31. DISCRETE DYNAMICS; FINITE-STATE MACHINES

cf. Ch. 3 Discrete Dynamics, Lee and Seshia (2016) [17]

cf. 3.1 Discrete Systems, Lee and Seshia (2016) [17]

e.g. consider,

count number of cars in garage by counting number of cars that enter and leave.

Each entry, departure, modeled as discrete event;

discrete event occurs at instance of time rather than over time.

cont., integrator case: given input $x : \mathbb{R} \rightarrow \mathbb{R}$ (cont.-time signal)

signal u going into up port of counter, on the other hand,

$$u : \mathbb{R} \rightarrow \{\text{absent}, \text{present}\} \equiv \{\{\}, \{\ast\}\}$$

means $\forall t \in \mathbb{R}, u(t) = \text{absent} = \{\}$, meaning there's no event at that time, or

$u(t) = \text{present} \equiv \{\ast\}$, meaning that there is.

Counter.

when event present at up/down input port,

increments/decrement its count and produces on

output new value of the count

at all other times (when both inputs absent), produces no output (count output absent)

$$c : \mathbb{R} \rightarrow \{\text{absetn}\} \cup \mathbb{Z} \equiv \{\{\}\} \cup \mathbb{Z}$$

Clearly, no need for this counter to do anything when input is absent.

31.0.1. *Discrete signals.* Discrete signals consist of sequence of instantaneous events in time

consider signal $e : \mathbb{R} \rightarrow \{\text{absent}\} \cup X, X \in \mathbf{ObjSets}$.

Definition 7. *Signal e is **discrete signal** if, absent most of time, and, we can count, in order, times which it's present. \forall time it's present, we have discrete event.*

Count events in order is important.

Let $T \subseteq \mathbb{R}$ s.t.

$$T = \{t \in \mathbb{R} | e(t) \neq \text{absent} \equiv \{\}\}$$

Then e discrete if \exists 1-to-1 function $f : T \rightarrow \mathbb{N}$. That's order preserving, i.e. $\forall t_1, t_2 \in T$, where $t_1 \leq t_2$, then $f(t_1) \leq f(t_2)$ reactions of discrete system triggered by environment in which discrete system operates, **event triggered**.

when both inputs to counter are absent, no reaction occur.

31.0.2. *Modeling Actors as Functions.* integrator actor in Sec. 2.2, Fig. 3.2, modeled by

$$I_i : \mathbb{R}^{\mathbb{R}_+} \rightarrow \mathbb{R}^{\mathbb{R}_+}$$

$$I_i : L(\mathbb{R}, \mathbb{R}_+) \rightarrow L(\mathbb{R}, \mathbb{R}_+)$$

$$y = I_i(x)$$

where $i \equiv$ initial value of integration, x, y cont.-time signal

e.g., if $i = 0, \forall t \in \mathbb{R}_+, x(t) = 1$, then

$$y(t) = i + \int_0^t x(\tau) d\tau = t$$

Similarly, counter in Fig. 3.1, modeled by

$$c_i : (\mathbb{R}_+ \rightarrow \{\text{absent}, \text{present}\})^P \rightarrow (\mathbb{R}_+ \rightarrow \{\text{absent}\} \cup \mathbb{Z})$$

where $P = \{\text{up}, \text{down}\} \equiv \{u, d\} =$ set of input ports.

For example,

$$u : \mathbb{R}_+ \rightarrow \{\text{absent}, \text{present}\} \equiv \{\emptyset, \{\ast\}\}$$

$$u : t \mapsto u(t) \in \equiv \{\emptyset, \{\ast\}\}$$

Lee and Seshia (2016) [17] notation:

$$A^B \equiv \text{set of all functions from } B \text{ to } A$$

A particular reaction will observe values of inputs at particular time t , and calculate output values for some time t .

Suppose actor has input ports $P = \{p_1 \dots p_N\}$.

Assume $\forall p \in P, V_p \in \mathbf{ObjSets} \equiv$ values that maybe received on port p , when input present.

$V_p \equiv$ type of port p .

At reaction, $\forall p \in P$, let value of $p, p \in V_p \cup \{\text{absent}\}$ (Lee and Seshia seems to reuse notation, notation by context, here; but it's troublesome)

valuation of inputs P is assignment of value in V_p, \forall variable $p \in P$, or assertion that p absent.

I think Lee and Seshia is terribly confused in the writing and lack of clarity is horrendous and pernicious.

32. DISCRETE DYNAMICS; FINITE-STATE MACHINES

cf. Ch. 3 Discrete Dynamics, Lee and Seshia (2016) [17] cf. 3.1 Discrete Systems, Lee and Seshia (2016) [17] e.g. consider, count number of cars in garage by counting number of cars that enter and leave. Each entry, departure, modeled as discrete event;

discrete event occurs at instance of time rather than over time. cont., integrator case: given input $x : \mathbb{R} \rightarrow \mathbb{R}$ (cont.-time signal)

signal u going into up port of counter, on the other hand,

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means $\forall t \in \mathbb{R}, u(t) = \text{absent} = \{\}$, meaning there's no event at that time, or

$u(t) = \text{present} \equiv \{\ast\}$, meaning that there is. Counter.

when event present at up/down input port,

increments/decrement its count and produces on

output new value of the count

at all other times (when both inputs absent), produces no output (count output absent)

$$c : \mathbb{R} \rightarrow \{\text{absetn}\} \cup \mathbb{Z} \equiv \{\{\}\} \cup \mathbb{Z}$$

Clearly, no need for this counter to do anything when input is absent.

32.0.1. *Discrete signals.* Discrete signals consist of sequence of instantaneous events in time consider signal $e : \mathbb{R} \rightarrow \{ \text{absent} \} \cup X$, $X \in \mathbf{ObjSets}$.

Definition 8. Signal e is **discrete signal** if, absent most of time, and, we can count, in order, times which it's present. \forall time it's present, we have discrete event. Count events in order is important. Let $T \subseteq \mathbb{R}$ s.t.

$$T = \{t \in \mathbb{R} | e(t) \neq \text{absent} \equiv \{\{\}\}$$

Then e discrete if \exists 1-to-1 function $f : T \rightarrow \mathbb{N}$. That's order preserving, i.e. $\forall t_1, t_2 \in T$, where $t_1 \leq t_2$, then $f(t_1) \leq f(t_2)$ reactions of discrete system triggered by environment in which discrete system operates, **event triggered**. when both inputs to counter are absent, no reaction occur.

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$$I_i : \mathbb{R}^{\mathbb{R}_+} \rightarrow \mathbb{R}^{\mathbb{R}_+}$$

$$I_i : L(\mathbb{R}, \mathbb{R}_+) \rightarrow L(\mathbb{R}, \mathbb{R}_+)$$

$$y = I_i(x)$$

where $i \equiv$ initial value of integration, x, y cont.-time signal e.g., if $i = 0$, $\forall t \in \mathbb{R}_+$, $x(t) = 1$, then

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Similarly, counter in Fig. 3.1, modeled by

$$c_i : (\mathbb{R}_+ \rightarrow \{ \text{absent}, \text{present} \})^P \rightarrow (\mathbb{R}_+ \rightarrow \{ \text{absent} \} \cup \mathbb{Z})$$

where $P = \{ \text{up}, \text{down} \} \equiv \{u, d\}$ = set of input ports. For example,

$$u : \mathbb{R}_+ \rightarrow \{ \text{absent}, \text{present} \} \equiv \{\emptyset, \{*\}\}$$

$$u : t \mapsto u(t) \in \{ \emptyset, \{*\} \}$$

Lee and Seshia (2016) [17] notation:

$$A^B \equiv \text{set of all functions from } B \text{ to } A$$

A particular reaction will observe values of inputs at particular time t , and calculate output values for some time t . Suppose actor has input ports $P = \{p_1 \dots p_N\}$. Assume $\forall p \in P$, $V_p \in \mathbf{ObjSets} \equiv$ values that maybe received on port p , when input present. $V_p \equiv$ type of port p . At reaction, $\forall p \in P$, let value of p , $p \in V_p \cup \{ \text{absent} \}$ (Lee and Seshia seems to reuse notation, notation by context, here; but it's troublesome) valuation of inputs P is assignment of value in V_p , \forall variable $p \in P$, or assertion that p absent. I think Lee and Seshia is terribly confused in the writing and lack of clarity is horrendous and pernicious.

32.1. **Discrete Signals.** Assume output is absent at times t where reaction doesn't occur. e.g.

$$Q = \{ \text{count} \}$$

$V_{\text{count}} = \mathbb{Z}$ at reaction, count assigned count of cars in garage. So ports $P = \{p_1 \dots p_N\}$, output ports $Q = \{q_1 \dots q_M\}$, $\forall p \in P$, $V_p \in \mathbf{Set}$ or \mathbb{R} , $\forall q \in Q$, $V_q \in \mathbf{Set}$ or \mathbb{R} , at reaction,

$$y : P \rightarrow V_p \bigcup \{\emptyset\}$$

$$z : Q \rightarrow V_q \bigcup \{\emptyset\}$$

32.2. Finite-State Machines.

32.2.1. *Notion of a State.* cf. Sec. 3.2 The Notion of State, Lee, Seshia (2011) [?] state, state of a system, formally, encoding of everything about past that has an affect on system's reaction to amount and future inputs cf. Sec. 3.3 Finite-State Machines, Lee, Seshia (2011) [?] Finite state machine; assume states $\equiv S \in \mathbf{FiniteSet}$

32.3. **Transitions.** cf. Sec. 3.3.1 Transitions, Lee, Seshia (2011) [?] **transition** - directed edge, \forall transition, label with "guard/action" **guard** - determines whether transition maybe taken on reactions **action** -specifies what outputs are produced on each reaction guard is a **predicate** (boolean valued) s.t. true when transition should be taken. e.g. example 3.4, FSM model

\neg	
\sim	negation, not $\neg A$ true, iff A false
$!$	
\wedge	logical conjunction, and if A and B both true, T , $A \wedge B$ true. Else, it's false.
$\&$	
\vee	
$+$	logical (inclusion) disjunction, or if A or B (or both) true, $A \vee B$ false, if both false, $A \vee B$ false
\parallel	
\oplus	
$\underline{\vee}$	exclusive disjunction, xor $A \oplus B$ true when either A or B , but not both are true.

A	B	$\neg A$	$A \wedge B$	$A \vee B$	$A \oplus B$
T	T	F	T	T	F
T	F	F	F	T	T
F	T	T	F	T	T
F	F	T	F	F	F

action (in reference to Lee, Seshia (2011)), on a transition, action specifies result-ing valuation on output ports when transition is taken. "When assigning a value to an output port, we use notation name := value to distinguish assignment from a predicate, which would be written name = value. Let

ports $P_{\text{in}} = \{p_1^{(\text{in})}, \dots p_{N_{\text{in}}}^{(\text{in})}\}$

output ports $P_{\text{out}} = \{p_1^{(\text{out})}, \dots p_{N_{\text{out}}}^{(\text{out})}\}$

At a reaction,

$$\begin{aligned}
 i &: P_{\text{in}} \rightarrow V_{P_{\text{in}}} \cup \{\emptyset\} \\
 i &: p \mapsto i(p) \\
 o &: P_{\text{out}} \rightarrow V_{P_{\text{out}}} \cup \{\emptyset\} \\
 o &: p \mapsto o(p)
 \end{aligned}$$

Given

$$i(p_1^{(\text{in})}, \dots p_{N_{\text{in}}}^{(\text{in})}) \in (V_{P_{\text{in}}} \cup \{\emptyset\})^{P_{\text{in}}} \times S$$

where $S \equiv$ states of the system,

guard, g_{guard} ,

$$\begin{aligned}
 g_{\text{guard}} &: (V_{P_{\text{in}}} \cup \{\emptyset\})^{P_{\text{in}}} \times S \rightarrow \mathbb{Z}_2 = \{\text{true}, \text{false}\} \\
 g_{\text{guard}} &(p_1^{(\text{in})}, \dots p_{N_{\text{in}}}^{(\text{in})}) \in \{\text{true}, \text{false}\}
 \end{aligned}$$

action a ,

$$a : g_{\text{guard}}, e \mapsto o(p_1^{(\text{out})}, \dots p_{N_{\text{out}}}^{(\text{out})})$$

where $e \in E$, $E \equiv$ set of all possible transitions. cf. Sec. 3.3.3. Update Functions of Lee, Seshia (2011) (States, Inputs, Outputs, update, initial State) $\equiv (S, I, O, u, s_0)$, where $u : S \times I \rightarrow S \times O$, and update \equiv transition function (synonymous)

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