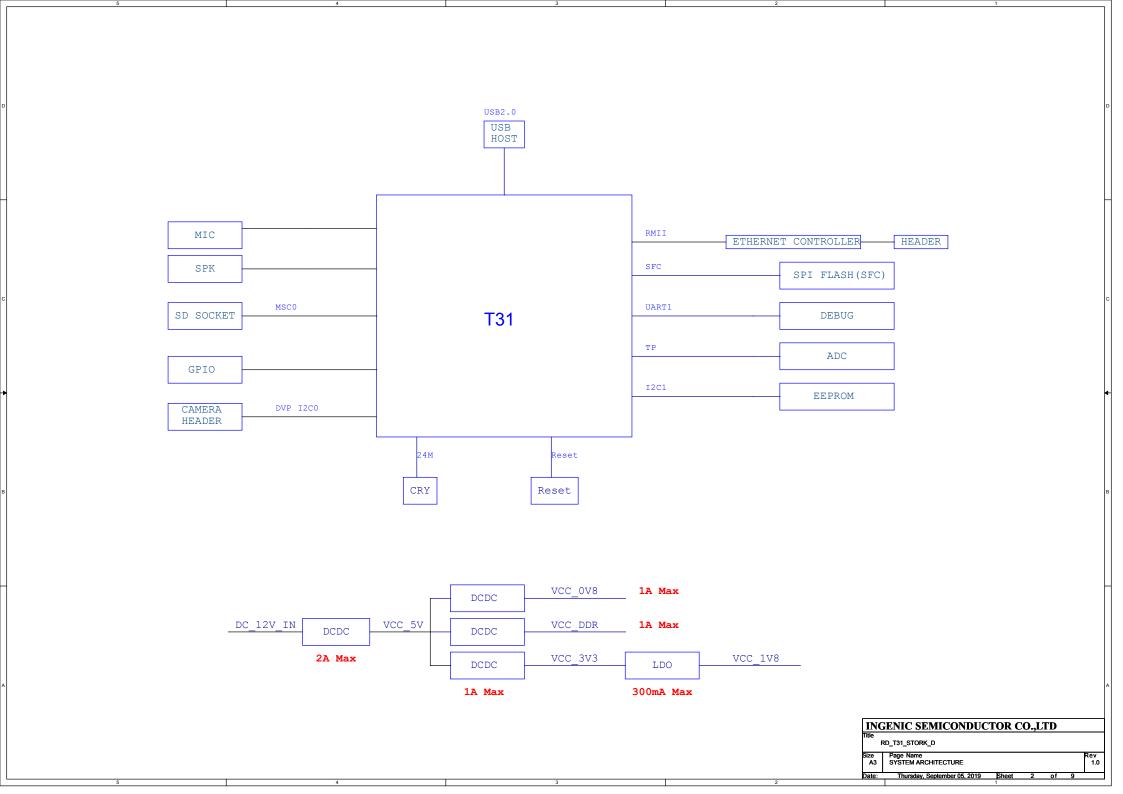


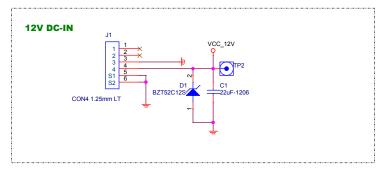
RD_T31_STORK_D Schematic Revision 1.0

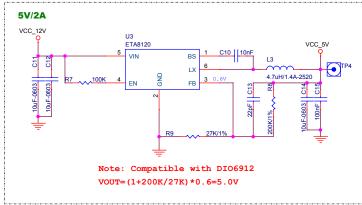
Title	Page
COVER SHEET	1
SYSTEM ARCHITECTURE	2
POWER_SUPPLY	3
T31_POWER	4
T31_IO/NOR FLASH/EEPROM	5
DVP_CONNECTOR/SD_CARD	6
ETH/USB/AUDIO	7
RST/BOOT/DEBUG	8
REVISION HISTORY	9

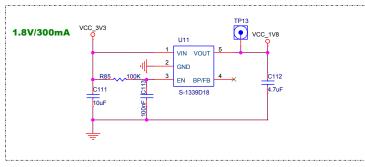
INGENIC SEMICONDUCTOR CO.,LTD						
Title	RD_T31_STORK_D					
Size A3	Page Name COVER SHEET					Rev 1.0
Date:	Thursday, September 05, 2019	Sheet	1	of	9	

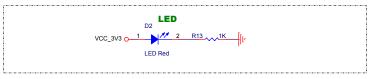


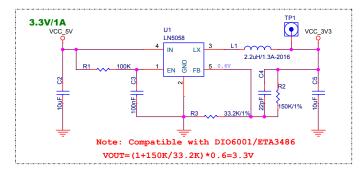
POWER SUPPLY

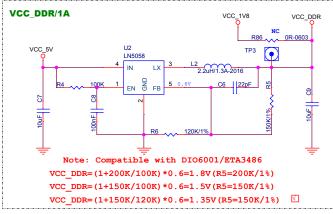


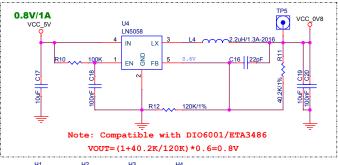


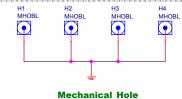






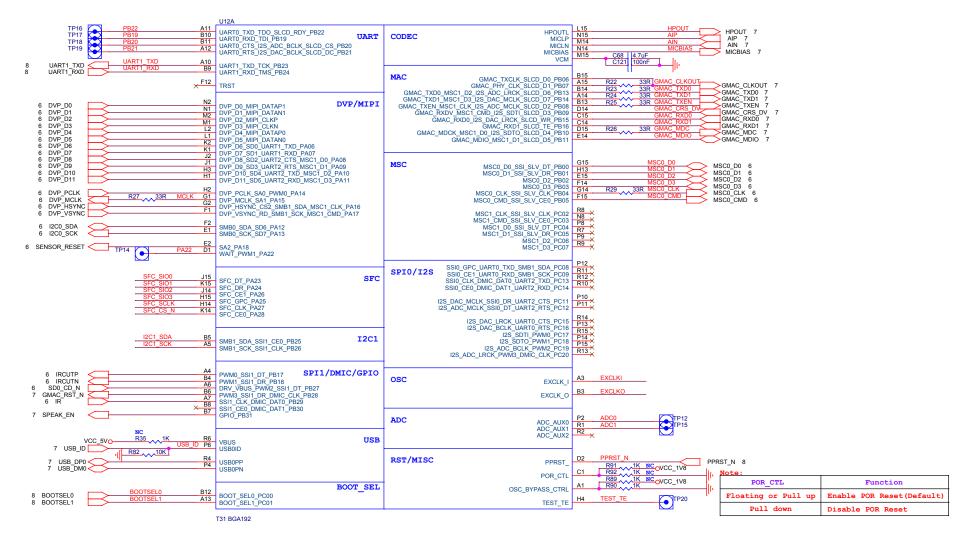


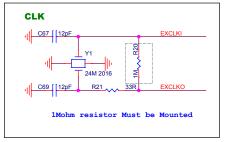


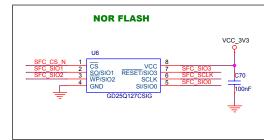


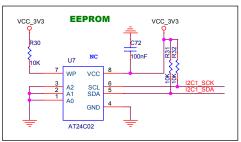
T31-POWER U12B R14 0R-0603 VCC_DDR O-VDDMEM_ VSS_2 VSS_3 VDDMEM_2 VDDMEM_3 VSS_3 F8 VSS_4 F9 VSS_5 F10 VSS_6 F10 VSS_6 G5 VSS_9 G7 VSS_10 G7 VSS_11 G9 VSS_12 G11 VSS_13 G11 VSS_14 F10 VSS_14 F10 VSS_15 F16 VSS_16 F16 VSS_17 F16 VSS_17 F17 VSS_18 F17 V VDDMEM_4 VDDMEM_5 VDDMEM_6 VDDMEM D11 DDRVDD DDRVDD . DDRVDD_2 E6 E7 E8 DDRVDD : DDRVDD_4 DDRVDD_5 E9 E10 E11 DDRVDD_7 DDRVDD_8 A8 A9 VCC_1V8 DDRPLL VCCA VCC_0V8 O DDRPLL_VCCD DRPLL, J4 J12 VDD_1 K6 VDD_2 K6 VDD_3 K6 VDD_3 K6 VDD_5 K6 VDD_6 L6 VDD_6 L6 VDD_7 VDD_9 L7 VDD_1 L8 VDD_1 L9 VDD_1 MB VDD_11 MB VDD_11 MB VDD_18 MB VDD_19 MB VDD_18 MB VDD_18 MB VDD_18 MB VDD_18 MB VDD_19 MB VDD_19 MB VDD_19 MB VDD_18 MB VDD_19 VDD_19 MB VDD_19 VDD_1 R15 0R-0603 VDD VCC_0V8 VDDMEM DDRPLL_VSSA M11 C21 VDD_20 B2 PLL_VSS FB12 / 1000ohm/250mA FB3 / 1000ohm/250mA VCC_0V8 (VCC_1V8 (PLL VDD DDR_VREF B1 M5 PLL_VDDHV ADC_AVSS СЗ 4.7uF R18 ~~ M12 VCC 1V8 C23 VDDI00 CODEC_AVSS C13 N13 A2 100nF VCC_3V3 VDDIO1 VSSIO_OSC VDDIO2 VCC_1V8 VDDIO_OSC USB AVSS M4 VCC_1V8 C ADC_AVDD MIPI_AVSS ADC_VREF FB6 1000ohm/250mA CODEC_AVDD L14 VCC_1V8 O-CODEC_AVDD G4 EFUSE_AVD TP6 P7 R5 P5 EFUSE_AVD VCC 0V8 USB_AVD08 USB_AVD18 VCC_3V3 D4 DDR_VREF R250 0R USB_AVD33 DDRVREF VCC_0V8 OVCC_1V8 OVCC_1V8 OVCC_1V8 N3 MIPI_AVD08 MIPI_AVD18 E12 ZQ R83 240R/1% DDR_ZQ T31 BGA192 DDRPLL_AVD18 VDDMEM C3 C32 10uF C104 100nF 100nF 100nF 100nF PLL_VDDHV VDDIO_D VDDIO_OSC SADC_AVDD CODEC_AVDD VDDIO2 10uF C53 C54 100nF C120 USB_AVD18 MIPI_AVD08 USB_AVD33 MIPI_AVD18 10uF C51 100nF C52 10uF C57 C20 9 10uF 100rF 100nF INGENIC SEMICONDUCTOR CO.,LTD RD_T31_STORK_D Page Name T31_POWER А3 1.0

T31_IO/NOR FLASH/EEPROM



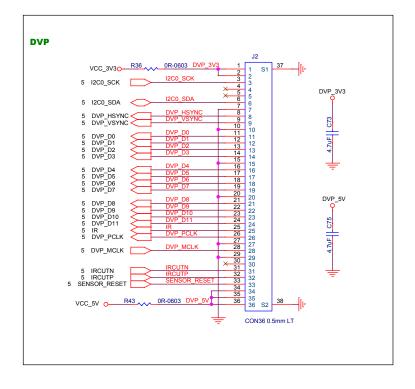


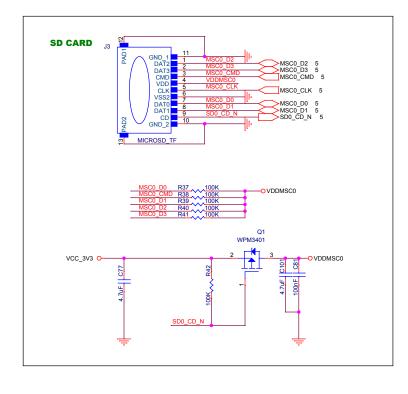




INGENIC SEMICONDUCTOR CO.,LTD						
Title	RD_T31_STORK_D					
Size A3	Page Name T31_IO/NOR FLASH/EEPROM					Rev 1.0
Date:	Thursday, September 05, 2019	Sheet	5	of	9	

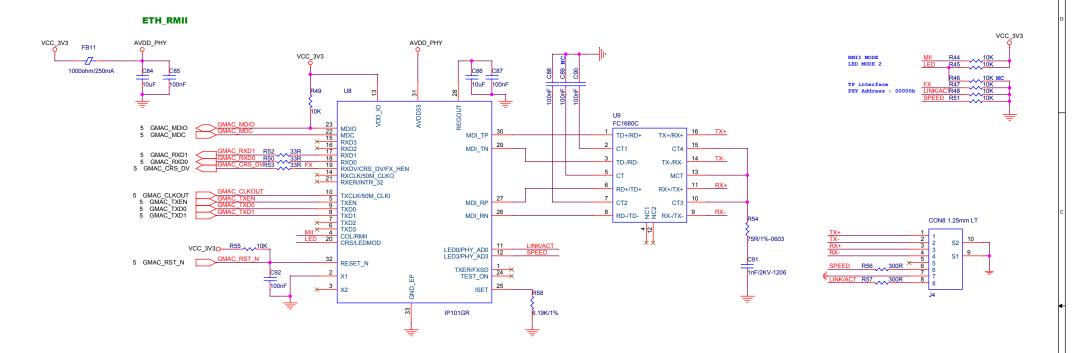
DVP_CONNECTOR/SD_CARD

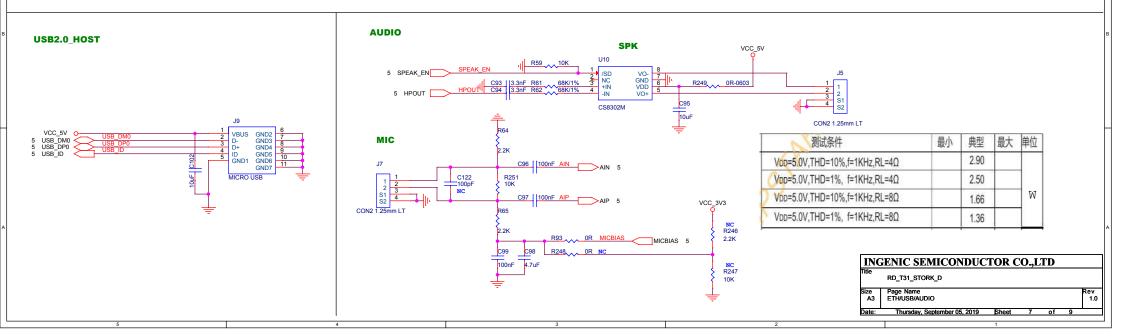




INGENIC SEMICONDUCTOR CO.,LTD						
Title	RD_T31_STORK_D					
Size A3	Page Name DVP_CONNECTOR/SD_CARD					Rev 1.0
Date:	Thursday, September 05, 2019	Sheet	6	of	9	

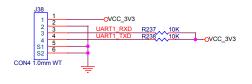
ETH RMII/USB/AUDIO





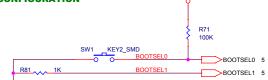






UART1_TXD UART1_TXD [6]
UART1_RXD UART1_RXD [6]

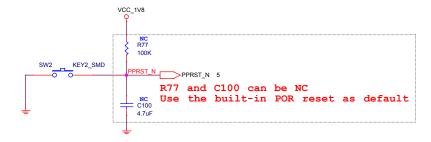
BOOT CONFIGURATION



VCC_3V3

T_SEL1	BOOT_SEL0	BOOT FROM
0	0	SD card : MSCO
Λ	1	epc . eptn/eet cpn

RESET CIRCUITS



INGENIC SEMICONDUCTOR CO.,LTD

RD_T31_STORK_D

Size Page Name A3 RST/BOOT/DEBUG

ito. Tharoady, coptombor 60, 2010

Date	Revision	Change
Sep 05 2019	Rev1.0	1. First Revision

INC	GENIC SEMICONDUC	TOR C	:O.,L	TD		
Title	RD_T31_STORK_D					
Size A3	Page Name REVISION HISTORY					Rev 1.
Date:	Thursday, September 05, 2019	Sheet	9	of	9	