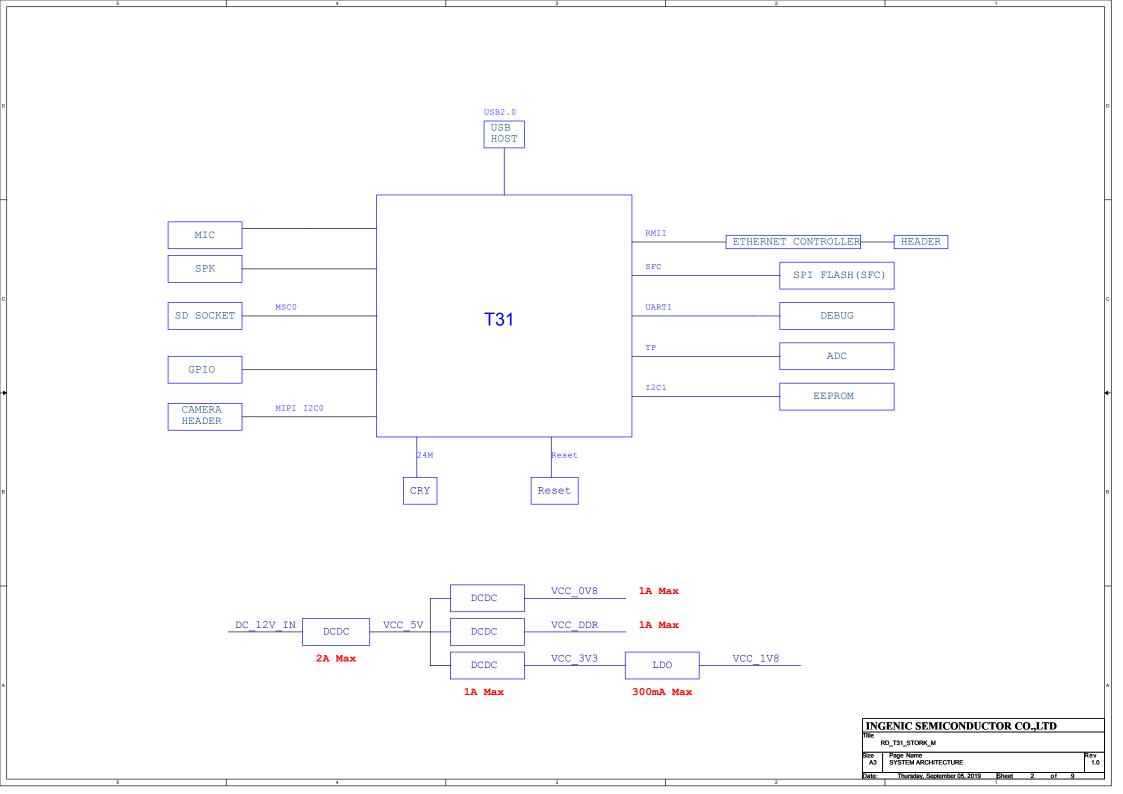


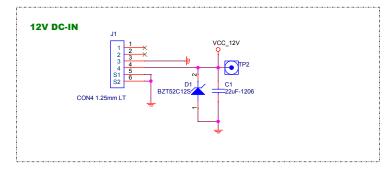
# RD\_T31\_STORK\_M Schematic Revision 1.0

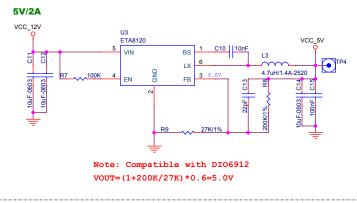
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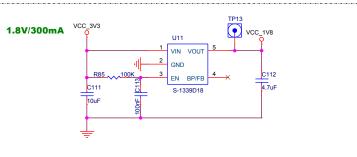
INGENIC SEMICONDUCTOR CO.,LTD						
Title	RD_T31_STORK_M					
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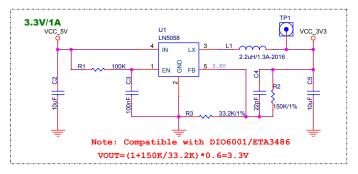
### **POWER SUPPLY**

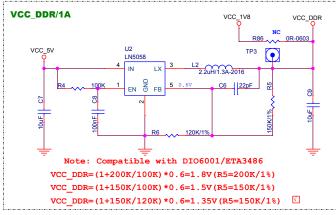


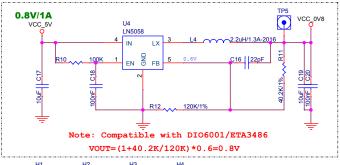


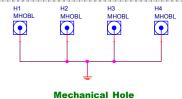






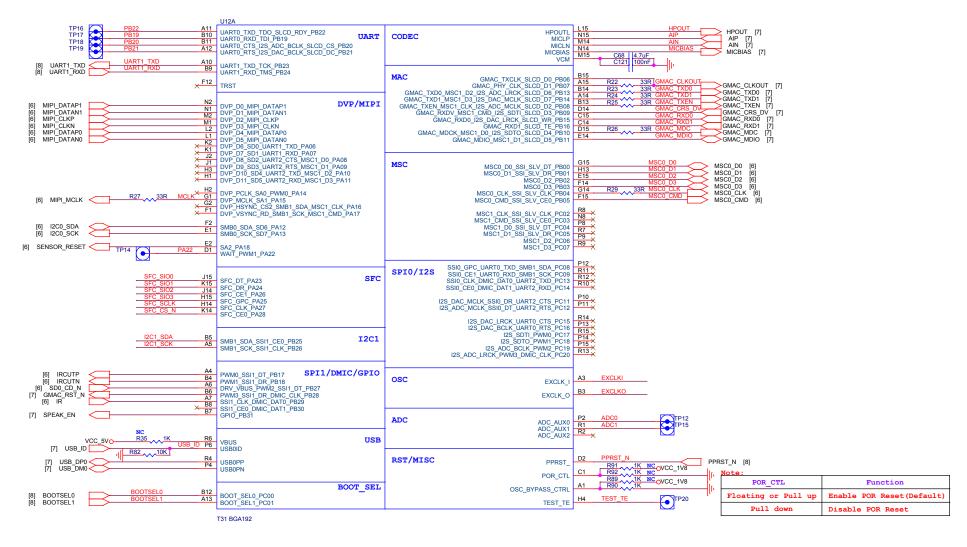


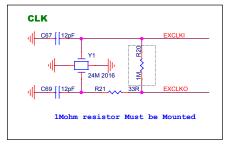


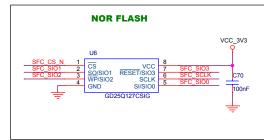


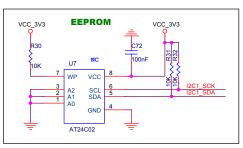
#### T31-POWER U12B R14 0R-0603 VCC\_DDR O-VDDMEM\_ VSS\_2 VSS\_3 VDDMEM\_2 VDDMEM\_3 VSS\_3 F8 VSS\_4 F9 VSS\_5 F10 VSS\_6 F10 VSS\_6 G5 VSS\_9 G7 VSS\_10 G7 VSS\_11 G9 VSS\_12 G11 VSS\_13 G11 VSS\_14 F10 VSS\_14 F10 VSS\_15 F16 VSS\_16 F16 VSS\_17 F16 VSS\_17 F17 VSS\_18 F17 V VDDMEM\_4 VDDMEM\_5 VDDMEM\_6 VDDMEM D11 DDRVDD DDRVDD . DDRVDD\_2 E6 E7 E8 DDRVDD : DDRVDD\_4 DDRVDD\_5 E9 E10 E11 DDRVDD\_7 DDRVDD\_8 A8 A9 VCC\_1V8 DDRPLL VCCA VCC\_0V8 O DDRPLL\_VCCD DRPLL, J4 J12 VDD\_1 K6 VDD\_2 K6 VDD\_3 K6 VDD\_3 K6 VDD\_5 K6 VDD\_6 L6 VDD\_6 L6 VDD\_7 VDD\_9 L7 VDD\_1 L8 VDD\_1 L9 VDD\_1 MB VDD\_11 MB VDD\_11 MB VDD\_18 MB VDD\_19 MB VDD\_18 MB VDD\_18 MB VDD\_18 MB VDD\_18 MB VDD\_19 MB VDD\_19 MB VDD\_19 MB VDD\_18 MB VDD\_19 VDD\_19 MB VDD\_19 VDD\_1 R15 0R-0603 VDD VCC\_0V8 VDDMEM DDRPLL\_VSSA M11 C21 VDD\_20 B2 PLL\_VSS FB12 / 1000ohm/250mA FB3 / 1000ohm/250mA VCC\_0V8 ( VCC\_1V8 ( PLL VDD DDR\_VREF B1 M5 PLL\_VDDHV ADC\_AVSS СЗ 4.7uF R18 ~~ M12 VCC 1V8 C23 VDDI00 CODEC\_AVSS C13 N13 A2 100nF VCC\_3V3 VDDIO1 VSSIO\_OSC VDDIO2 VCC\_1V8 VDDIO\_OSC USB AVSS FB7 / 1000ohm/250mA M4 VCC\_1V8 C ADC\_AVDD MIPI\_AVSS ADC\_VREF FB6 1000ohm/250mA CODEC\_AVDD L14 VCC\_1V8 O-CODEC\_AVDD G4 EFUSE\_AVD TP6 P7 R5 P5 EFUSE\_AVD VCC 0V8 USB\_AVD08 USB\_AVD18 VCC\_3V3 D4 DDR\_VREF USB\_AVD33 DDRVREF R250 0R VCC\_0V8 OVCC\_1V8 OVCC\_1V8 OVCC\_1V8 N3 MIPI\_AVD08 MIPI\_AVD18 E12 ZQ R83 240R/1% DDR\_ZQ T31 BGA192 DDRPLL\_AVD18 VDDMEM C3 C32 10uF C104 100nF 100nF 100nF 100nF PLL\_VDDHV VDDIO\_D VDDIO\_OSC SADC\_AVDD CODEC\_AVDD VDDIO2 10uF C53 C54 100nF C120 USB\_AVD18 USB\_AVD33 MIPI\_AVD08 MIPI\_AVD18 10uF C51 100nF C52 10uF C57 C20 9 10uF 100rF 100nF INGENIC SEMICONDUCTOR CO.,LTD RD\_T31\_STORK\_M Page Name T31\_POWER А3 1.0

## T31\_IO/NOR FLASH/EEPROM



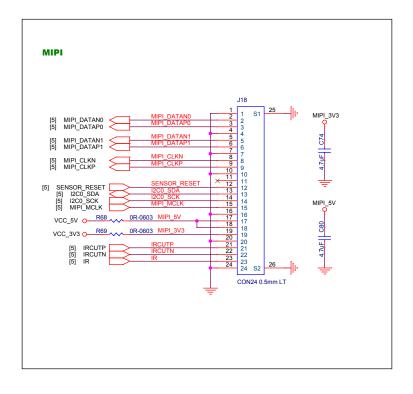


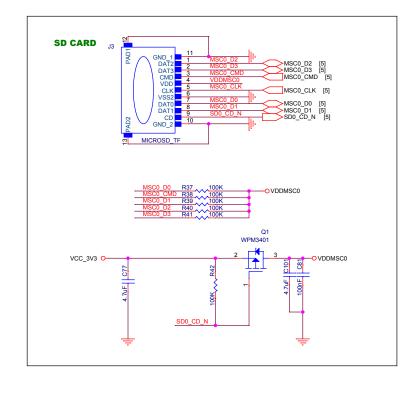




INC	INGENIC SEMICONDUCTOR CO.,LTD							
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А3	T31_IO/NOR FLASH/EEPROM					1.0		
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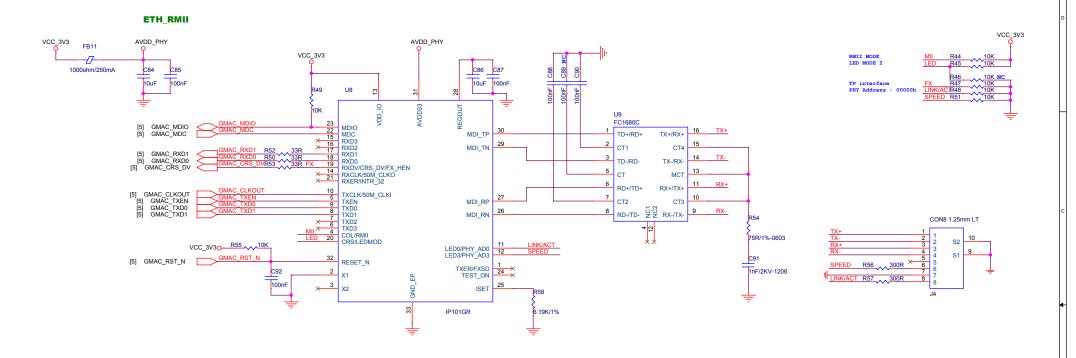
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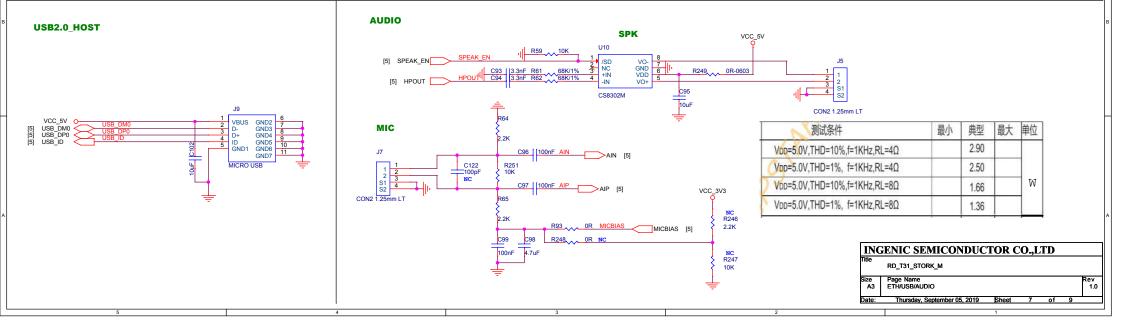




	INGENIC SEMICONDUCTOR CO.,LTD								
Title	RD_T31_STORK_M								
Size A3	Page Name MIPI_CONNECTOR/SD_CARD					Rev 1.0			
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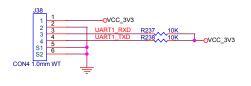
## ETH RMII/USB/AUDIO





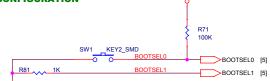






UART1_TXD	$\overline{}$	HADT4 TVD	ren
UART1_RXD	<u> </u>	UART1_TXD	
	<u> </u>	UART1 RXD	[5]

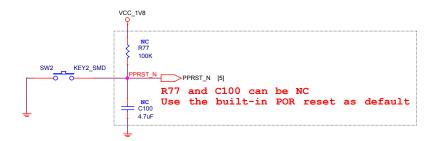
#### **BOOT CONFIGURATION**



VCC\_3V3

OT_SEL1	BOOT_SEL0	BOOT FROM
0	0	SD card : MSCO

#### RESET CIRCUITS



INGENIC SEMICONDUCTOR CO.,LTD RD\_T31\_STORK\_M

Size Page Name A3 RST/BOOT/DEBUG

Date	Revision	Change
Sep 05 2019	Rev1.0	1. First Revision

INC	INGENIC SEMICONDUCTOR CO.,LTD							
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