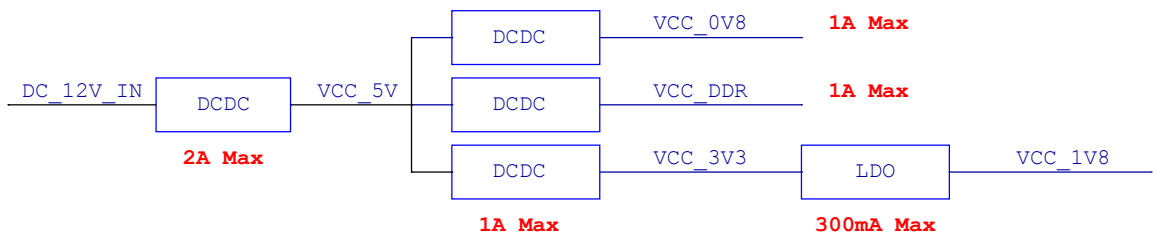
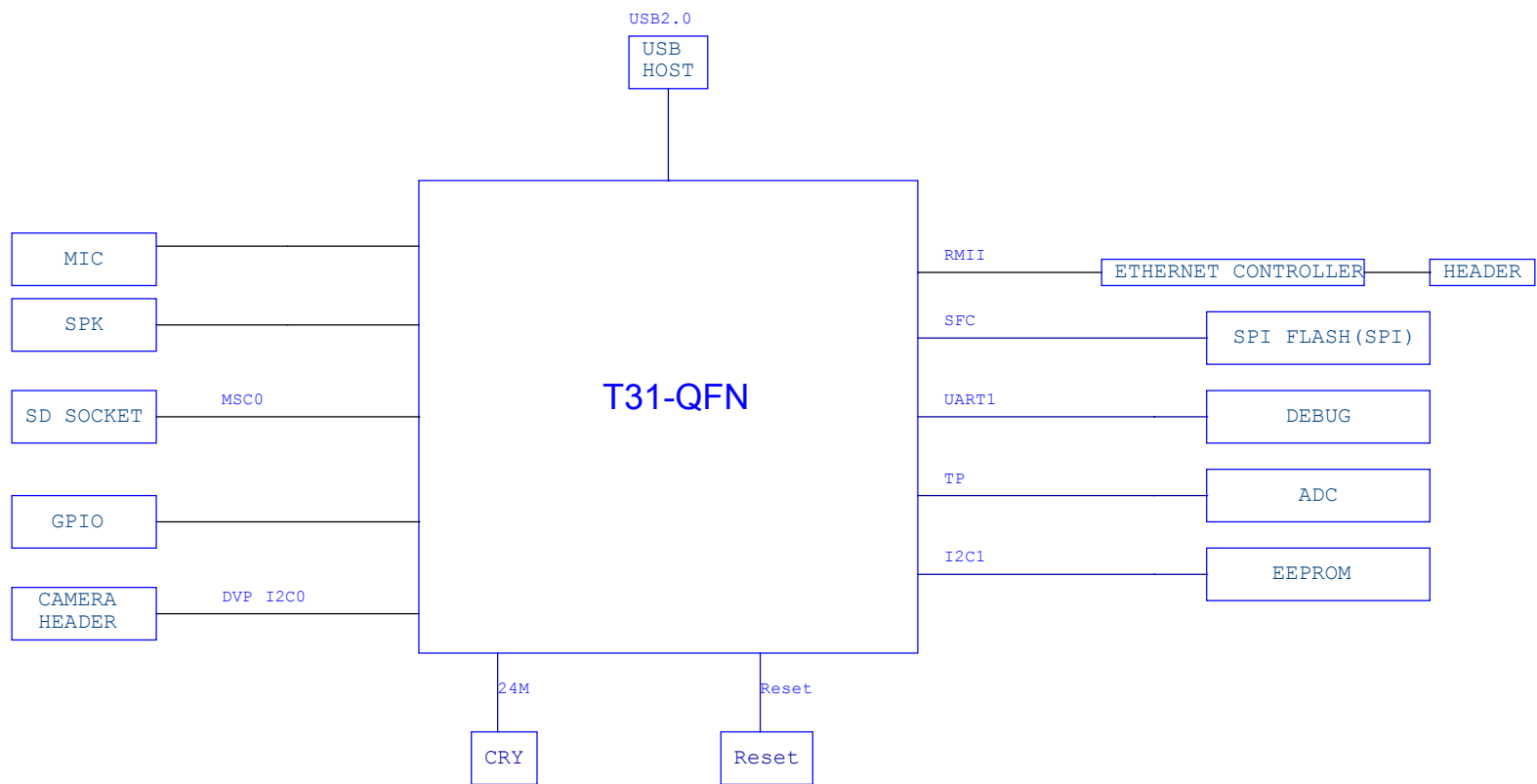




RD_T31_SNIPE_D

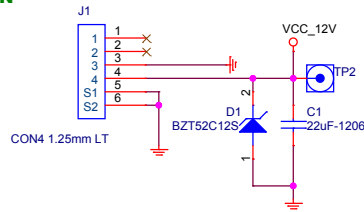
Schematic Revision 1.0

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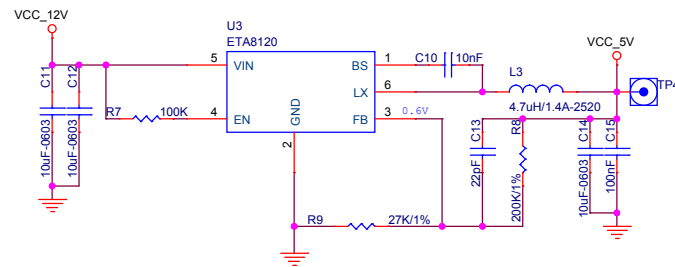


POWER SUPPLY

12V DC-IN

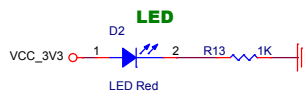
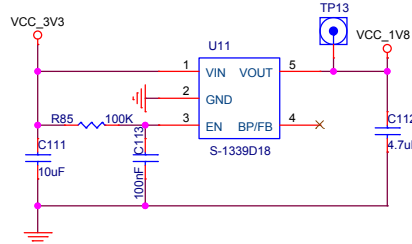


5V/2A

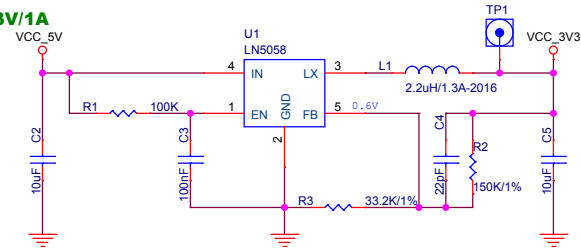


Note: Compatible with DIO6912
 $V_{OUT} = (1 + 200K/27K) * 0.6 = 5.0V$

1.8V/300mA

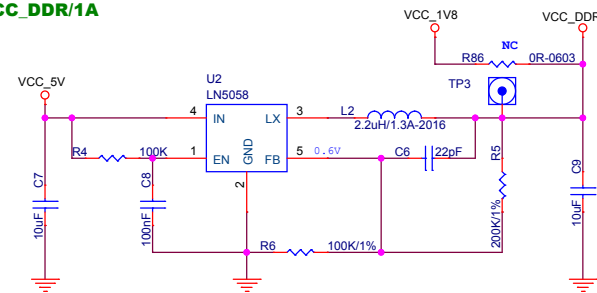


3.3V/1A



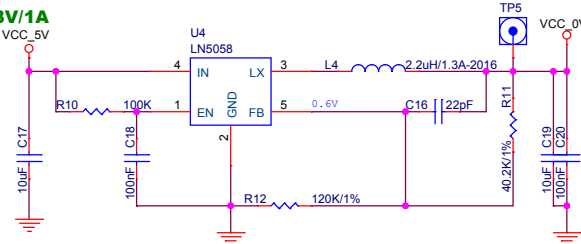
Note: Compatible with DIO6001/ETA3486
 $V_{OUT} = (1 + 150K/33.2K) * 0.6 = 3.3V$

VCC_DDR/1A

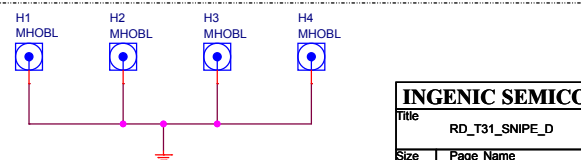


Note: Compatible with DIO6001/ETA3486
 $V_{CC_DDR} = (1 + 200K/100K) * 0.6 = 1.8V$ (R5=200K/1%)
 $V_{CC_DDR} = (1 + 150K/100K) * 0.6 = 1.5V$ (R5=150K/1%)
 $V_{CC_DDR} = (1 + 150K/120K) * 0.6 = 1.35V$ (R5=150K/1%)

0.8V/1A



Note: Compatible with DIO6001/ETA3486
 $V_{OUT} = (1 + 40.2K/120K) * 0.6 = 0.8V$

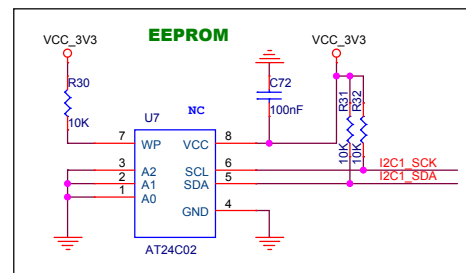
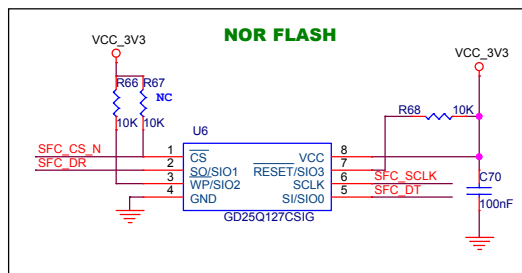
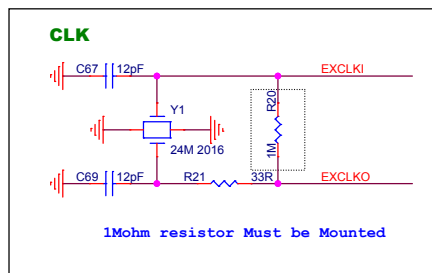
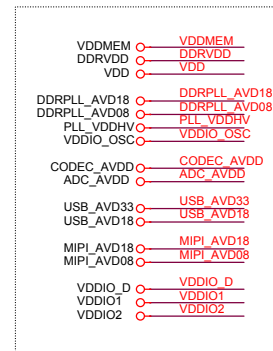
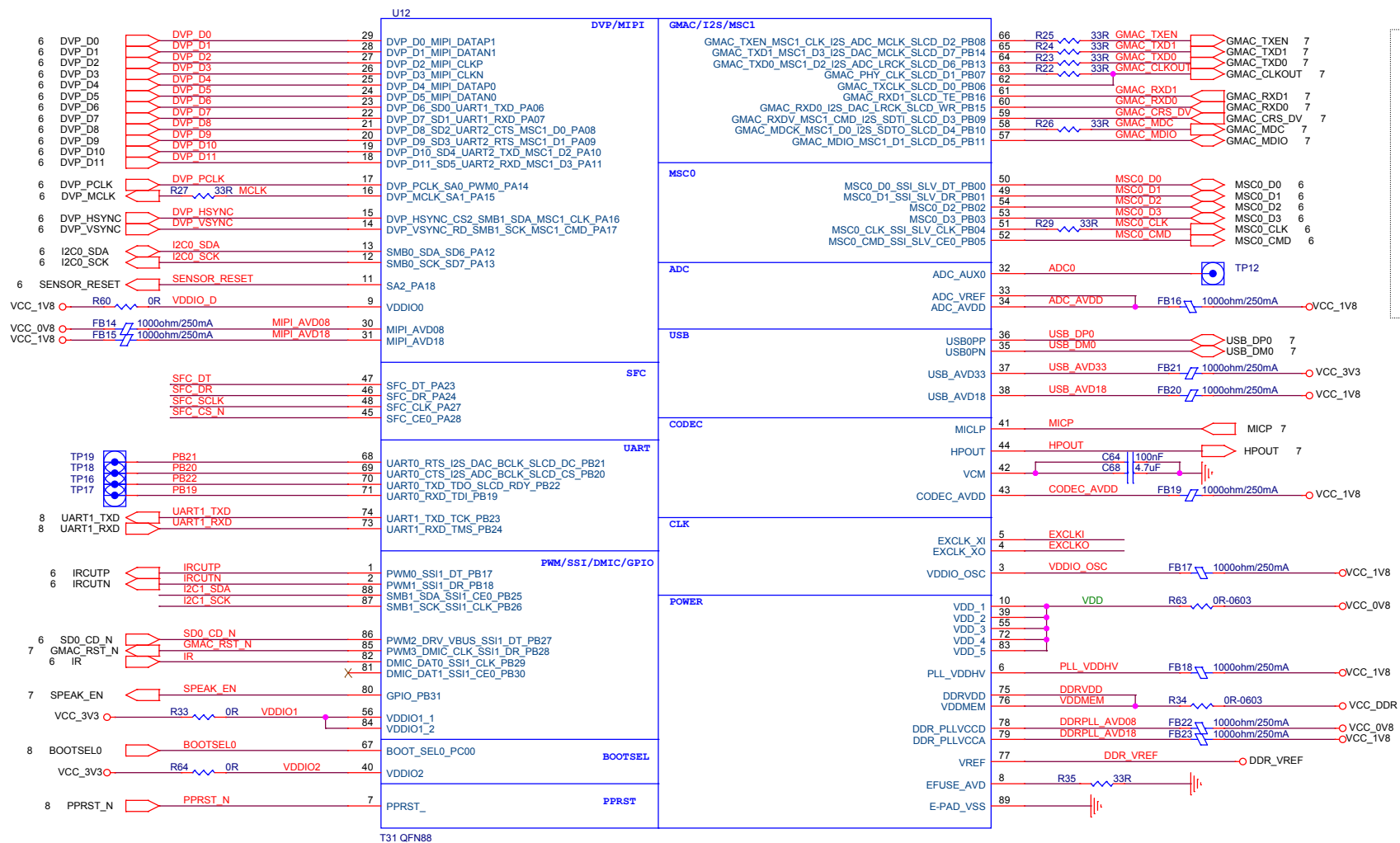


Mechanical Hole

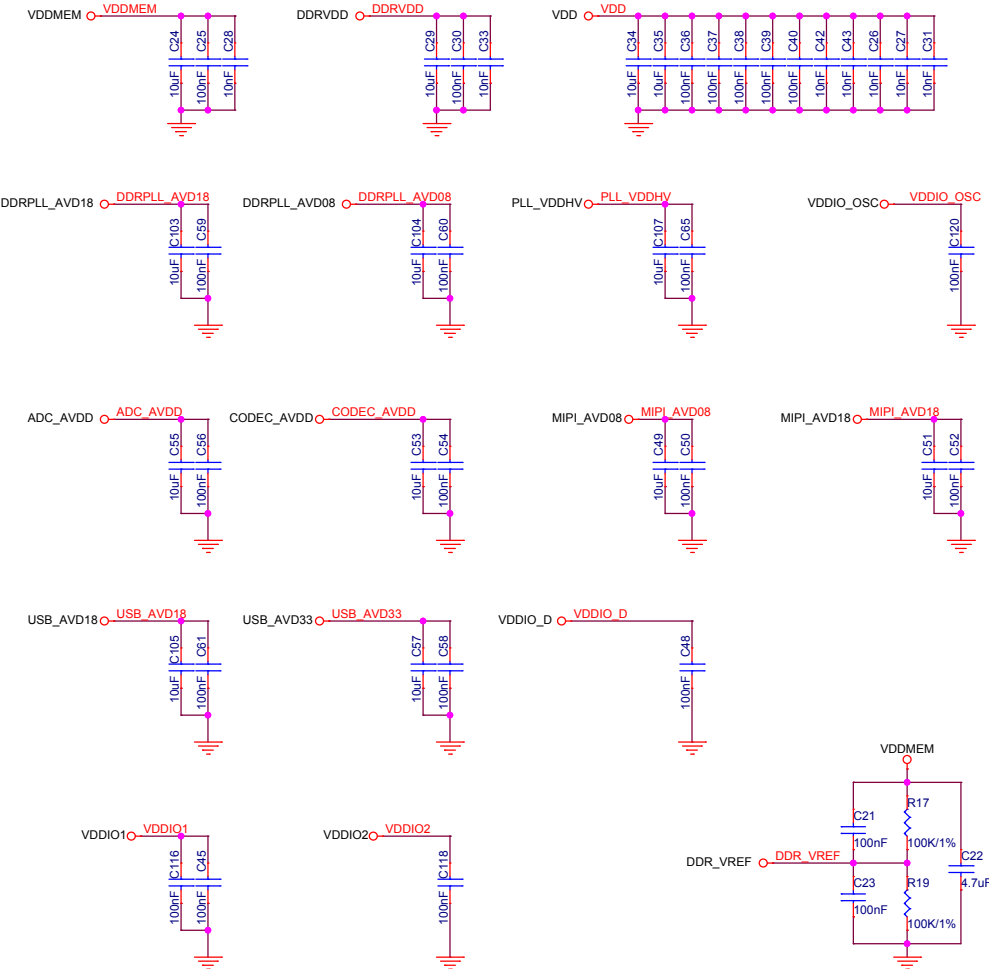
INGENIC SEMICONDUCTOR CO.,LTD

Title		RD_T31_SNIPE_D
Size	Page Name	POWER_SUPPLY
A3		Rev 1.0
Date:	Thursday, September 05, 2019	Sheet 3 of 9

T31_IO/NOR FLASH/EEPROM

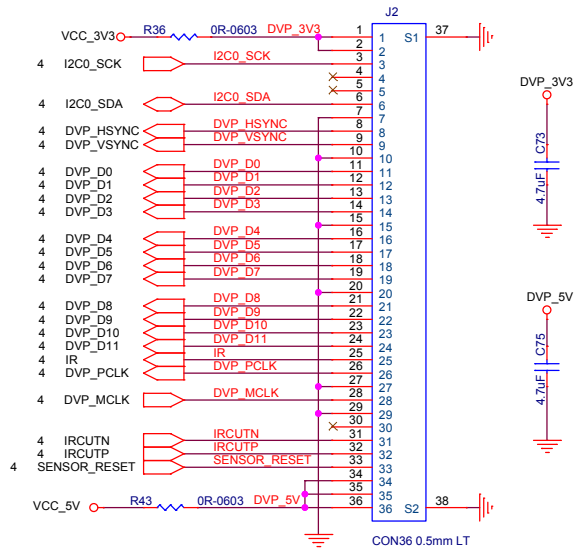


T31_Filter-Capacitor

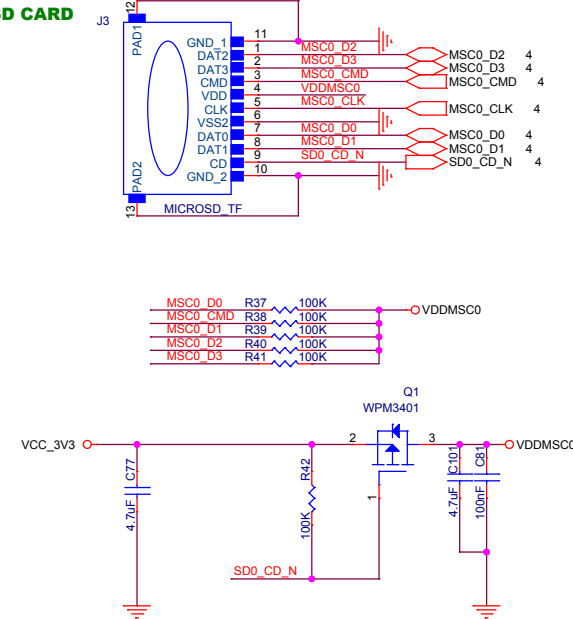


DVP_CONNECTOR/SD_CARD

DVP

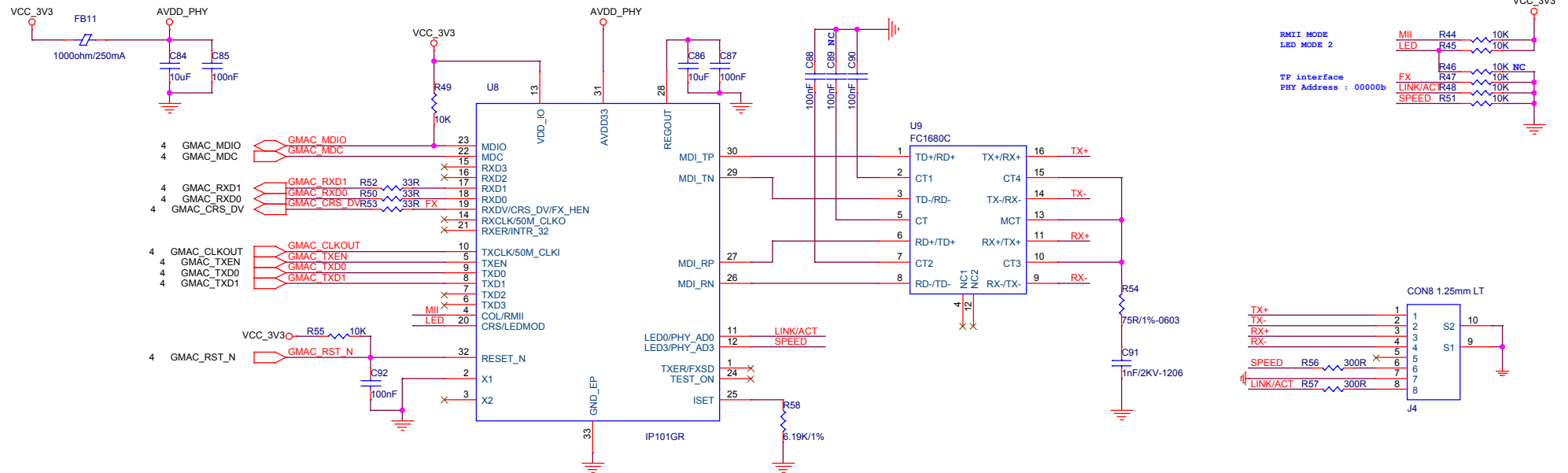


SD CARD



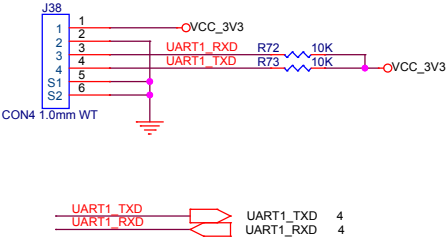
ETH_RMII/USB/AUDIO

ETH_RMII

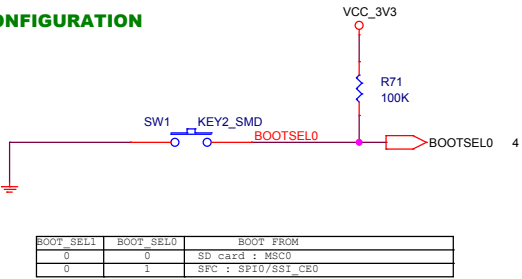


RST/BOOT/DEBUG

DEBUG



BOOT CONFIGURATION



RESET CIRCUITS

