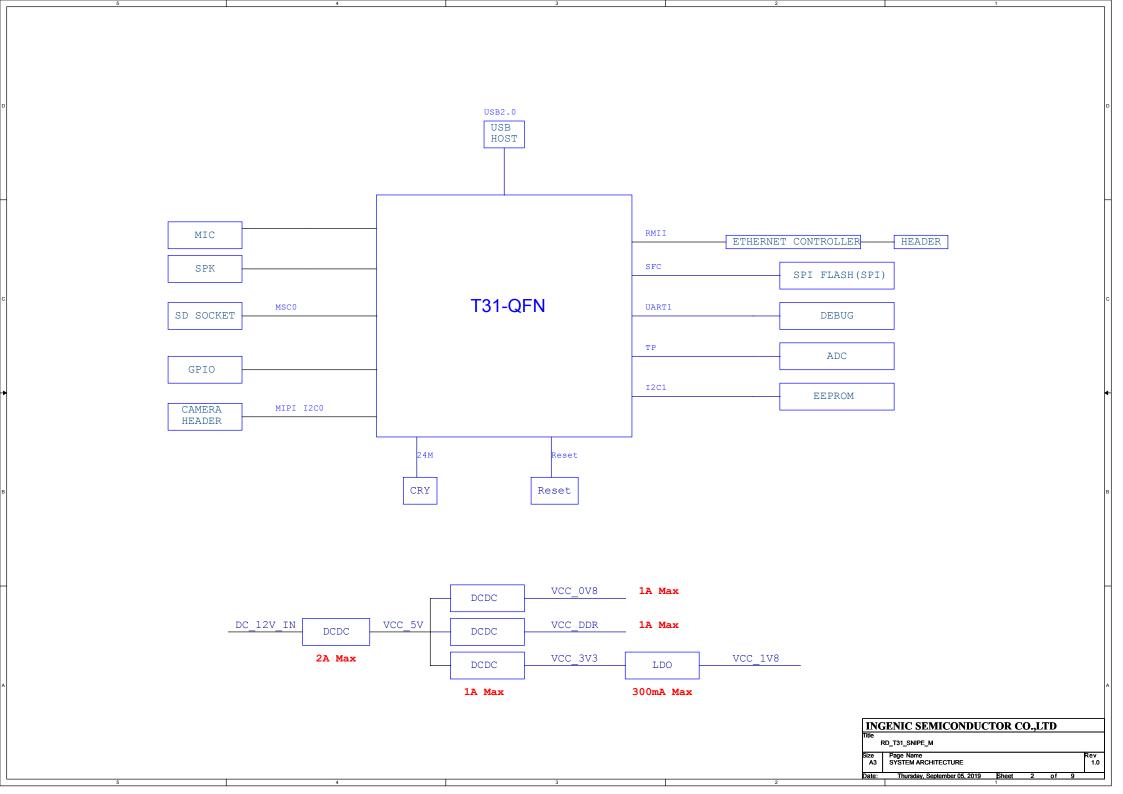


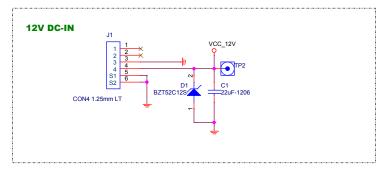
# RD\_T31\_SNIPE\_M Schematic Revision 1.0

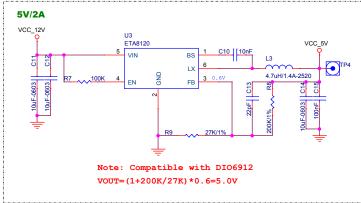
Title	Page
COVER SHEET	1
SYSTEM ARCHITECTURE	2
POWER_SUPPLY	3
T31_IO/NOR FLASH/EEPROM	4
T31_Filter-Capacitor	5
MIPI_CONNECTOR/SD_CARD	6
ETH/USB/AUDIO	7
RST/BOOT/DEBUG	8
REVISION HISTORY	9

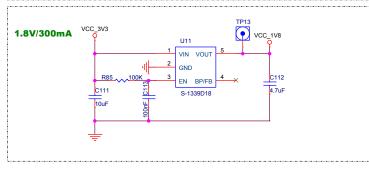
INC	GENIC SEMICONDUC	TOR C	:O.,L	TD		
Title	RD_T31_SNIPE_M					
Size A3	Page Name COVER SHEET					Rev 1.0
Date:	Thursday, September 05, 2019	Sheet	1	of	9	



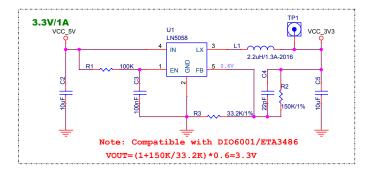
### **POWER SUPPLY**

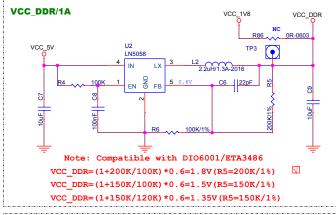


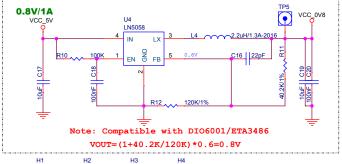


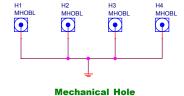




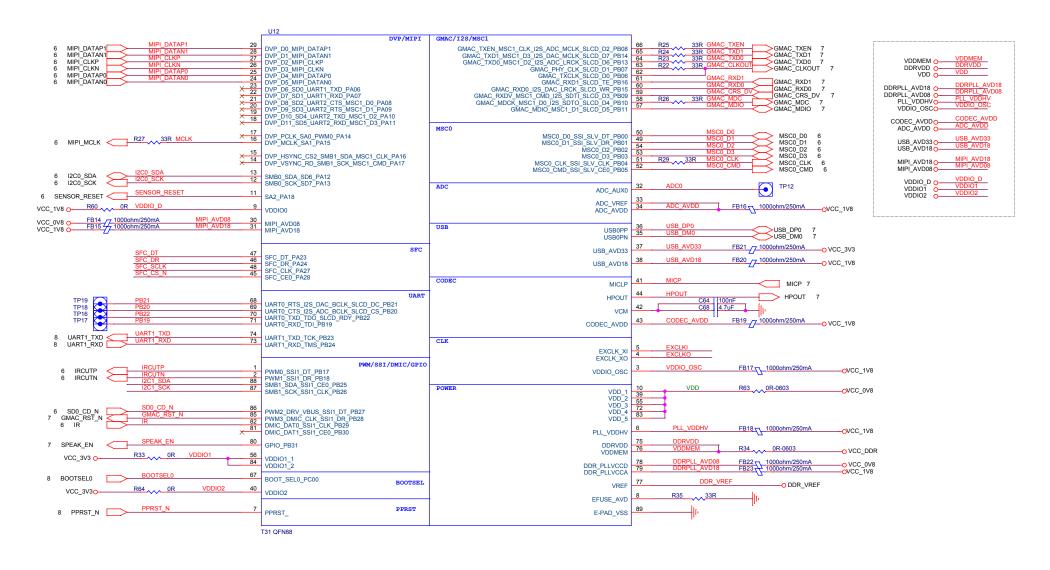


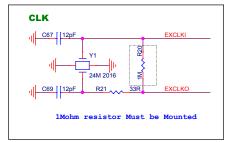


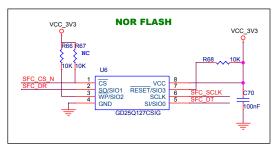


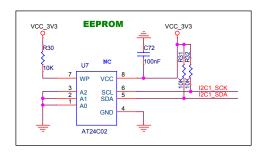


# T31\_IO/NOR FLASH/EEPROM



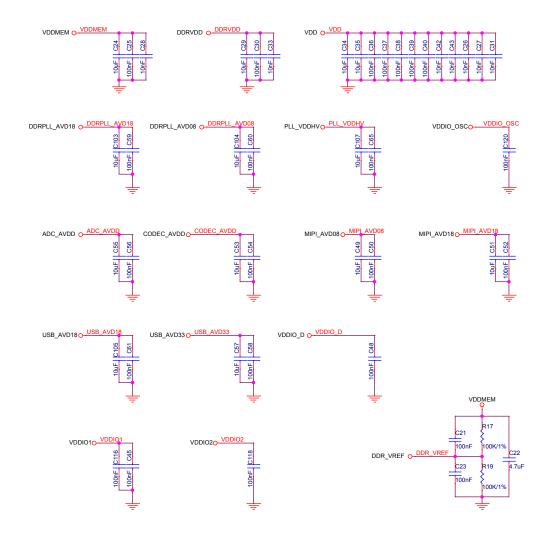






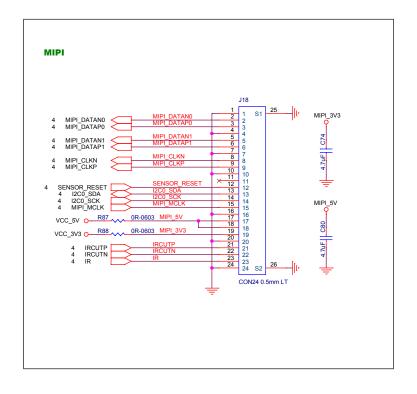
INGENIC SEMICONDUCTOR CO.,LTD								
Title	RD_T31_SNIPE_M							
Size A3	Page Name T31_IO/NOR FLASH/EEPROM					Rev 1.0		
Date:	Thursday, September 05, 2019	Sheet	4	of	9			

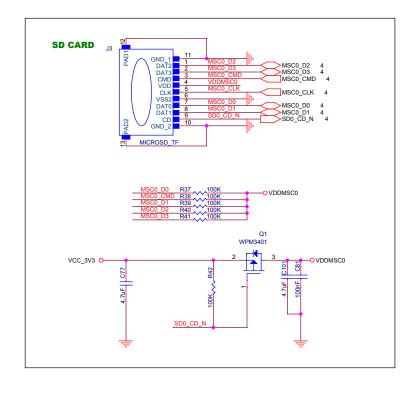
# **T31\_Filter-Capacitor**



INGENIC SEMICONDUCTOR CO.,LTD							
Title	RD_T31_SNIPE_M						
Size A3	Page Name T31_Filter-Capacitor					Rev 1.0	
Date:	Thursday, September 05, 2019	Sheet	5	of	9		

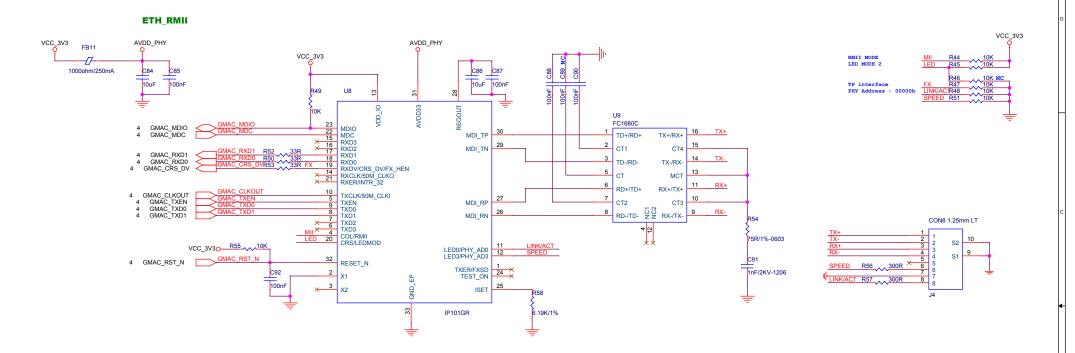
# MIPI\_CONNECTOR/SD\_CARD

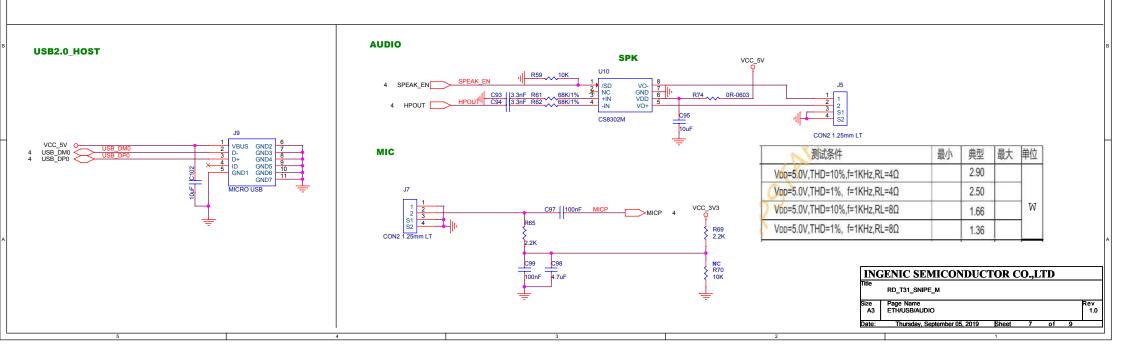




INGENIC SEMICONDUCTOR CO.,LTD							
Title	RD_T31_SNIPE_M						
Size A3	Page Name MIPI_CONNECTOR/SD_CARD					Rev 1.0	
Date:	Thursday, September 05, 2019	Sheet	6	of	9		

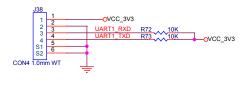
## ETH RMII/USB/AUDIO



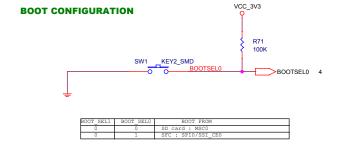




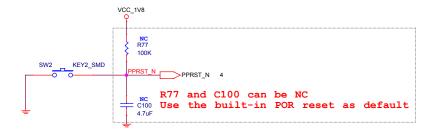




UART1\_TXD 4 UART1\_RXD 4



#### RESET CIRCUITS



Date	Revision	Change
Sep 05 2019	Rev1.0	1. First Revision
	1	

INC	INGENIC SEMICONDUCTOR CO.,LTD								
Title	Title RD_T31_SNIPE_M								
Size A3	Page Name REVISION HISTORY					Rev 1.0			
Date:	Thursday, September 05, 2019	Sheet	9	of	9				