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Lab 7 Prelab

1. **Writing to TCNT1**
out TCNT1H, r25
out TCNT1L, r24
2. **Reading from TCNT1**
in r24, TCNT1L
in r25, TCNT1H
3. If the Timer/Counter0 is in normal mode with no prescaling and 128 (0xF0) has just been written to TCNT0, it will take $(255 + 1 - 128 = 128)$ 128 cycles before TOV0 is set. This will take $128/16 \text{ Mhz} = 8 \mu\text{s}$.