







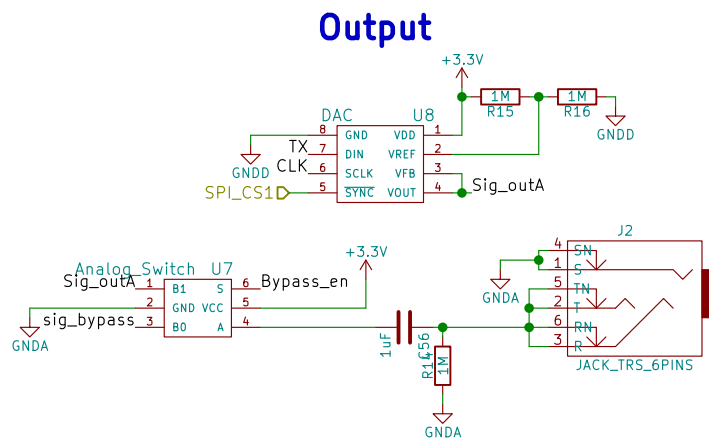
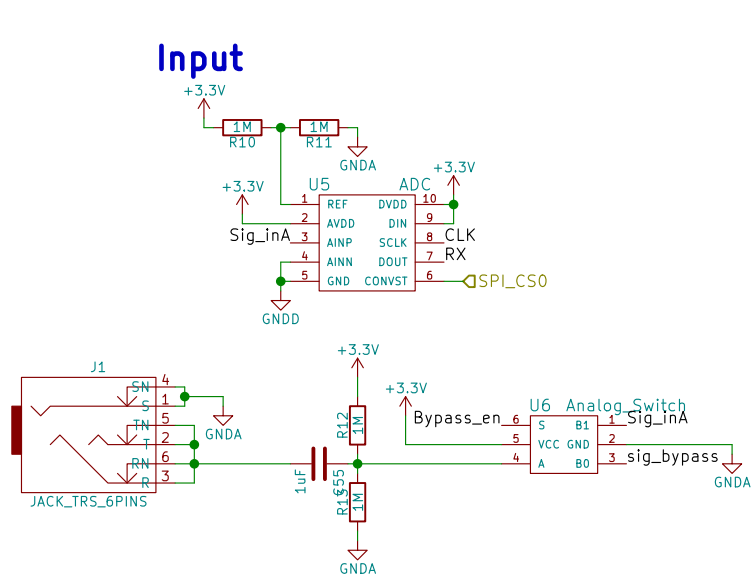




Bypass\_en  Bypass\_en  
 SPI\_RX  RX  
 SPI\_TX  TX  
 SPI\_CLK  CLK  
  
 SPI\_CS2   One of these may be used for flash memory  
 SPI\_CS3  



Sheet: /Peripherals/		
File: Peripherals.sch		
<b>Title:</b>		
Size: A4	Date: 2017-02-23	Rev: 1.1
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