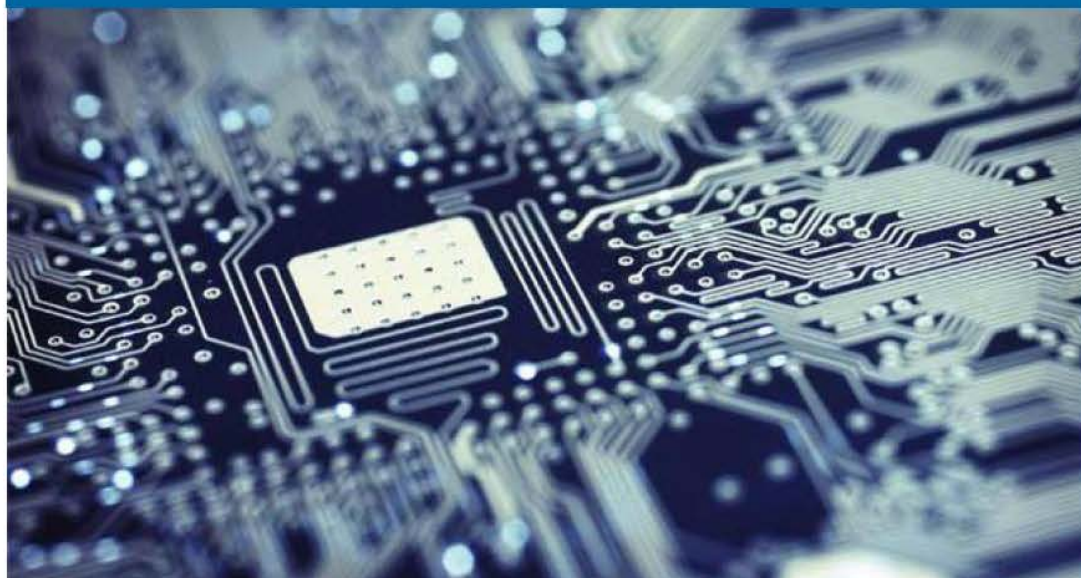


# IEEE 文献推荐报告

——IEEE Integrated Circuit (IC) 特刊

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# IEEE 文献推荐报告

## Integrated Circuit (IC) 特刊

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海洋工程	光学	无线通讯
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## IEEE 文献推荐

关键词 FPGA (field-programmable gate array)/ application-specific integrated circuit (ASIC)

合并检索-检索式: ("All Metadata":field programmable gate array OR FPGA) AND  
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☐ Early Access Articles (16) ☐ Courses (1) ☐ Standards (1)

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**Year**

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**Affiliation**

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☐ Design of Evaluation Board for Image Processing ASIC and VHDL Implementation of FPGA Interface

Chaitra M; Aravind H S; Anantha Shayanam G R; Harish Bohara; Najeer Ahmmad Shiak; Srividhya S

2018 International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering (ICRIEECE)

Year: 2018 | Conference Paper | Publisher: IEEE

☐ Analog- and Mixed-Signal Fabrics

James C. Kemerling; Robert Greenwell; Bhaskar Bharath

Proceedings of the IEEE

Year: 2015 | Volume: 103, Issue: 7 | Journal Article | Publisher: IEEE

Cited by: Papers (5)

☐ Single Event Effects Test Results for Advanced Field Programmable Gate Arrays

Gregory R. Allen; Gary M. Swift

2006 IEEE Radiation Effects Data Workshop

(以上数据截止至 2023 年 5 月 16 日，数据每天更新)

### 高被引文献 (被论文引用):

- I. Kuon and J. Rose, "Measuring the Gap Between FPGAs and ASICs," in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 26, no. 2, pp. 203-215, Feb. 2007, doi: 10.1109/TCAD.2006.884574.  
URL: <https://ieeexplore.ieee.org/document/4068926>

- S. Kumar et al., "A network on chip architecture and design methodology," Proceedings IEEE Computer Society Annual Symposium on VLSI. New Paradigms for VLSI Systems Design. ISVLSI 2002, Pittsburgh, PA, USA, 2002, pp. 117-124, doi: 10.1109/ISVLSI.2002.1016885.  
URL: <https://ieeexplore.ieee.org/document/1016885>
- J. Cong, B. Liu, S. Neuendorffer, J. Noguera, K. Vissers and Z. Zhang, "High-Level Synthesis for FPGAs: From Prototyping to Deployment," in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 30, no. 4, pp. 473-491, April 2011, doi: 10.1109/TCAD.2011.2110592.  
URL: <https://ieeexplore.ieee.org/document/5737854>

### 高被引文献（被专利引用）：

- Mirsky and DeHon, "MATRIX: a reconfigurable computing architecture with configurable instruction distribution and deployable resources," 1996 Proceedings IEEE Symposium on FPGAs for Custom Computing Machines, Napa Valley, CA, USA, 1996, pp. 157-166, doi: 10.1109/FPGA.1996.564808.  
URL: <https://ieeexplore.ieee.org/document/564808>
- K. Y. Tong, V. Kheterpal, V. Rovner, L. Pileggi and H. Schmit, "Regular logic fabrics for a via patterned gate array (VPGA)," Proceedings of the IEEE 2003 Custom Integrated Circuits Conference, 2003., San Jose, CA, USA, 2003, pp. 53-56, doi: 10.1109/CICC.2003.1249358.  
URL: <https://ieeexplore.ieee.org/document/1249358>
- P. S. Zuchowski, C. B. Reynolds, R. J. Grupp, S. G. Davis, B. Cremen and B. Troxel, "A hybrid ASIC and FPGA architecture," IEEE/ACM International Conference on Computer Aided Design, 2002. ICCAD 2002., San Jose, CA, USA, 2002, pp. 187-194, doi: 10.1109/ICCAD.2002.1167533.  
URL: <https://ieeexplore.ieee.org/document/1167533>

### 热门文献：

- S. M. Trimberger, "Three Ages of FPGAs: A Retrospective on the First Thirty Years of FPGA Technology," in Proceedings of the IEEE, vol. 103, no. 3, pp. 318-331, March 2015, doi: 10.1109/JPROC.2015.2392104.  
URL: <https://ieeexplore.ieee.org/document/7086413>
- A. Shawahna, S. M. Sait and A. El-Maleh, "FPGA-Based Accelerators of Deep Learning Networks for Learning and Classification: A Review," in IEEE Access, vol. 7, pp. 7823-7859, 2019, doi: 10.1109/ACCESS.2018.2890150.  
URL: <https://ieeexplore.ieee.org/document/8594633>
- S. M. Trimberger and J. J. Moore, "FPGA Security: Motivations, Features, and Applications," in Proceedings of the IEEE, vol. 102, no. 8, pp. 1248-1265, Aug. 2014, doi: 10.1109/JPROC.2014.2331672.  
URL: <https://ieeexplore.ieee.org/document/6849432>

**期刊推荐:****1. [IEEE Transactions on Very Large Scale Integration \(VLSI\) Systems](#)****期刊介绍:**

该刊为月刊，影响因子为 2.775，主要关注电路设计、芯片及晶圆制造、封装、测试和系统应用等方面的研究。

**2. [IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems](#)****期刊介绍:**

该刊为月刊，影响因子为 2.565，主要关注集成电路中的模拟、混合信号、光学或微波组件以及相关的计算机辅助设计等方面的研究。

**3. [IEEE Transactions on Nuclear Science](#)****期刊介绍:**

该刊为月刊，影响因子为 1.703，主要关注与核科学相关的计算、检测电离辐射的仪器、反应堆仪表及控制等方面的研究。





### 会议推荐:

1. [International Conference on Field Programmable Logic and Applications](#)

#### 会议介绍:

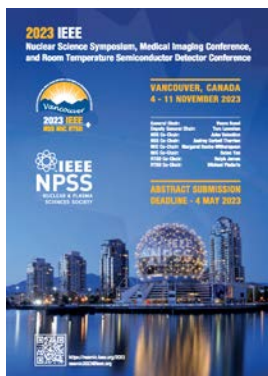
该会议聚焦可重构系统架构及其应用、嵌入式处理器、设计自动化方法及工具等方面的研究。



2. [IEEE Symposium on Nuclear Science \(NSS/MIC\)](#)

#### 会议介绍:

该会议聚焦模拟和数字电路系统的理论、设计及实施等方面的研究。



3. [International Conference on ASIC \(ASICON\)](#)

#### 会议介绍:

该会议聚焦集成电路设计、IC 制造、IC 工艺及器件、CAD/CAE 工具开发最新进展等方面的研究。



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☐ BiCMOS ASICs: technology and applications  
T. Wong; M. Jain; D. White  
Proceedings, Second Annual IEEE ASIC Seminar and Exhibit,  
Year: 1989 | Conference Paper | Publisher: IEEE

☐ Clock tree synthesis for high performance ASICs  
J. Burakis  
[1991] Proceedings Fourth Annual IEEE International ASIC Conference and Exhibit  
Year: 1991 | Conference Paper | Publisher: IEEE  
Cited by: Papers (9) | Patents (2)

☐ Level synthesis approach to application-specific integrated circuits (ASIC) design  
O. Levia  
Proceedings, Second Annual IEEE ASIC Seminar and Exhibit,  
Year: 1989 | Conference Paper | Publisher: IEEE  
Cited by: Papers (1)

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### 高被引文献 (被论文引用):

- J. Mitola and G. Q. Maguire, "Cognitive radio: making software radios more personal," in IEEE Personal Communications, vol. 6, no. 4, pp. 13-18, Aug. 1999, doi: 10.1109/98.788210.  
URL: <https://ieeexplore.ieee.org/document/788210>
- X. L. Xie and G. Beni, "A validity measure for fuzzy clustering," in IEEE Transactions on Pattern Analysis and Machine Intelligence, vol. 13, no. 8, pp. 841-847, Aug. 1991, doi: 10.1109/34.85677.  
URL: <https://ieeexplore.ieee.org/document/85677>
- K. E. Petersen, "Silicon as a mechanical material," in Proceedings of the IEEE, vol. 70, no. 5, pp. 420-457, May 1982, doi: 10.1109/PROC.1982.12331.  
URL: <https://ieeexplore.ieee.org/document/1456599>

### 高被引文献 (被专利引用):

- K. E. Petersen, "Silicon as a mechanical material," in Proceedings of the IEEE, vol. 70, no. 5, pp. 420-457, May 1982, doi: 10.1109/PROC.1982.12331.  
URL: <https://ieeexplore.ieee.org/document/1456599>



- Qiuting Huang and M. Oberle, "A 0.5mW passive telemetry IC for biomedical applications," Proceedings of the 23rd European Solid-State Circuits Conference, Southampton, UK, 1997, pp. 172-175.  
URL: <https://ieeexplore.ieee.org/document/1470891>
- V. Chandra, A. Xu, H. Schmit and L. Pileggi, "An interconnect channel design methodology for high performance integrated circuits," Proceedings Design, Automation and Test in Europe Conference and Exhibition, Paris, France, 2004, pp. 1138-1143 Vol.2, doi: 10.1109/DATE.2004.1269045.  
URL: <https://ieeexplore.ieee.org/document/1269045>

### 热门文献:

- "IEEE Standard for Ethernet," in IEEE Std 802.3-2018 (Revision of IEEE Std 802.3-2015), vol., no., pp.1-5600, 31 Aug. 2018, doi: 10.1109/IEEESTD.2018.8457469.  
URL: <https://ieeexplore.ieee.org/document/8457469>
- G. Palumbo and D. Pappalardo, "Charge Pump Circuits: An Overview on Design Strategies and Topologies," in IEEE Circuits and Systems Magazine, vol. 10, no. 1, pp. 31-45, First Quarter 2010, doi: 10.1109/MCAS.2009.935695.  
URL: <https://ieeexplore.ieee.org/document/5430472>
- N. P. Jouppi et al., "In-datacenter performance analysis of a tensor processing unit," 2017 ACM/IEEE 44th Annual International Symposium on Computer Architecture (ISCA), Toronto, ON, Canada, 2017, pp. 1-12, doi: 10.1145/3079856.3080246.  
URL: <https://ieeexplore.ieee.org/document/8192463>

### 期刊推荐:

1. [IEEE Journal of Solid-State Circuits](#)

### 期刊介绍:

该刊为月刊，影响因子为 6.126，主要关注集成电路的晶体管级设计方面的研究。此外，该刊还涵盖了与 IC 设计相关的电路建模、技术、系统设计、布局及测试等方面的研究。



## 2. [IEEE Transactions on Electron Devices](#)

### 期刊介绍:

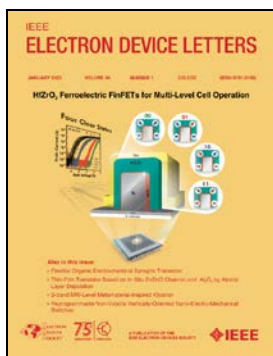
该刊为月刊，影响因子为 3.221，主要关注集成电路器件理论、建模、设计及其性能和可靠性等方面的研究。



## 3. [IEEE Electron Device Letters](#)

### 期刊介绍:

该刊为月刊，影响因子为 4.816，主要关注集成电路器件理论、建模、设计及其性能和可靠性等方面的研究。



### 会议推荐:

#### 1. [IEEE International Solid-State Circuits Conference \(ISSCC\)](#)

### 会议介绍:

该会议聚焦模拟设计、数字电路系统及架构、电源管理、内存及存储系统等方面的研究



## 2. [2021 ACM/IEEE 48th Annual International Symposium on Computer Architecture \(ISCA\)](#)

### 会议介绍:

该会议聚焦处理器、内存及存储系统体系结构、处理器和系统架构可靠性等方面的研究。



## 3. [2022 International Electron Devices Meeting \(IEDM\)](#)

### 会议介绍:

该会议聚焦半导体和电子设备相关技术、设计、制造、物理和建模等方面的研究。



## 分开检索-检索式 2: ("All Metadata":field programmable gate array) OR ("All Metadata":FPGA)

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☐ Books (97) ☐ Standards (8) ☐ Courses (2)

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☐ **Testing of Uncustomized Segmented Channel Field Programmable Gate Arrays**

Tong Liu; Wei Kang Huang; F. Lombardi

Third International ACM Symposium on Field-Programmable Gate Arrays

Year: 1995 | Conference Paper | Publisher: IEEE

Cited by: Papers (9)

☒ **Abstract**

☐ **FPGA '97: 1997 ACM/SIGDA International Symposium on Field Programmable Gate Arrays**

IEEE Transactions on Very Large Scale Integration (VLSI) Systems

Year: 1998 | Volume: 6, Issue: 2 | Journal Article | Publisher: IEEE

☒ **On Nominal Delay Minimization in LUT-Based FPGA Technology Mapping**

J. Cong; Yuzheng Ding

Third International ACM Symposium on Field-Programmable Gate Arrays

Year: 1995 | Conference Paper | Publisher: IEEE

Cited by: Papers (5)

(以上数据截止至 2023 年 5 月 16 日, 数据每天更新)

### 高被引文献 (被论文引用):

- R. C. Baumann, "Radiation-induced soft errors in advanced semiconductor technologies," in IEEE Transactions on Device and Materials Reliability, vol. 5, no. 3, pp. 305-316, Sept. 2005, doi: 10.1109/TDMR.2005.853449.  
URL: <https://ieeexplore.ieee.org/document/1545891>
- E. Monmasson and M. N. Cirstea, "FPGA Design Methodology for Industrial Control Systems—A Review," in IEEE Transactions on Industrial Electronics, vol. 54, no. 4, pp. 1824-1842, Aug. 2007, doi: 10.1109/TIE.2007.898281.  
URL: <https://ieeexplore.ieee.org/document/4267891>
- A. Putnam et al., "A reconfigurable fabric for accelerating large-scale datacenter services," 2014 ACM/IEEE 41st International Symposium on Computer Architecture (ISCA), Minneapolis, MN, USA, 2014, pp. 13-24, doi: 10.1109/ISCA.2014.6853195.  
URL: <https://ieeexplore.ieee.org/document/6853195>

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高被引文献（被专利引用）:

- J. R. Hauser and J. Wawrzynek, "Garp: a MIPS processor with a reconfigurable coprocessor," Proceedings. The 5th Annual IEEE Symposium on Field-Programmable Custom Computing Machines Cat. No.97TB100186), Napa Valley, CA, USA, 1997, pp. 12-21, doi: 10.1109/FPGA.1997.624600.  
URL: <https://ieeexplore.ieee.org/document/624600>
- V. Vaish et al., "Synthetic Aperture Focusing using a Shear-Warp Factorization of the Viewing Transform," 2005 IEEE Computer Society Conference on Computer Vision and Pattern Recognition (CVPR'05) - Workshops, San Diego, CA, USA, 2005, pp. 129-129, doi: 10.1109/CVPR.2005.537.  
URL: <https://ieeexplore.ieee.org/document/1565441>
- P. Chow, Soon Ong Seo, J. Rose, K. Chung, G. Paez-Monzon and I. Rahardja, "The design of a SRAM-based field-programmable gate array-Part II: Circuit design and layout," in IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 7, no. 3, pp. 321-330, Sept. 1999, doi: 10.1109/92.784093.  
URL: <https://ieeexplore.ieee.org/document/784093>

## 热门文献:

- J. Mitra and T. K. Nayak, "An FPGA-Based Phase Measurement System," in IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 26, no. 1, pp. 133-142, Jan. 2018, doi: 10.1109/TVLSI.2017.2758807.  
URL: <https://ieeexplore.ieee.org/document/8082787>
- K. Guo et al., "Angel-Eye: A Complete Design Flow for Mapping CNN Onto Embedded FPGA," in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 37, no. 1, pp. 35-47, Jan. 2018, doi: 10.1109/TCAD.2017.2705069.  
URL: <https://ieeexplore.ieee.org/document/7930521>
- D. T. Nguyen, T. N. Nguyen, H. Kim and H. -J. Lee, "A High-Throughput and Power-Efficient FPGA Implementation of YOLO CNN for Object Detection," in IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 27, no. 8, pp. 1861-1873, Aug. 2019, doi: 10.1109/TVLSI.2019.2905242.  
URL: <https://ieeexplore.ieee.org/document/8678682>

**期刊推荐:**1. [IEEE Transactions on Circuits and Systems II: Express Briefs](#)**期刊介绍:**

该刊为月刊，影响因子为 3.691，主要关注电路理论、分析、设计和实现，以及电路技术在系统和信号处理中的应用等方面的研究。

2. [IEEE Transactions on Circuits and Systems I: Regular Papers](#)**期刊介绍:**

该刊为月刊，影响因子为 4.14，主要关注电路理论、分析、设计和实际实现，以及电路技术在系统及信号处理中的应用等方面的研究。

3. [IEEE Transactions on Computers](#)**期刊介绍:**

该刊为月刊，影响因子为 3.183，主要关注面计算机操作系统和软件系统、实时系统和嵌入式系统、计算机组件等方面的研究。



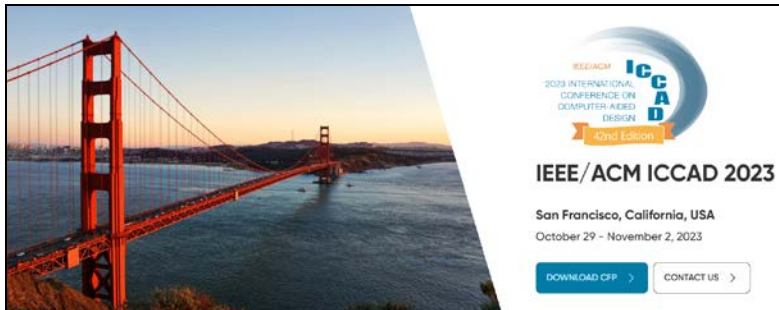


### 会议推荐:

1. [2022 IEEE/ACM International Conference On Computer Aided Design \(ICCAD\)](#)

### 会议介绍:

该会议聚焦从器件、电路到系统 CMOS 设计中与 CAD 相关的研究。



2. [2021 58th ACM/IEEE Design Automation Conference \(DAC\)](#)

### 会议介绍:

该会议聚焦电子芯片到系统的设计及设计自动化等方面的研究。



3. [2022 55th IEEE/ACM International Symposium on Microarchitecture \(MICRO\)](#)

### 会议介绍:

该会议聚焦电路系统微架构、编译器、芯片等方面的研究。



相关企业发文情况

(注：以下数据截止至 2023 年 5 月 16 日，数据每天更新)

1. AMD Xilinx, Inc. (Advanced Micro Devices, Inc.) (US)

在 IEEE Xplore 发文总量：3,269 篇

发文链接：[点击这里](#)

Search within results

Items Per Page

Export

Set Search Alerts

Search History

Showing 1-25 of 3,269 results for ("Author Affiliations":Xilinx) OR ("Author Affiliations":AMD) OR ("Author Affiliations":Advanced Micro Devices) x

Conferences (2,478)

Journals (613)

Magazines (173)

Early Access Articles (5)

Show

All Results

Open Access Only

Year

Single Year

Range

1971

2023

From

To

1971

2023

Author

Affiliation

Select All on Page

Sort By

Relevance

High performance 65 nm SOI technology with enhanced transistor strain and advanced-low-K BEOL

W.-H. Lee; A. Waite; H. Nii; H.M. Nayfeh; V. McGahay; H. Nakayama; D. Fried; H. Chen; L. Black; R. Bolam; J. Cheng; D. Chidambarrao; C. Christiansen; M. Cullinan-Scholl; D.R. Davies; A. Domenicucci; P. Fisher; J. Fitzsimmons; J. Gill; M. Gribelyuk; D. Harmon; J. Holt; K. Ida; M. Kiene; J. Kluth; C. Labelle; A. Madan; K. Malone; P.V. McLaughlin; M. Minami; D. Mocuta; R. Murphy; C. Muzzy; M. Newport; S. Panda; I. Peidous; A. Sakamoto; T. Sato; G. Sudo; H. VanMeer; T. Yamashita; H. Zhu; P. Agnello; G. Bronner; G. Freeman; S.-F. Huang; T. Ivers; S. Luning; K. Miyamoto; H. Nye; J. Pellerin; K. Rim; D. Schepis; T. Spooner; X. Chen; M. Khare; M. Horstmann; A. Wei; T. Kammler; J. Hontschel; H. Bierstedt; H.-J. Engelmann; A. Hellmich; K. Hempel; G. Koerner; A. Neu; R. Otterbach; C. Reiche; M. Trentsch; P. Press; K. Froberg; M. Schaller; H. Salz; J. Hohage; H. Ruelke; J. Klais; M. Raab; D. Greenlaw; N. Kepler

IEEE International Electron Devices Meeting, 2005. IEDM Technical Digest.

Year: 2005 | Conference Paper | Publisher: IEEE

Cited by: Papers (30) | Patents (2)

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Rapid decisions

在 FPGA 领域发文量：572 篇

发文链接：[点击这里](#)

相关热门文章：

- S. M. Trimberger, "Three Ages of FPGAs: A Retrospective on the First Thirty Years of FPGA Technology," in Proceedings of the IEEE, vol. 103, no. 3, pp. 318-331, March 2015, doi: 10.1109/JPROC.2015.2392104.  
URL: <https://ieeexplore.ieee.org/document/7086413>
- S. M. Trimberger and J. J. Moore, "FPGA Security: Motivations, Features, and Applications," in Proceedings of the IEEE, vol. 102, no. 8, pp. 1248-1265, Aug. 2014, doi: 10.1109/JPROC.2014.2331672.  
URL: <https://ieeexplore.ieee.org/document/6849432>
- J. Cong, B. Liu, S. Neuendorffer, J. Noguera, K. Vissers and Z. Zhang, "High-Level Synthesis for FPGAs: From Prototyping to Deployment," in IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, vol. 30, no. 4, pp. 473-491, April 2011, doi: 10.1109/TCAD.2011.2110592.  
URL: <https://ieeexplore.ieee.org/document/5737854>

## 2. Intel Corporation (US)

在 IEEE Xplore 发文总量: 17,724 篇

发文链接: [点击这里](#)

The screenshot shows the IEEE Xplore search interface. At the top, there's a search bar with the text "Search within results" and a magnifying glass icon. To the right of the search bar are buttons for "Items Per Page", "Export", "Set Search Alerts", and "Search History". Below the search bar, it says "Showing 1-25 of 17,724 results for ('Author Affiliations':intel) OR ('Author Affiliations':Altera )". There are checkboxes for "Conferences (12,827)", "Journals (3,933)", "Magazines (808)", "Books (116)", and "Early Access Articles (40)". On the left side, there's a "Show" section with "All Results" selected and "Open Access Only" as an option. Below that is a "Year" section with a "Single Year" and "Range" button, and a date range from 1968 to 2023. There's also an "Author" and "Affiliation" section. The main results area shows two entries: "Architecture and methodology of a SoPC with 3.25Gbps CDR based SERDES and 1Gbps dynamic phase alignment" and "Arria™ 10 device architecture". On the right side, there are two promotional banners: "Need Full-Text access to IEEE Xplore for your organization?" and "Get Critical Training on".

在 FPGA 领域发文量: 640 篇

发文链接: [点击这里](#)

相关热门文章:

- R. Tessier, K. Pock and A. DeHon, "Reconfigurable Computing Architectures," in Proceedings of the IEEE, vol. 103, no. 3, pp. 332-354, March 2015, doi: 10.1109/JPROC.2014.2386883.  
URL: <https://ieeexplore.ieee.org/document/7086414>
- E. Nurvitadhi, D. Sheffield, Jaewoong Sim, A. Mishra, G. Venkatesh and D. Marr, "Accelerating Binarized Neural Networks: Comparison of FPGA, CPU, GPU, and ASIC," 2016 International Conference on Field-Programmable Technology (FPT), Xi'an, China, 2016, pp. 77-84, doi: 10.1109/FPT.2016.7929192.  
URL: <https://ieeexplore.ieee.org/document/7929192>
- A. Basu et al., "Low-Power, Adaptive Neuromorphic Systems: Recent Progress and Future Directions," in IEEE Journal on Emerging and Selected Topics in Circuits and Systems, vol. 8, no. 1, pp. 6-27, March 2018, doi: 10.1109/JETCAS.2018.2816339.  
URL: <https://ieeexplore.ieee.org/document/8316846>

### 3. Microchip Technology Inc. (US)

在 IEEE Xplore 发文总量: 219 篇

发文链接: [点击这里](#)

The screenshot shows the IEEE Xplore search results page for the query "Microchip". The results are filtered to show 1-25 of 219 results. The search criteria are "Author Affiliations: Microchip". The results are sorted by Relevance. The first result is "Scaling Split-Gate Flash Memory Technology for Advanced MCU and Emerging Applications" by N. Do; J. Kim; S. Lemke; L. Tee; Y. Tkachev; X. Liu; P. Ghazavi; F. Zhou; B. Villard; S. Jourba; C. Decobert; S. Hong; T. Vu; S. Trinh; A. Ly; H. Tran; V. Tiwari; M. Reiten, published in the 2019 IEEE 11th International Memory Workshop (IMW). The second result is "A 55 nm Logic-Process-Compatible, Split-Gate Flash Memory Array Fully Demonstrated at Automotive Temperature with High Access Speed and Reliability" by Nhan Do; Latt Tee; Santosh Hariharan; Steven Lemke; Mandana Tadayoni; Will Yang; MT Wu; Jinho Kim; Yueh-Hsin Chen; Chien-Sheng Su; Vipin Tiwari; Stephen Zhou; Rodger Qian; Ian Yue, published in the 2015 IEEE International Memory Workshop (IMW). The page also includes a sidebar with filters for Year (1988 to 2023), Author, and Affiliation, and a "Need Full-Text" banner.

在 FPGA 领域发文量: 10 篇

发文链接: [点击这里](#)

相关热门文章:

- N. Rezzak, J. -J. Wang, S. Varela, G. Bakker and A. N. Gu, "Neutron and Proton Characterization of Microsemi 28 nm PolarFire SONOS-Based FPGA," 2018 IEEE Radiation Effects Data Workshop (REDW), Waikoloa, HI, USA, 2018, pp. 1-5, doi: 10.1109/NSREC.2018.8584300.  
URL: <https://ieeexplore.ieee.org/document/8584300>
- H. Om'mani, M. Tadayoni, N. Thota, Ian Yue and Nhan Do, "A novel test structure to implement a programmable logic array using split-gate flash memory cells," 2013 IEEE International Conference on Microelectronic Test Structures (ICMTS), Osaka, Japan, 2013, pp. 192-194, doi: 10.1109/ICMTS.2013.6528170.  
URL: <https://ieeexplore.ieee.org/document/6528170>
- N. Rezzak, J. -J. Wang, F. Hawley and E. Hamdy, "Proton Characterization of RTG4 Flash-Based FPGA for LEO Environment," 2019 IEEE Radiation Effects Data Workshop, San Antonio, TX, USA, 2019, pp. 1-5, doi: 10.1109/REDW.2019.8906605.  
URL: <https://ieeexplore.ieee.org/document/8906605>

#### 4. Lattice Semiconductor Corporation (US)

在 IEEE Xplore 发文总量: 101 篇

发文链接: [点击这里](#)

The screenshot shows the IEEE Xplore search results page for the query "Author Affiliations":Lattice". It displays 101 results, with filters for Conferences (72), Journals (27), and Magazines (2). The results are sorted by Relevance. Two results are visible:

- A 32x32 Array Terahertz Sensor in 65-nm CMOS Technology**  
Tong Fang; Min Liu; Li-Yuan Liu; Zi-Teng Cai; Run-Jiang Dou; Peng Feng; Nan Qi; Zhao Zhang; Jian Liu; Nan-Jian Wu  
2021 IEEE International Conference on Integrated Circuits, Technologies and Applications (ICTA)  
Year: 2021 | Conference Paper | Publisher: IEEE
- Temperature and humidity stress failure on copper pillar (CuP) flip chip package device**  
Kaye Ann de las Alas; Christine Ison; Julius Oliver Rivera; Ramil dela Cruz; Ruffy Aguares; Devang Vyas; Toan Nguyen; Michelle Bailon-Somintac  
2017 IEEE 24th International Symposium on the Physical and Failure Analysis of Integrated Circuits (IPFA)  
Year: 2017 | Conference Paper | Publisher: IEEE

On the right side, there are promotional banners for "Need Full-Text" and "Publish Open Access with IEEE".

在 FPGA 领域发文量: 18 篇

发文链接: [点击这里](#)

相关热门文章:

- G. Karypis, R. Aggarwal, V. Kumar and S. Shekhar, "Multilevel hypergraph partitioning: applications in VLSI domain," in IEEE Transactions on Very Large Scale Integration (VLSI) Systems, vol. 7, no. 1, pp. 69-79, March 1999, doi: 10.1109/92.748202.  
URL: <https://ieeexplore.ieee.org/document/748202>
- S. Fong, J. Ariyoshi and T. Ema, "Embedded Flash on a Low-Power 65-nm Logic Technology," in IEEE Electron Device Letters, vol. 33, no. 9, pp. 1261-1263, Sept. 2012, doi: 10.1109/LED.2012.2204950.  
URL: <https://ieeexplore.ieee.org/document/6241403>
- H. Kojima et al., "Embedded Flash on 90nm Logic Technology & Beyond for FPGAs," 2007 IEEE International Electron Devices Meeting, Washington, DC, USA, 2007, pp. 677-680, doi: 10.1109/IEDM.2007.4419036.  
URL: <https://ieeexplore.ieee.org/document/4419036>

## 5. Achronix Semiconductor Corporation (US)

在 IEEE Xplore 发文总量: 6 篇

发文链接: [点击这里](#)

The screenshot shows the IEEE Xplore search interface. At the top, there's a search bar with 'Search within results' and a magnifying glass icon. To the right are buttons for 'Export', 'Set Search Alerts', and 'Search History'. Below the search bar, it says 'Showing 1-6 of 6 results for ("Author Affiliations":Achronix) ×'. There are checkboxes for 'Conferences (5)' and 'Journals (1)'. On the left, there's a 'Show' section with 'All Results' selected and 'Open Access Only' as an option. Below that is a 'Year' section with a 'Single Year' button and a 'Range' button. The 'Range' section shows a slider from 2009 to 2021, with 'From' and 'To' fields set to 2009 and 2021 respectively. There are also 'Author' and 'Affiliation' dropdown menus. On the right, there are two search results. The first result is 'A radiation hardened reconfigurable FPGA' by Shankamarayanan Ramaswamy et al., published in the 2009 IEEE Aerospace conference. The second result is 'Effective Segmentation approach of Package-to-PCB modeling using Full-Wave EM field Solver' by Hansel Dsilva et al., published in the 2020 IEEE Electrical Design of Advanced Packaging and Systems (EDAPS). To the right of the search results, there are two promotional banners: 'Need Full-Text access to IEEE Xplore for your organization? CONTACT IEEE TO SUBSCRIBE >' and a '10 YEARS' anniversary logo.

在 FPGA 领域发文量: 3 篇

发文链接: [点击这里](#)

相关热门文章:

- S. Ramaswamy et al., "A radiation hardened reconfigurable FPGA," 2009 IEEE Aerospace conference, Big Sky, MT, USA, 2009, pp. 1-10, doi: 10.1109/AERO.2009.4839506.  
URL: <https://ieeexplore.ieee.org/document/4839506>
- A. T. Kelly et al., "Mitigation of Single-Event Charge Sharing in a Commercial FPGA Architecture," in IEEE Transactions on Nuclear Science, vol. 61, no. 4, pp. 1635-1642, Aug. 2014, doi: 10.1109/TNS.2014.2338397.  
URL: <https://ieeexplore.ieee.org/document/6870676>
- A. T. Kelly et al., "Mitigation of single-event charge sharing in a commercial FPGA architecture," 2013 14th European Conference on Radiation and Its Effects on Components and Systems (RADECS), Oxford, UK, 2013, pp. 1-8, doi: 10.1109/RADECS.2013.6937410.  
URL: <https://ieeexplore.ieee.org/document/6937410>



## IEEE 突破性专利研究——半导体领域

（数据来源：1970 专利分析报告，查看报告原文，请[点击这里](#)）

### 案例一：Kandou

背景：

- Kandou 设计了一种高速、节能和高引脚利用率的串行链接集成电路组件；
- Kandou 使用的是一种新的串行链接传输模式，在现有连接上使用更少的能量传输更多位；

影响：

- 企业估值为 2.24 亿至 3.36 亿美元；
- 这项技术已授权给业内领先的半导体公司，包括 Marvell 和 Coherent Logix；

专利详情：

- 12 项被高度引用的专利引用了 121 篇 IEEE 现有技术文献；
- 平均每个专利引用了 10 篇 IEEE 文献；

Kandou 专利频繁引用以下 IEEE 出版物中有影响力的出版物：

- [IEEE Transactions on Very Large Scale Integration \(VLSI\) Systems](#)
- [IEEE Transactions Audio and Electroacoustics \(now IEEE Transactions on Signal Processing\)](#)
- [IEEE Transactions of Information Theory](#)
- [IEEE International Conference on Communications](#)
- [IEEE Journal of Solid-State Circuits](#)

### 案例二：Cap Wireless/Triquint/Qorvo

背景：

- Cap Wireless 曾经是 Spatium 宽带放大器产品线的所有企业；
- Spatium 使用获得专利的同轴空间组合技术来提高宽带射频功率效率；

影响：

- 这些设备的全球市场价值 6 亿美元；
- Triquint Semiconductor 以 1,480 万美元收购了 Cap Wireless，并与 RFMD 合并成立了 Qorvo；
- Qorvo 收入超过 30 亿美元，目前仍在继续销售 Spatium 放大器；

专利详情：

- Cap Wireless 获得的 5 项专利被高度引用，其中 4 项被引用了 781 次；
- 这 4 项专利每项引用了 29-30 篇 IEEE 文献，共计 117 篇 IEEE 参考文献；

Cap Wireless 专利频繁引用以下 IEEE 出版物中有影响力的出版物：

- [IEEE Transactions on Microwave Theory and Techniques](#)
- [IEEE Microwave and Wireless Components Letters](#)
- [IEEE/MTT-S International Microwave Symposium](#)
- [IEEE Microwave and Wireless Technology Letters](#)

## IEEE 突破性专利研究——电子领域

（数据来源：1970 专利分析报告，查看报告原文，请[点击这里](#)）

### 案例一：Nymi/Bionym

背景：

- Nymi 开发了可穿戴设备，通过心跳来验证一个人的身份；
- 被认为是最安全的生物认证设备之一；

影响：

- Nymi 的估值为 4,700 万至 7,000 万美元；
- 满足制药市场对安全和快速身份验证的需求——将身份验证时间缩短 75%；

专利详情：

- 9 项专利引用了相同的 6 篇 IEEE 文献；
- 其中引用的 2 篇 IEEE 文章比 Nymi 申请的第一个专利早了 13 年，表明这项使能科学发表在 IEEE 期刊上，比 Nymi 公司为他们设备申请专利概念早了十多年；

Nymi 专利频繁引用以下 IEEE 出版物中有影响力的出版物：

- [IEEE Transactions on Instrumentation and Measurement](#)
- [IEEE Transactions on Biometrical Engineering](#)

### 案例二：Pelican/Tessera/Xperi

背景：

- Pelican 智能手机阵列相机采用 16 个不同的镜头和 4x4 网格成像通道；
- 每个子摄像头只捕捉一种颜色，提高了图像质量并减少了噪声；

影响：

- 从诺基亚和高通筹集了 2,000 万美元；
- Pelican 被 Tessera（现在的 Xperi）收购，其先进的成像解决方案将用于下一代应用和设备；

专利详情：

- 58 项专利提到了相机阵列或镜头阵列；
- 综合起来，被高度引用的专利中有超过 500 篇 IEEE 参考文献；

Pelican 成像专利频繁引用以下 IEEE 出版物中有影响力的出版物：

（由于这些专利是在 2013-2016 年间授予的，这表明超分辨率的想法在成为专利技术之前的六七年已出现在 IEEE 出版物中。）

- [2006 IEEE International Conference on Acoustics Speech and Signal Processing Proceedings](#)

## IEEE Xplore 使用技巧

### 远程访问

远程访问功能是为了为了方便订购 IEEE 产品的用户在非授权的 IP 范围内，依然可以享受我们资源而推出的特色化服务功能。用户可以联系 [iel@igroup.com.cn](mailto:iel@igroup.com.cn) 申请开通。

**远程访问功能可支持的设备：**笔记本电脑、平板电脑、手机

**远程访问功能账号的有效期：**90 天

**远程访问功能设置的步骤：**

1. 将设备（笔记本电脑、平板电脑、手机），连接 WIFI（必须是可以访问 IEEE Xplore 的 IP 地址）；
2. 打开 IEEE Xplore 首页：<https://ieeexplore.ieee.org/Xplore/home.jsp>；
3. 点击下图红色框内的“Personal Sign In”，登陆 IEEE 个人账号（与机构无关，读者可自己注册）；



若没有个人账号，可点击如下图的“Create Account”，注册个人账号：



4. 登陆个人账号之后，在 My Settings 内，选择“Remote Access”；
5. 根据提示配对设备；
6. 配对成功后，90 天内这台设备将可以在任何地方，通过同一个浏览器访问下载机构订购的 IEEE Xplore 内的内容。（即配对账号时用的 Firefox 浏览器，之后在非授权 IP 范围内下载文献，也必须是 Firefox 浏览器。）
7. 90 天后，此配对将自动失效，读者需要重新配对。或者在 My Setting 内的“Remote Access”内直接点击“Refresh Remote Access”（但必须是同一设备，同一浏览器的前提下）。

**注意点：**

1. 是配对设备（即笔记本电脑，平板电脑或手机）。所以开通此功能和之后下载文献必须是同一台设备。
2. 配对时的浏览器与配对成功后远程访问的浏览器必须是同一个。期间，建议您不要升级浏览

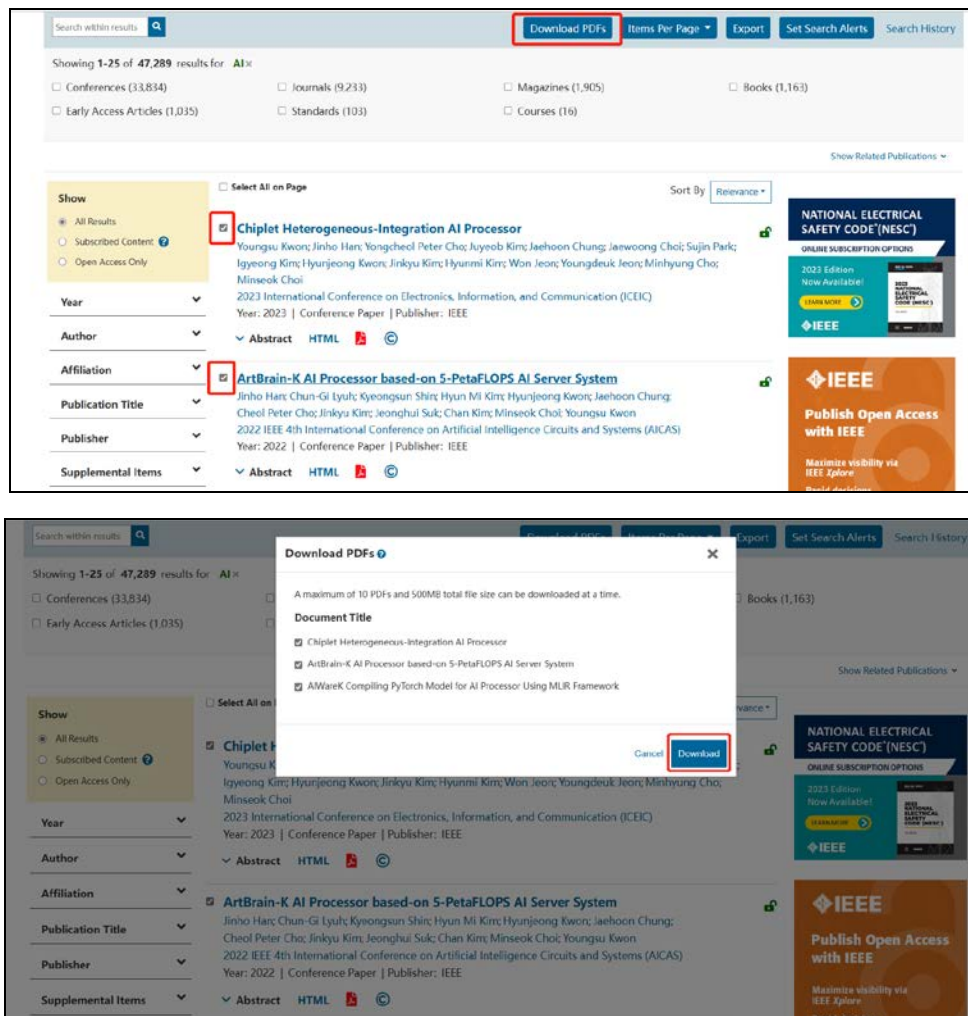
器，以免配对失效。

### 3. 账号的有效期限是 90 天

## 批量下载

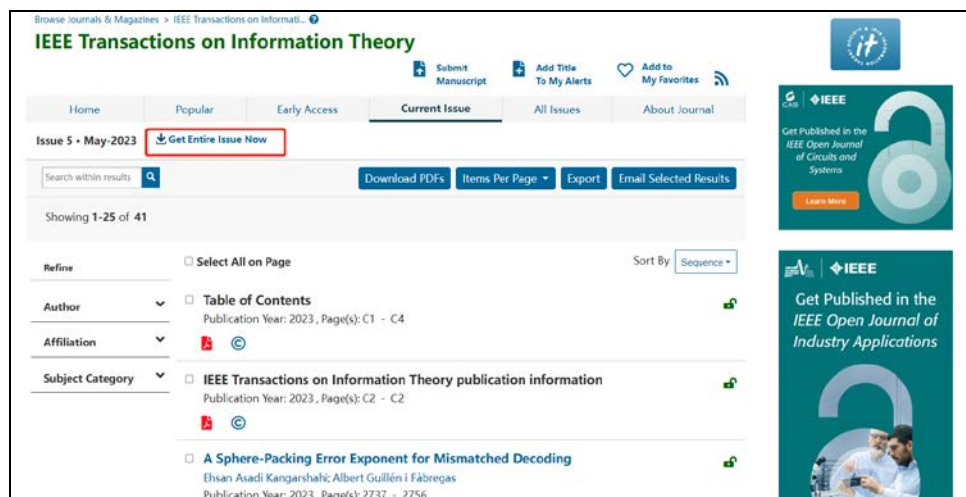
针对 IEL 客户，使用 IEEE Xplore 平台可以对文献进行批量下载。

用户通过平台关键词检索，可以在检索结果找到感兴趣的文章进行勾选，然后点击文献列表上方的 Download PDFs 标识，就可以批量下载 PDF 全文，每次最多选择 10 篇文献，且文件总大小不超过 500MB。如下图：



## 整期下载

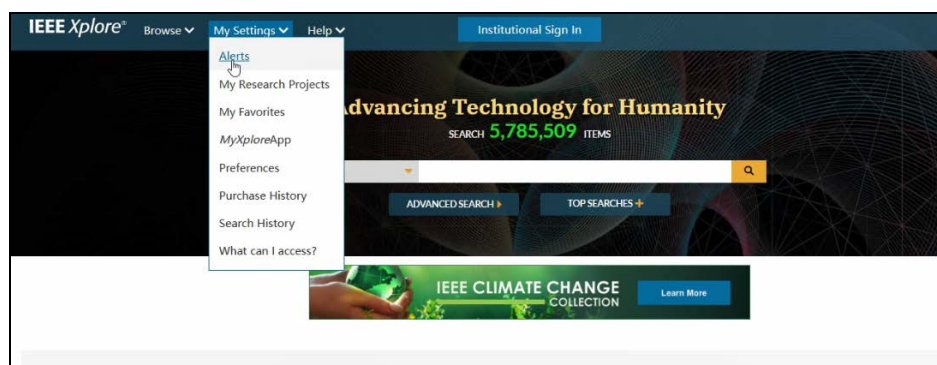
针对 IEL 客户，部分期刊开放整期下载功能，用户可以在期刊主页对某一期的内容批量下载。  
在 IEEE Xplore 平台左上角点击 **Browse – Journals & Magazines** 对期刊进行检索后，点击期刊标题来到期刊主页。在 **Current Issue** 标签下，会看到正下方的 **Get Entire Issue Now** 标识，点击该标识就可以下载当前期的整期内容。如下图：



## 内容提醒

在个人账号登录的情况下，用户可以点击页面顶部“**My Settings**”下的“**Alerts**”，设置提醒。用户可以订阅期刊、会议、标准、电子书的更新通知，查看并管理施引文献、保存的检索式、订阅的作者发文提醒。

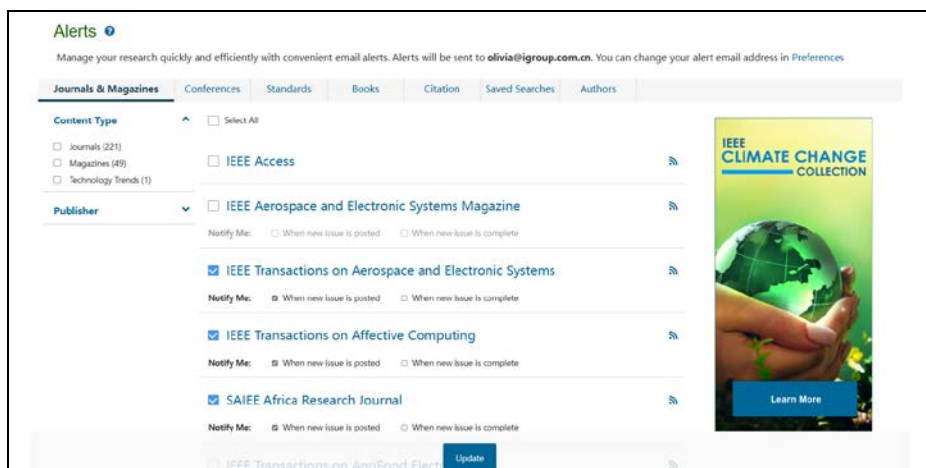
如下图：





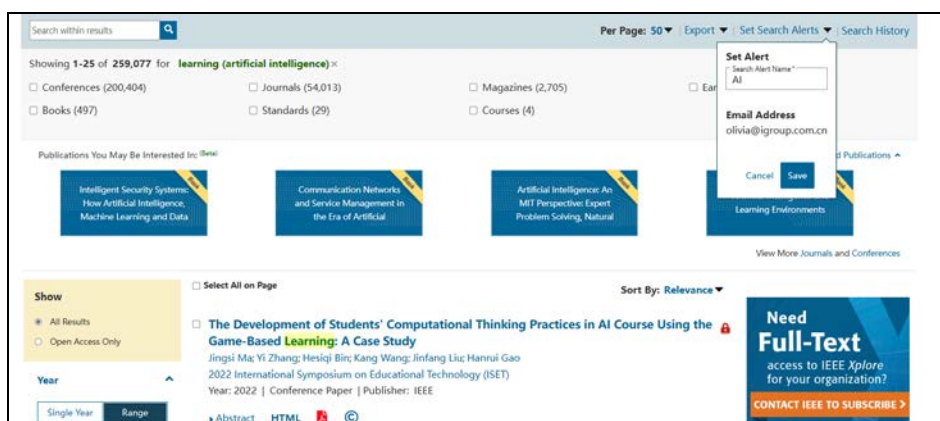
在“Alerts”页面，分别点击“Journals & Magazines”、“Conferences”、“Standards”、“Books”，就可以订阅期刊、会议、标准、电子书的更新通知。

如下图：



用户可以在检索结果页面，点击“Set Search Alerts”，输入保存检索式的名称，点击“Save”，就可以设置检索式更新提醒的服务。当出现有关检索式内容的更新，平台会发送邮件提醒。每个账号最多可以保存 15 个检索式。

如下图：



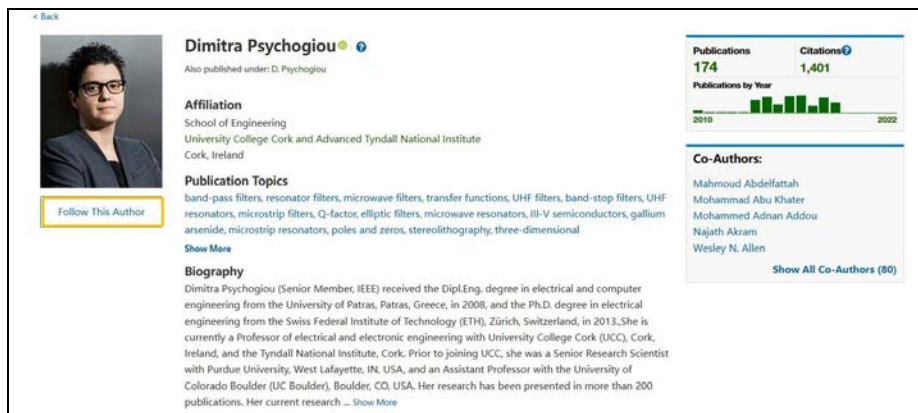
在“Alerts”页面，点击“Saved Searches”，就可以查看并管理之前保存的检索式。

如下图：




用户可以通过作者订阅，关注订阅作者的最新发文动态。最多可以关注 15 位作者，当订阅作者发布最新研究内容时，用户将收到邮件提醒。在作者详情页面中，点击“Follow This Author”即可订阅。

如下图：



The image shows the IEEE Xplore author profile for Dimitra Psychogiou. It includes a profile picture, a 'Follow This Author' button, and various statistics and details.

**Dimitra Psychogiou** 

Also published under: D. Psychogiou

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**Publication Topics**  
band-pass filters, resonator filters, microwave filters, transfer functions, UHF filters, band-stop filters, UHF resonators, microstrip filters, Q-factor, elliptic filters, microwave resonators, III-V semiconductors, gallium arsenide, microstrip resonators, poles and zeros, stereolithography, three-dimensional  
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**Biography**  
Dimitra Psychogiou (Senior Member, IEEE) received the Dipl.Eng. degree in electrical and computer engineering from the University of Patras, Patras, Greece, in 2008, and the Ph.D. degree in electrical engineering from the Swiss Federal Institute of Technology (ETH), Zürich, Switzerland, in 2013. She is currently a Professor of electrical and electronic engineering with University College Cork (UCC), Cork, Ireland, and the Tyndall National Institute, Cork. Prior to joining UCC, she was a Senior Research Scientist with Purdue University, West Lafayette, IN, USA, and an Assistant Professor with the University of Colorado Boulder (UC Boulder), Boulder, CO, USA. Her research has been presented in more than 200 publications. Her current research ... [Show More](#)

**Publications**  
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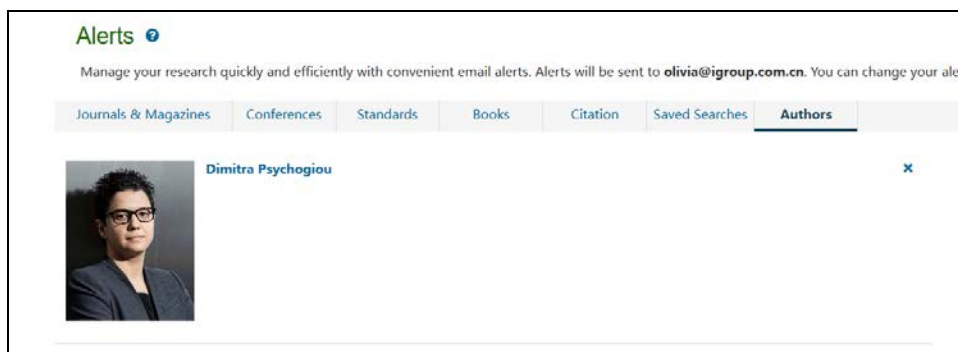
**Citations**  
1,401

**Publications by Year**  
2010 2022

**Co-Authors:**  
Mahmoud Abdelfattah  
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在“Alerts”页面，点击“Authors”，就可以查看并管理之前订阅作者的发文提醒。

如下图：



The image shows the 'Alerts' page in IEEE Xplore. It has a header with the title 'Alerts' and a sub-header 'Manage your research quickly and efficiently with convenient email alerts. Alerts will be sent to olivia@igroup.com.cn. You can change your alert settings here.' Below the header is a navigation bar with tabs: 'Journals & Magazines', 'Conferences', 'Standards', 'Books', 'Citation', 'Saved Searches', and 'Authors'. The 'Authors' tab is selected. Below the navigation bar is a list of authors. The first author is Dimitra Psychogiou, with a profile picture and a blue 'x' icon to the right.

My Research Projects

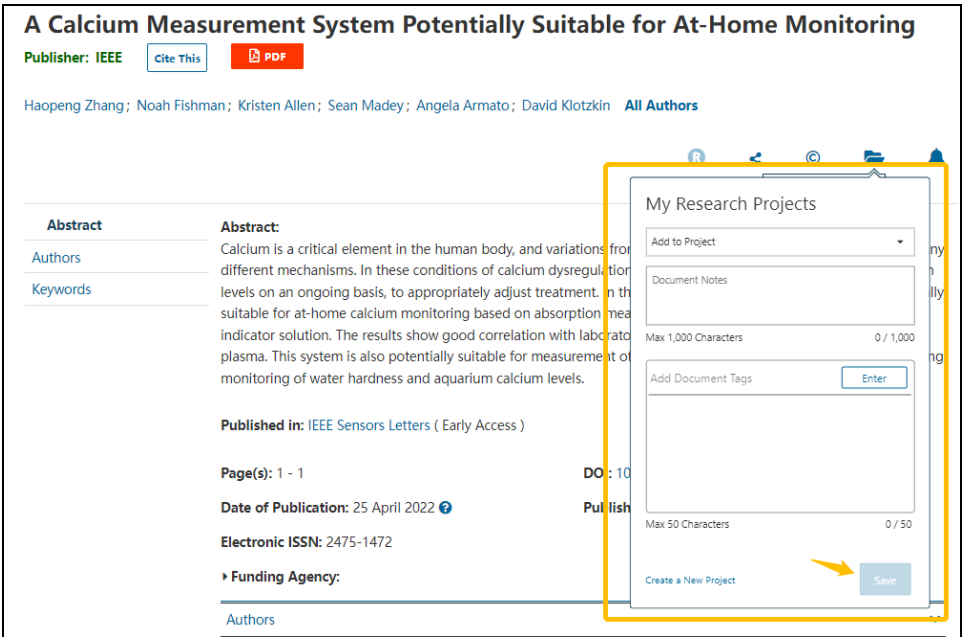
在个人账号登录的情况下，用户可以将重要文献保存至“My Research Projects”。当前，这一功能可实现 15 个文件夹的创建，每个文件夹可保存 1000 份文献。

如下图：

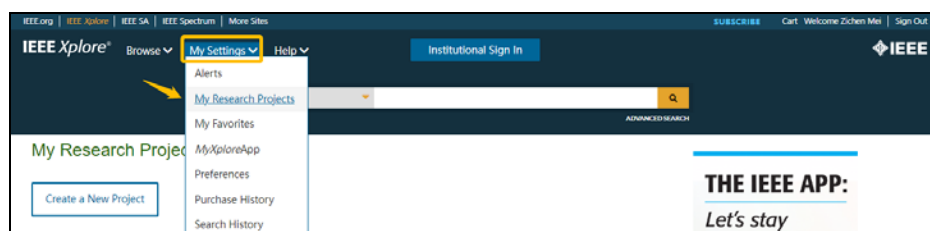


通过“Add to Project”中的下拉菜单，选择已有目标文件夹，或点击“Create a New Project”创建新的文件夹，再将文献添加至文件夹内。

如下图：



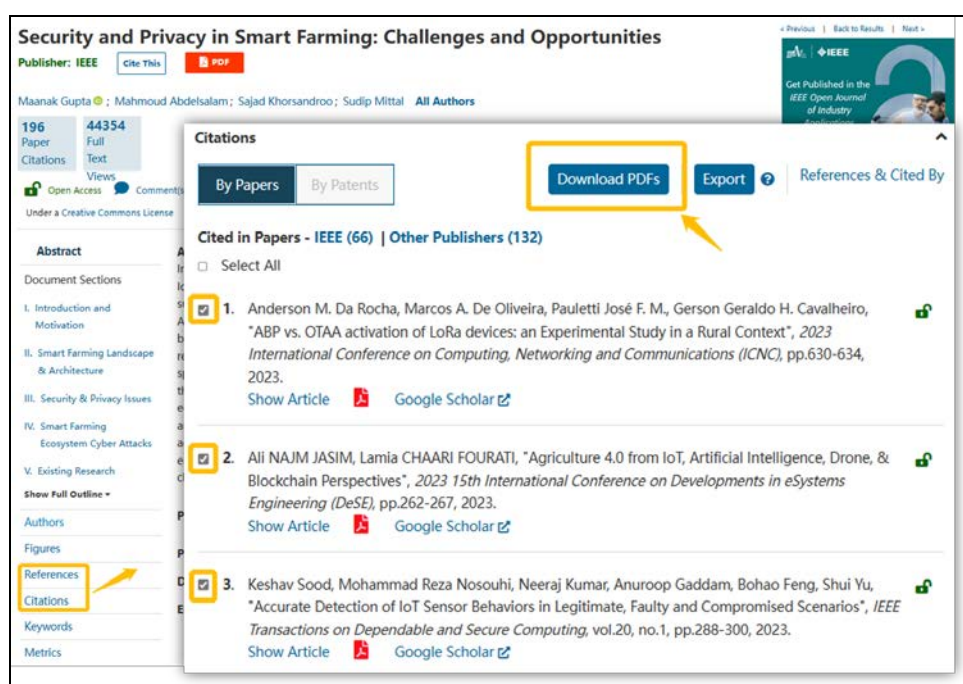
通过点击页面顶部“My Settings”下的“My Research Projects”，查看各文件夹具体文献。  
如下图：



支持批量导出参考文献和施引文献全文及相应信息

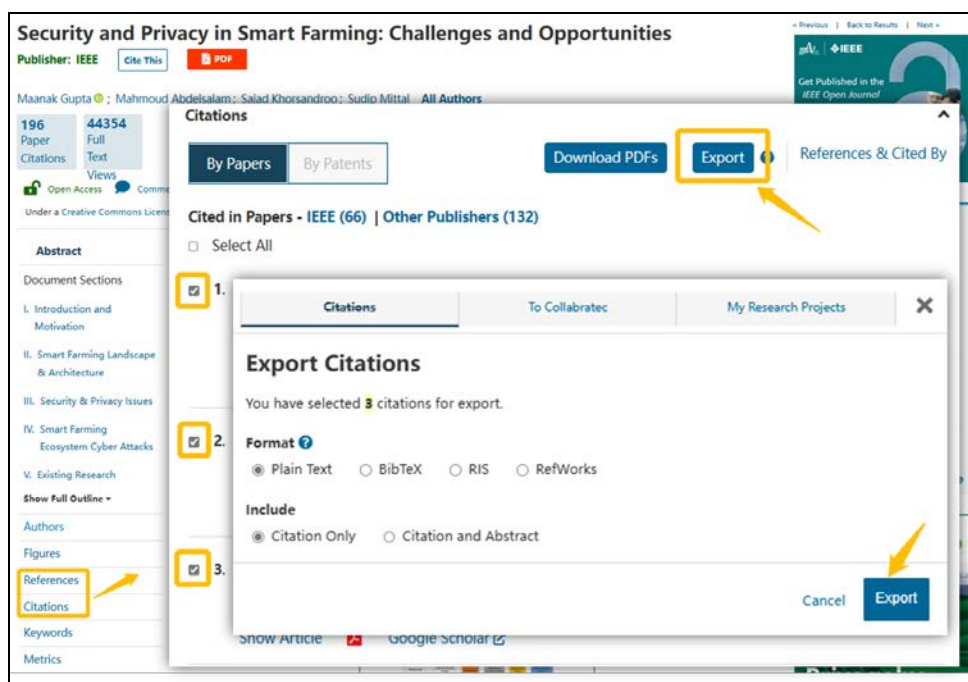
通过点击文摘页面左侧导航栏中的“Reference”或者“Citation”勾选想要下载的参考文献或施引文献，点击“Download PDFs”即可批量导出全文。

如下图：



通过点击文摘页面左侧导航栏中的“Reference”或者“Citation”，勾选目标参考文献或施引文献，点击“Export”，再选择目标格式后，即可批量导出参考文献或施引文献信息。

如下图：



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