MOTOROLA

SN54/74LS48

BCD TO 7-SEGMENT DECODER

BCD TO 7-SEGMENT DECODER

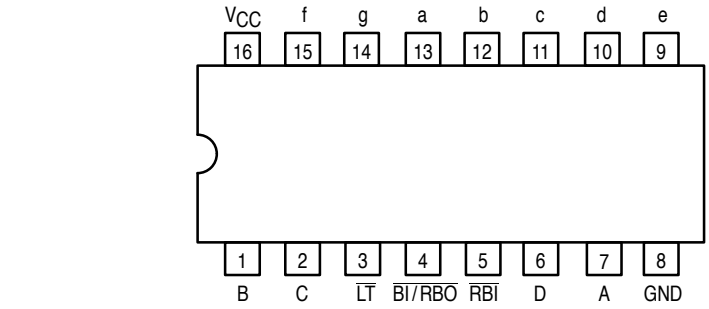
LOW POWER SCHOTTKY

The SN54/74LS48 is a BCD to 7-Segment Decoder consisting of NAND gates, input buffers and seven AND-OR-INVERT gates. Seven NAND gates and one driver are connected in pairs to make BCD data and its complement available to the seven decoding AND-OR-INVERT gates. The remaining NAND gate and three input buffers provide lamp test, blanking input/ripple-blanking input for the LS48.

The circuit accepts 4-bit binary-coded-decimal (BCD) and, depending on the state of the auxiliary inputs, decodes this data to drive other components. The relative positive logic output levels, as well as conditions required at the auxiliary inputs, are shown in the truth tables.

The LS48 circuit incorporates automatic leading and/or trailing edge zero-blanking control (RBI and RBO). Lamp Test (LT) may be activated any time when the BI/RBO node is HIGH. Both devices contain an overriding blanking input (BI) which can be used to control the lamp intensity by varying the frequency and duty cycle of the BI input signal or to inhibit the outputs.

- Lamp Intensity Modulation Capability (BI/RBO)
- Internal Pull-Ups Eliminate Need for External Resistors
- Input Clamp Diodes Eliminate High-Speed Termination Effects

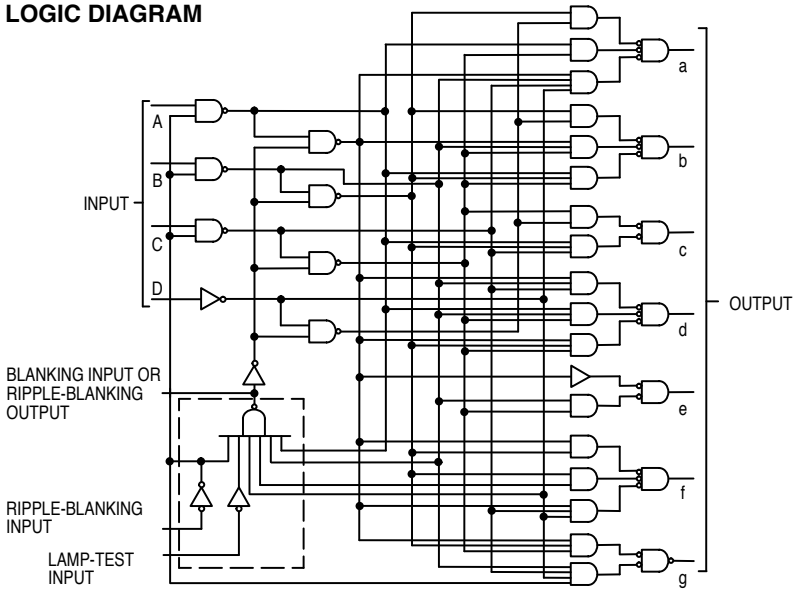


ORDERING INFORMATION

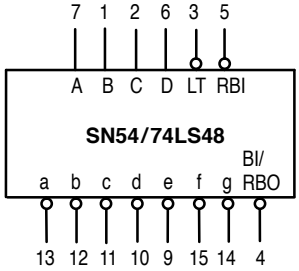
SN54LSXXJ	Ceramic
SN74LSXXN	Plastic
SN74LSXXD	SOIC

CONNECTION DIAGRAM DIP (TOP VIEW)

LOGIC DIAGRAM



LOGIC SYMBOL

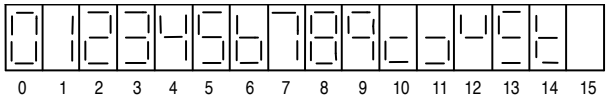


V_{CC} = PIN 16
GND = PIN 8

PIN NAMES

		LOADING (Note a)	
		HIGH	LOW
A, B, C, D	BCD Inputs	0.5 U.L.	0.25 U.L.
$\overline{RBI}$	Ripple-Blanking (Active Low) Input	0.5 U.L.	0.25 U.L.
$\overline{LT}$	Lamp-Test (Active Low) Input	0.5 U.L.	0.25 U.L.
$\overline{BI/RBO}$	Blanking Input or Ripple-	0.5 U.L.	0.75 U.L.
	Blanking Output (Active Low)	1.2 U.L.	2(1) U.L.
$\overline{BI}$	Blanking (Active Low) Input	0.5 U.L.	0.25 U.L.
		Open-Collector	3.75 (1.25) U.L. (48)

NOTES:
a) Unit Load (U.L.) = 40 μA HIGH/1.6 mA LOW
b) Outut current measured at V_{OUT} = 0.5 V
Output LOW drive factor is SN54LS/74LS48: 1.25 U.L. for Military (54), 3.75 U.L. for Commercial (74).



NUMERICAL DESIGNATIONS — RESULTANT DISPLAYS

TRUTH TABLE
SN54/74LS48

	INPUTS							OUTPUTS							
DECIMAL OR FUNCTION	$\overline{LT}$	$\overline{RBI}$	**D**	**C**	**B**	**A**	$\overline{BI/RBO}$	**a**	**b**	**c**	**d**	**e**	**f**	**g**	**NOTE**
0	H	H	L	L	L	L	H	H	H	H	H	H	H	L	1
1	H	X	L	L	L	H	H	L	H	H	L	L	L	L	1
2	H	X	L	L	H	L	H	H	H	L	H	H	L	H	
3	H	X	L	L	H	H	H	H	H	H	H	L	L	H	
4	H	X	L	H	L	L	H	L	H	H	L	L	H	H	
5	H	X	L	H	L	H	H	H	L	H	H	L	H	H	
6	H	X	L	H	H	L	H	L	L	H	H	H	H	H	
7	H	X	L	H	H	H	H	H	H	H	L	L	L	L	
8	H	X	H	L	L	L	H	H	H	H	H	H	H	H	
9	H	X	H	L	L	H	H	H	H	H	L	L	H	H	
10	H	X	H	L	H	L	H	L	L	L	H	H	L	H	
11	H	X	H	L	H	H	H	L	L	H	H	L	L	H	
12	H	X	H	H	L	L	H	L	H	L	L	L	H	H	
13	H	X	H	H	L	H	H	H	L	L	H	L	H	H	
14	H	X	H	H	H	L	H	L	L	L	H	H	H	H	
15	H	X	H	H	H	H	H	L	L	L	L	L	L	L	
$\overline{BI}$	X	X	X	X	X	X	L	L	L	L	L	L	L	L	2
$\overline{RBI}$	H	L	L	L	L	L	L	L	L	L	L	L	L	L	3
$\overline{LT}$	L	X	X	X	X	X	H	H	H	H	H	H	H	H	4

NOTES:
(1) BI/RBO is wired-AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO). The blanking out (BI) must be open or held at a HIGH level when output functions 0 through 15 are desired, and ripple-blanking input (RBI) must be open or at a HIGH level if blanking of a decimal 0 is not desired. X=input may be HIGH or LOW.
(2) When a LOW level is applied to the blanking input (forced condition) all segment outputs go to a LOW level, regardless of the state of any other input condition.
(3) When ripple-blanking input (RBI) and inputs A, B, C, and D are at LOW level, with the lamp test input at HIGH level, all segment outputs go to a HIGH level and the ripple-blanking output (RBO) goes to a LOW level (response condition).
(4) When the blanking input/ripple-blanking output (BI/RBO) is open or held at a HIGH level, and a LOW level is applied to lamp-test input, all segment outputs go to a LOW level.

GUARANTEED OPERATING RANGES

Symbol	Parameter		Min	Typ	Max	Unit
V_{CC}	Supply Voltage	54 74	4.5 4.75	5.0 5.0	5.5 5.25	V
T_A	Operating Ambient Temperature Range	54 74	−55 0	25 25	125 70	°C
I_{OH}	Output Current — High $\overline{a}$ to $\overline{g}$	54, 74			−100	μA
I_{OH}	Output Current — High $\overline{BI/RBO}$	54, 74			−50	μA
I_{OL}	Output Current — Low $\overline{a}$ to $\overline{g}$	54 74			2.0 6.0	mA
I_{OL}	Output Current — Low $\overline{BI/RBO}$ $\overline{BI/RBO}$	54 74			1.6 3.2	mA

DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol	Parameter		Limits Min	Limits Typ	Limits Max	Unit	Test Conditions	
V_{IH}	Input HIGH Voltage		2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V_{IL}	Input LOW Voltage	54			0.7	V	Guaranteed Input LOW Voltage for All Inputs	
		74			0.8			
V_{IK}	Input Clamp Diode Voltage				−1.5	V	V_{CC} = MIN, I_{IN} = −18 mA	
V_{OH}	Output HIGH Voltage		2.4	4.2		μA	V_{CC} = MIN, I_{OH} = −50 μA, V_{IN} = V_{IH} or U.L. per Truth Table	
I_O	Output Current $\overline{a}$ to $\overline{g}$		−1.3	−2.0		mA	V_{CC} = MIN, V_O = 0.85 V Input Conditioner as for V_{OH}	
V_{OL}	Output LOW Voltage a to g	54, 74			0.4	V	I_{OL} = 2.0 mA	V_{CC} = MIN, V_{IH} = 2.0 V V_{IL} = V_{IL} MAX
		74			0.5	V	I_{OL} = 6.0 mA	
V_{OL}	Output LOW Voltage $\overline{BI/RBO}$	54, 74			0.4	V	I_{OL} = 1.6 mA	V_{CC} = MAX, V_{IH} = 2.0 V V_{IL} = V_{IL} MAX
		74			0.5	V	I_{OL} = 3.2 mA	
I_{IH}	Input HIGH Current (Except $\overline{BI/RBO}$)				20	μA	V_{CC} = MAX, V_{IN} = 2.7 V	
					0.1	mA	V_{CC} = MAX, V_{IN} = 7.0 V	
I_{IL}	Input LOW Current (Except $\overline{BI/RBO}$)				−0.4	mA	V_{CC} = MAX, V_{IN} = 0.4 V	
I_{IL}	Input LOW Current $\overline{BI/RBO}$				−1.2	mA	V_{CC} = MAX, V_{IN} = 0.4 V	
I_{CC}	Power Supply Current			25	38	mA	V_{CC} = MAX	
I_{OS}	Short Circuit Current $\overline{BI/RBO}$ (Note 1)		−0.3		−2.0	mA	V_{CC} = MAX	

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

AC CHARACTERISTICS (V_{CC} = 5.0 V, T_A = 25°C)

Symbol	Parameter	Limits Min	Limits Typ	Limits Max	Unit	Test Conditions
t_{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output from A Input			100	ns	C_L = 15 pF, R_L = 4.0 kΩ
t_{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output from A Input			100	ns	
t_{PHL}	Propagation Delay Time, HIGH-to-LOW Level Output from $\overline{RBI}$ Input			100	ns	C_L = 15 pF, R_L = 6.0 kΩ
t_{PLH}	Propagation Delay Time, LOW-to-HIGH Level Output from $\overline{RBI}$ Input			100	ns	