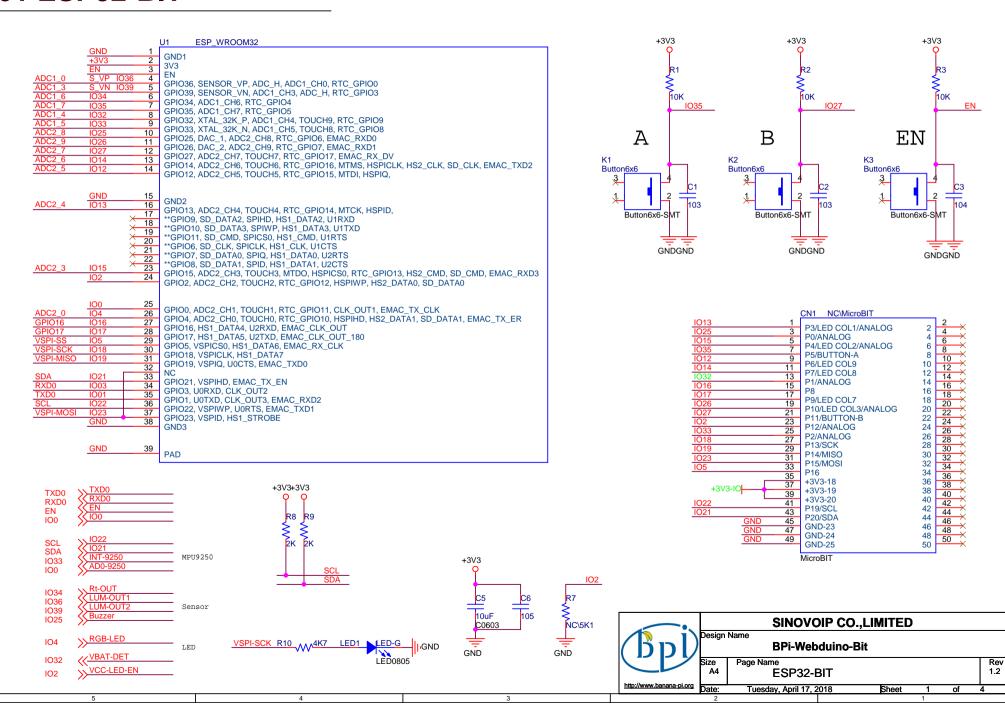
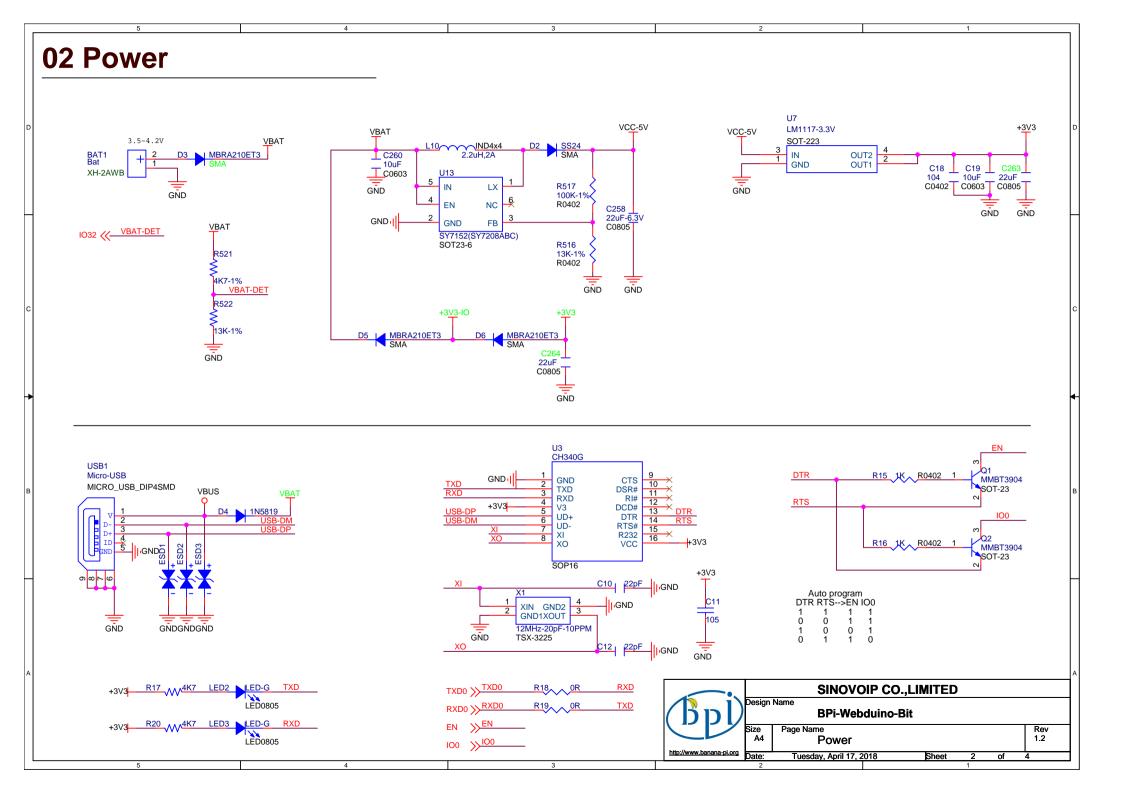
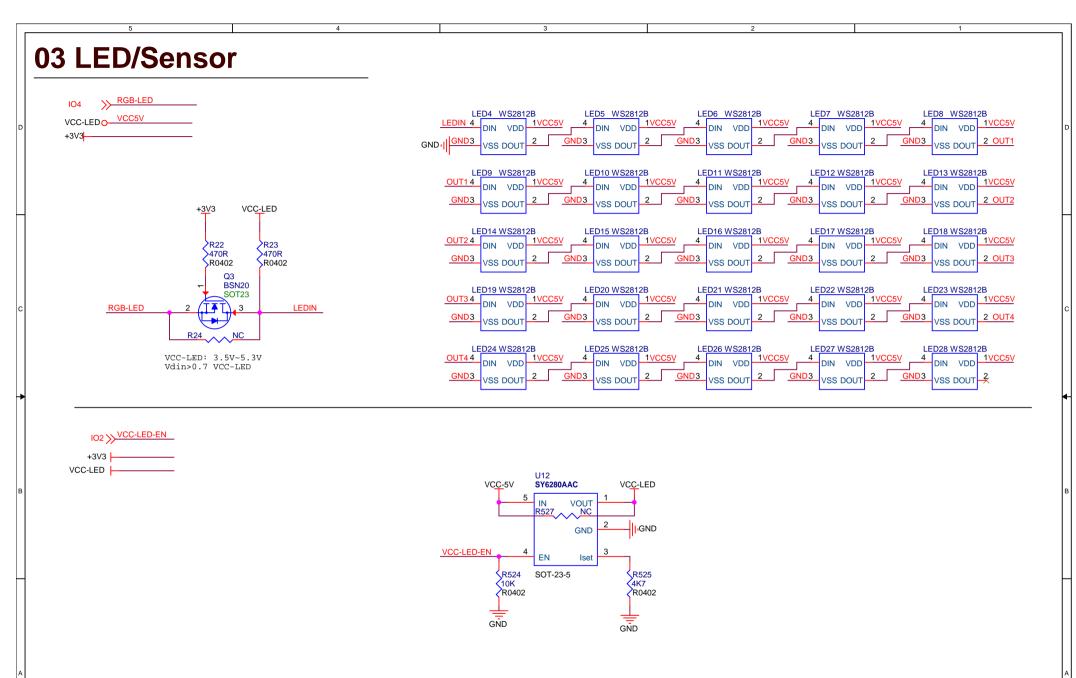
01 ESP32-BIT

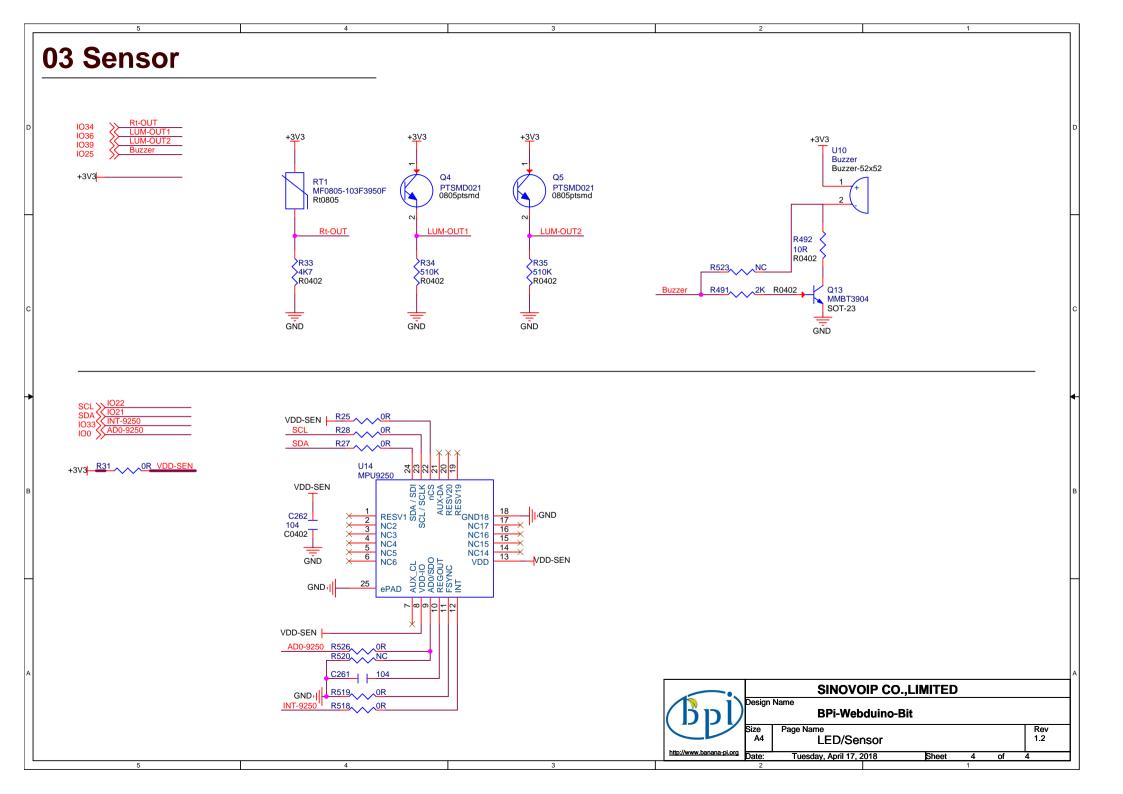
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		Design Name BPi-Webduino-Bit							
		Size	Page Name					Rev	
		A4	LED/Ser	nsor				1.2	
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REVISION HISTORY

Schematics Index:

Revision	Description	Date	Drawn	Checked	Approved
Ver 1.0	Release version	2018-01			
Ver 1.1		2018-03			
Ver 1.2	1. CN1 的P1改为IO32(复用VBAT-DET) 2. CN1 PIN35、37、39的网络改为+3V3-IO(可以输入、输出) 3. +3V3到VBAT增加两个肖特基二极管(CN1输入也可以点亮LED) 4. USB口的输入改到VBAT(防止第3点改完后大电流) 5. 增加C263、C264(电压低情况下,改善ESP32容易复位的情况) 6. D3改为SMA封装,使用2A的二极管	2018-04			

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(Bpl)	Design	Name BPi-Berry				
	Size A3	Page Name REVISION HISTORY				Rev 1.2
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