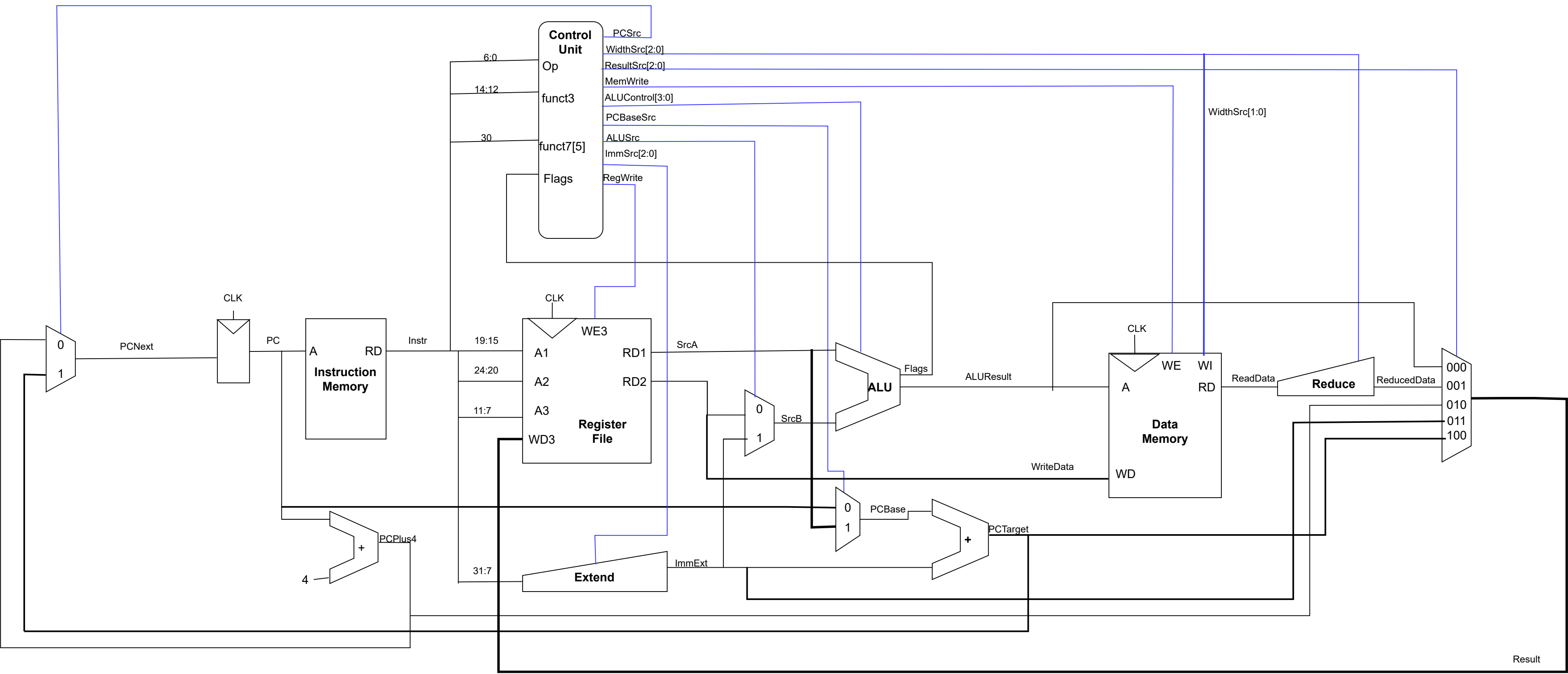
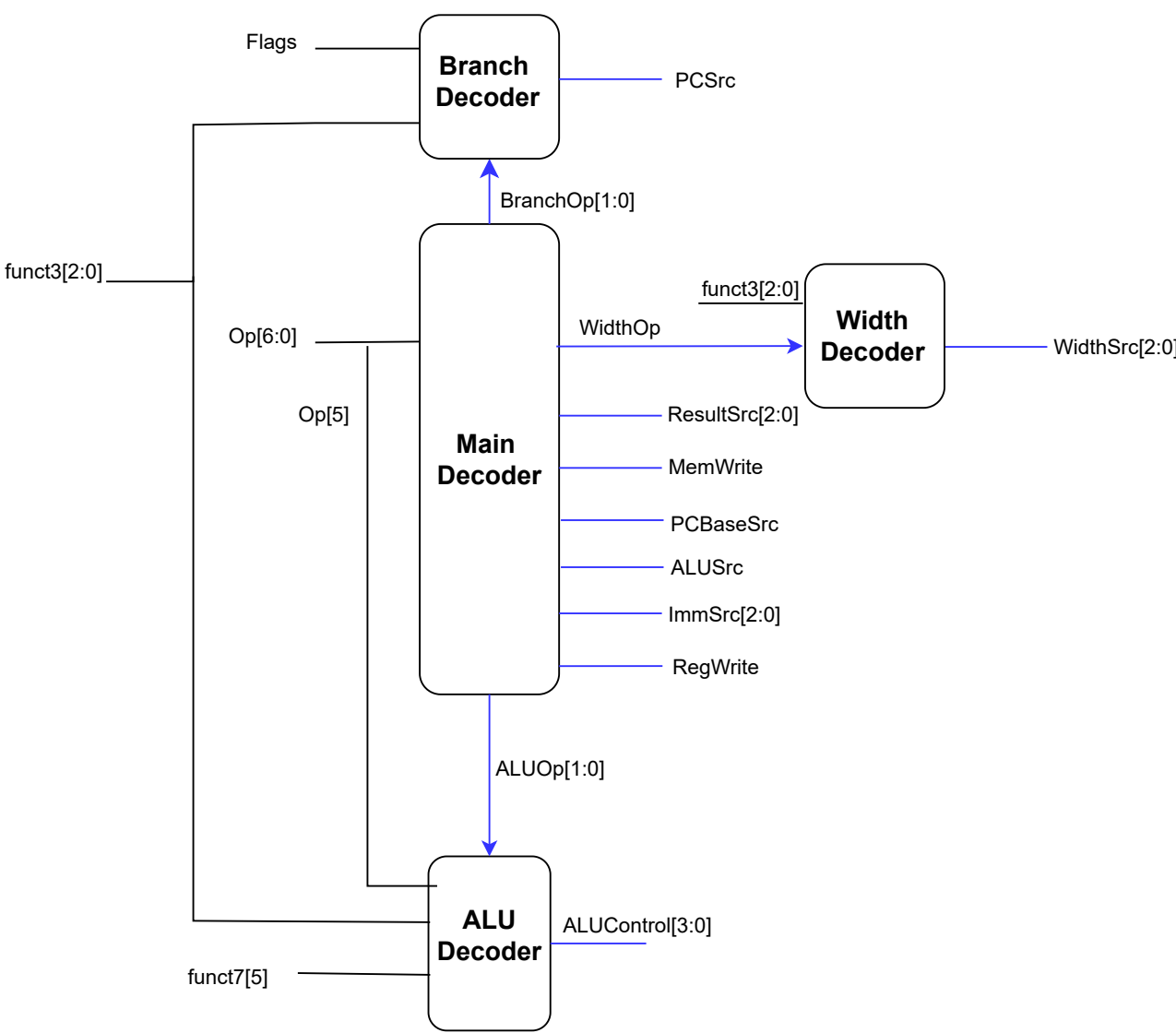


Microarchitecture



Control Unit



Top Level System Schematic

