

1. Finalized Pin Assignments Overview

a. TinyZero Processor (ATSAMD21G18A)

- I/O Pins Available: 20
- Key SPI Pins:
 - MOSI: Pin 11
 - MISO: Pin 12
 - SCLK: Pin 13
- I2C Pins:
 - SDA (AD4): Pin 10
 - SCL (AD5): Pin 47
- Digital Interrupt Pin:
 - IO3: Pin 14

b. nRF24 Transceiver

- SPI Interface:
 - SPI_IRQ: Pin 2
 - SPI_CS (Chip Select): Pin 7
 - CE (Chip Enable): Pin 9
 - MOSI: Pin 11
 - MISO: Pin 12
 - SCLK: Pin 13

c. MicroSD Card

- SPI Interface:
 - CS (Chip Select): Reassigned to Pin 8
 - MOSI: Pin 13
 - MISO: Pin 12
 - SCLK: Pin 11

d. BMA250 Accelerometer

- I2C Interface:
 - SDA (AD4): Pin 10

- SCL (AD5): Pin 47
 - Interrupt Pin:
 - INT1: IO3 (Pin 14)
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2. Pin Conflict Analysis

a. SPI Pin Conflicts Between nRF24 and MicroSD Card

- Pins 11 (MOSI) and 13 (SCLK):
 - nRF24:
 - MOSI: Pin 11
 - SCLK: Pin 13
 - MicroSD:
 - MOSI: Pin 13
 - SCLK: Pin 11

Conflict: Both devices are assigned to the same SPI pins but with **swapped functionalities**. This leads to **signal conflicts**, causing both devices to malfunction.

- Pin 12 (MISO):
 - **Shared Use:** Both nRF24 and MicroSD use MISO (Pin 12).

Note: Sharing MISO is acceptable **only** if each device has a unique **Chip Select (CS)** pin and is **not** active simultaneously.

b. I2C and SPI Pin Conflict

- Pin 10:
 - MicroSD: Initially used as CS (Pin 10)
 - BMA250 Accelerometer: Uses SDA (AD4, Pin 10)

Conflict Resolved:

- MicroSD CS Pin Reassigned to Pin 8.
- Pin 10 is now exclusively used for I2C SDA, eliminating the conflict.

c. Digital Interrupt Pin (IO3 - Pin 14)

- Usage:
 - BMA250 Accelerometer's INT1

Conflict Check:

- **nRF24 Transceiver:** Does not use Pin 14.
- **MicroSD Card:** Does not use Pin 14.
- **I2C (BMA250):** Uses Pins 10 (SDA) and 47 (SCL), not Pin 14.

Conclusion: Pin 14 (IO3) is dedicated solely to the BMA250 Accelerometer's INT1 and does not conflict with any other peripherals.

3. Addressing Potential Software Conflicts

a. SPI and I2C Libraries

- **nRF24 Library:**
 - Utilizes hardware SPI pins (11, 12, 13) with CS Pin 7.
- **SD Library:**
 - Utilizes hardware SPI pins (11, 12, 13) with CS Pin 8.
- **BMA250 Library:**
 - Utilizes I2C pins (10, 47) and IO3 (Pin 14) for interrupts.

Ensure that:

- **SPI Libraries:** Only one device is active on the SPI bus at any given time by correctly managing their CS pins (nRF24 on Pin 7, MicroSD on Pin 8).
- **I2C Libraries:** Exclusively use Pins 10 and 47 without interference from SPI devices.

b. Library Initialization and Usage

- **nRF24 Initialization:**
 - Ensure that Chip Select (CS) Pin 7 is correctly assigned and managed.
- **MicroSD Initialization:**
 - After reassigning CS Pin to 8, ensure the SD library is initialized with the correct CS pin.
- **BMA250 Initialization:**
 - Ensure that the INT1 pin (Pin 14) is correctly configured and **not used** by other libraries or functions.

c. Code Snippets Confirmation

Review the provided code snippets to ensure that:

1. MicroSD Library Initialization:

cpp



```
#include <SPI.h> #include <SD.h> const int SD_CS_PIN = 8; // Reassigned CS Pin void
setup() { Serial.begin(9600); // Initialize nRF24 as usual // ... // Initialize
MicroSD with new CS pin if (!SD.begin(SD_CS_PIN)) { Serial.println("SD initialization
failed!"); while (1); } Serial.println("SD initialized."); // Initialize I2C for
BMA250 Wire.begin(); // Initialize BMA250 // ... } void loop() { // Your main code }
```

2. BMA250 Initialization:

cpp



```
#include <Wire.h> #include "BMA250.h" BMA250 accelerometer; const int ACCEL_INT1_PIN
= 14; // I/O void setup() { Serial.begin(9600); // Initialize I2C Wire.begin(); //
Initialize BMA250 with INT1 on Pin 14 if (accelerometer.begin(BMA250_range_2g,
BMA250_update_time_16ms) != 0) { Serial.println("BMA250 initialization failed!");
while (1); } Serial.println("BMA250 initialized."); // Initialize SPI devices // ...
} void loop() { accelerometer.read(); Serial.print("X: ");
Serial.print(accelerometer.X); Serial.print(" Y: "); Serial.print(accelerometer.Y);
Serial.print(" Z: "); Serial.println(accelerometer.Z); delay(1000); }
```

Key Points:

- **No Overlapping Pin Assignments:** After reassigning the MicroSD CS pin to Pin 8, Pins 10, 14, and 47 are exclusively used for I2C and INT1 without interference.
- **SPI Bus Management:** Both nRF24 and MicroSD are connected to the same SPI bus (Pins 11, 12, 13) but have unique CS pins (7 and 8), ensuring only one device is active at a time.
- **INT1 (Pin 14):** Dedicated solely to the BMA250 Accelerometer, no other devices utilize this pin.

4. Final Pin Mapping Summary

Peripheral	Function	Pin
nRF24 Transceiver	SPI_IRQ	2
	SPI_CS	7
	CE	9
	MOSI	11

Peripheral	Function	Pin
	MISO	12
	SCLK	13
MicroSD Card	CS (Chip Select)	8
	MOSI	11
	MISO	12
	SCLK	13
BMA250 Accelerometer	SDA (AD4)	10
	SCL (AD5)	47
	INT1	14

5. Additional Recommendations

a. Verify All Connections

- **Double-Check Wiring:** Ensure that each peripheral is connected to the correct pin as per the final pin mapping.
- **Secure Connections:** Use proper connectors and ensure that there are no loose wires that might cause intermittent connections.

b. Test Peripherals Individually

- **Isolate and Test:**
 1. **nRF24 Transceiver:** Verify SPI communication independently.
 2. **MicroSD Card:** Verify SPI communication with the new CS pin independently.
 3. **BMA250 Accelerometer:** Verify I2C communication and INT1 functionality independently.
- **Integration Testing:** Once each peripheral functions correctly on its own, integrate them together and test the entire system.

c. Manage SPI Bus Carefully

- **Ensure Proper CS Management:** Make sure that **only one device's CS pin is LOW** at any given time. This prevents SPI devices from interfering with each other.

Example:

cpp



```
// Activate nRF24 digitalWrite(nRF24_CS_PIN, LOW); // Deactivate MicroSD
digitalWrite(SD_CS_PIN, HIGH); // ... communicate with nRF24 ... // Deactivate nRF24
digitalWrite(nRF24_CS_PIN, HIGH); // Activate MicroSD digitalWrite(SD_CS_PIN, LOW);
// ... communicate with MicroSD ...
```

d. Consider Using Pull-Up/Pull-Down Resistors

- **Stabilize I2C Lines:** Ensure that SDA (Pin 10) and SCL (Pin 47) have appropriate pull-up resistors (typically 4.7kΩ) to maintain signal integrity.
- **SPI Lines:** While SPI lines are typically driven actively, ensure that MOSI (Pin 11), MISO (Pin 12), and SCLK (Pin 13) are not left floating when not in use.

e. Power Supply Considerations

- **Ensure Adequate Power:** The MicroSD card can draw up to 100mA or more during write operations. Ensure that your power supply can handle the combined current requirements of all peripherals.
- **Avoid Coin Cells:** As specified, do **not** use coin cells for powering the TinyZero when using the MicroSD card due to its high current draw.

f. Software Considerations

- **Library Dependencies:** Ensure that all libraries used (nRF24, SD, BMA250) are compatible with the TinyZero and do not have conflicting dependencies or requirements.
- **Interrupt Handling:**
 - **BMA250 INT1:** If using interrupts, ensure that the interrupt service routine (ISR) for Pin 14 (IO3) does not conflict with other ISRs or critical sections in your code.

g. Addressing Schemes and Network Configuration

- **nRF24 Addressing:**
 - Ensure that the **network addresses** for the nRF24 modules are **unique** and correctly configured to avoid communication issues.
- **I2C Addressing:**
 - The BMA250 has a fixed I2C address (0x18 or 0x19). Ensure that **no other I2C devices** on the bus share this address to prevent conflicts.

6. Final Confirmation

a. No Conflicts with Pin 14 (IO3)

- **Usage:** Exclusively used for BMA250 Accelerometer's INT1.
- **No Overlap:** No other peripherals or code segments use Pin 14, ensuring no conflicts.

b. No Conflicts with SDA (Pin 10) and SCL (Pin 47)

- **SDA (Pin 10):** Exclusively used for I2C SDA by the BMA250 Accelerometer.
- **SCL (Pin 47):** Exclusively used for I2C SCL by the BMA250 Accelerometer.
- **No Overlap:** Pin 10 was initially conflicted with MicroSD CS, but after reassignment to Pin 8, Pin 10 is free for I2C SDA without conflicts.

c. SPI Bus Management

- **MOSI (Pin 11), MISO (Pin 12), SCLK (Pin 13):** Shared between nRF24 and MicroSD but managed via unique CS pins (7 for nRF24, 8 for MicroSD).

No Direct Pin Conflicts: As long as only one device is active on the SPI bus at a time, there are no pin conflicts.

7. Final Recommendations

1. Reassign MicroSD CS Pin to Pin 8:

- **Hardware:** Connect the MicroSD CS line to Pin 8.
- **Software:** Initialize the SD library with Pin 8 as the CS pin.

2. Ensure Proper SPI Bus Management:

- **Only One Active Device:** Activate nRF24 by setting CS Pin 7 LOW and deactivate MicroSD by setting CS Pin 8 HIGH, and vice versa.

Example:

cpp

 Copy code

```
#define NRF24_CS_PIN 7 #define SD_CS_PIN 8 void activateNRF24() {
digitalWrite(SD_CS_PIN, HIGH); // Deactivate MicroSD digitalWrite(NRF24_CS_PIN, LOW);
// Activate nRF24 } void deactivateNRF24() { digitalWrite(NRF24_CS_PIN, HIGH); //
Deactivate nRF24 } void activateSD() { digitalWrite(NRF24_CS_PIN, HIGH); //
Deactivate nRF24 digitalWrite(SD_CS_PIN, LOW); // Activate MicroSD } void
deactivateSD() { digitalWrite(SD_CS_PIN, HIGH); // Deactivate MicroSD }
```

3. Verify Interrupt Pin Configuration:

- **Pin 14 (IO3):** Dedicated to **BMA250 INT1**, ensure no other devices use this pin.
4. **Confirm No Overlapping I2C Addresses:**
 - **BMA250 I2C Address:** Ensure no other I2C devices share the same address (0x18 or 0x19).
 5. **Test the Entire Setup:**
 - **Individual Peripheral Testing:** Verify each peripheral works independently.
 - **Integrated Testing:** Test all peripherals together to ensure seamless communication without interference.
 6. **Monitor Power Consumption:**
 - **MicroSD Card:** Ensure a stable power supply capable of handling **100mA or more** during write operations.
 7. **Review and Optimize Code:**
 - **Ensure Proper Library Usage:** Check that each library is correctly initialized and managed without attempting to control conflicting pins.
 - **Interrupt Handling:** Ensure that ISRs are efficiently handled and do not block critical code segments.
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Conclusion

After reassigning the MicroSD Card's CS pin from Pin 10 to Pin 8 and dedicating Pin 14 (IO3) solely to the BMA250 Accelerometer's INT1, no further pin conflicts exist in your setup. The SCL (Pin 47) and SDA (Pin 10) are exclusively used for the I2C interface of the BMA250, and Pin 14 is dedicated to handling interrupts from the accelerometer.

Key Points:

- **No Additional Conflicts:** With the reassignment, Pin 14, Pin 10, and Pin 47 are free from conflicts with other peripherals.
- **SPI Bus Integrity:** Proper management of CS pins (7 and 8) ensures that nRF24 and MicroSD Card can coexist on the same SPI bus without interference.
- **Dedicated I2C Pins:** Pin 10 (SDA) and Pin 47 (SCL) are exclusively used for the BMA250 Accelerometer.

Next Steps:

1. **Implement Pin Reassignment:** Physically move the MicroSD CS pin to Pin 8 and update your code accordingly.
2. **Update and Verify Code:** Ensure that all library initializations reflect the new pin assignments.

3. **Conduct Thorough Testing:** Test each peripheral individually, followed by integrated testing to confirm the absence of conflicts.
4. **Monitor System Performance:** Keep an eye on power consumption and communication reliability to ensure long-term stability.