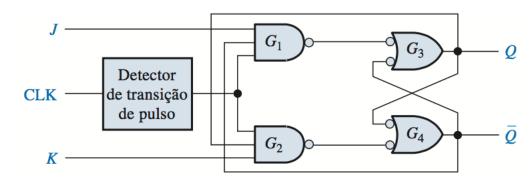
CIRCUITOS DIGITAIS

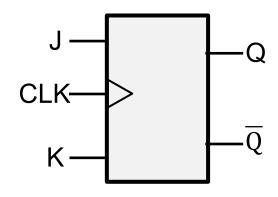
CIRCUITOS SEQUENCIAIS

Prof. Marcelo Grandi Mandelli

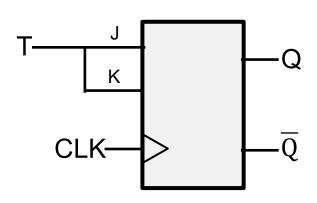
mgmandelli@unb.br

CLK	J	K	Q _{t+1}
≠↑	X	X	Q _t
↑	0	0	Qt
↑	0	1	0
↑	1	0	1
1	1	1	$\overline{Q_t}$

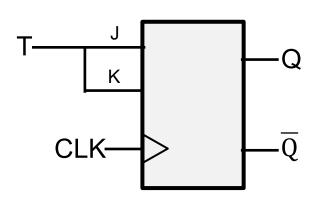






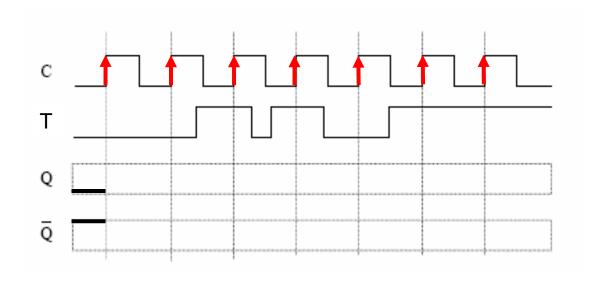


CLK	J	K	Q _{t+1}
≠↑	X	X	Q _t
↑	0	0	Qt
1	0	1	0
1	1	0	1
1	1	1	$\overline{Q_t}$

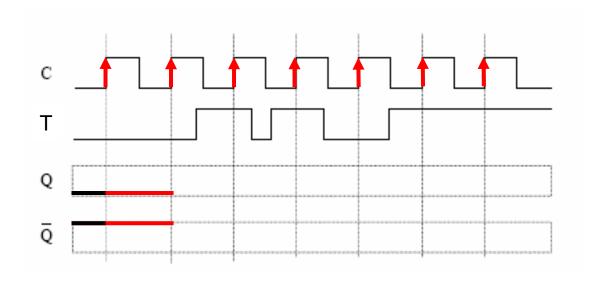


CLK	Т	Q _{t+1}
≠ ↑	X	Q t
↑	0	Q _t
1	1	$\overline{\mathbf{Q}_{t}}$

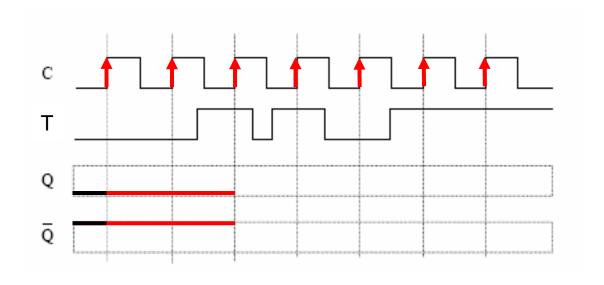
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
1	1	$\overline{Q_t}$



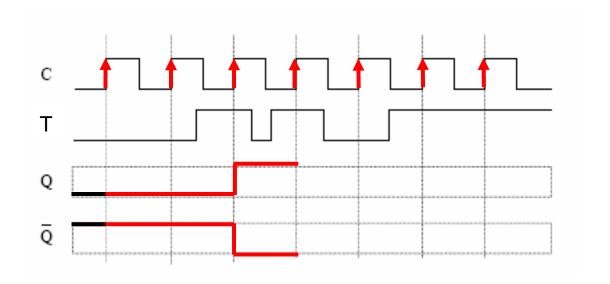
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
<u></u>	1	$\overline{Q_t}$



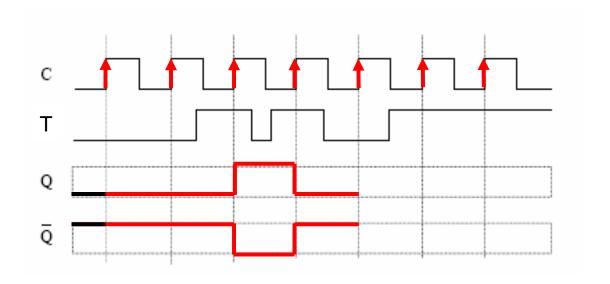
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
↑	1	$\overline{Q_t}$



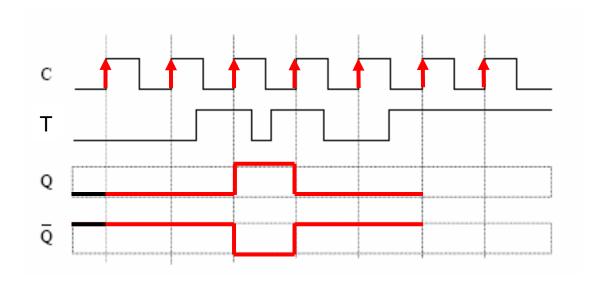
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
↑	1	$\overline{Q_t}$



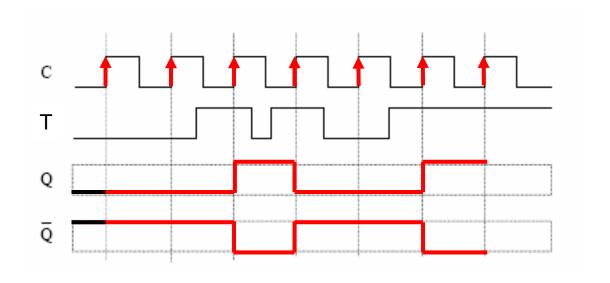
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
1	1	$\overline{Q_t}$



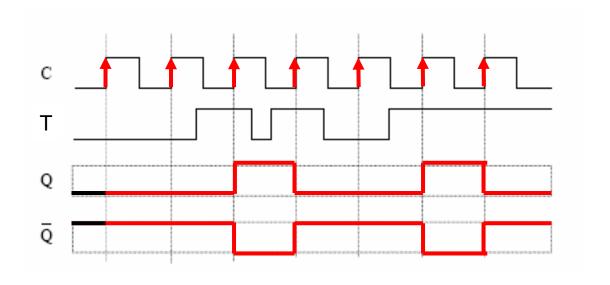
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
1	1	$\overline{Q_t}$

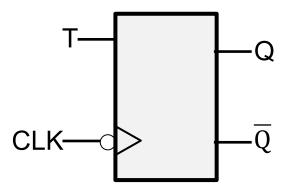


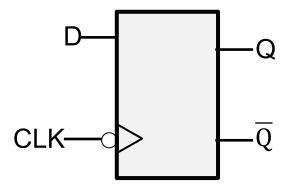
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
1	1	$\overline{Q_t}$

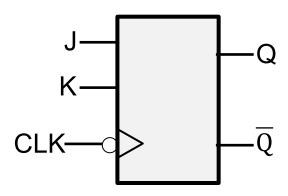


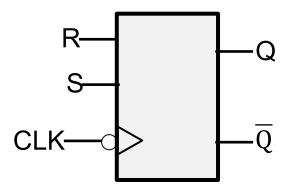
CLK	Т	Q _{t+1}
≠↑	X	\mathbf{Q}_{t}
↑	0	\mathbf{Q}_{t}
1	1	$\overline{Q_t}$

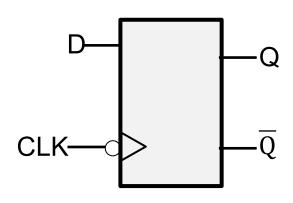






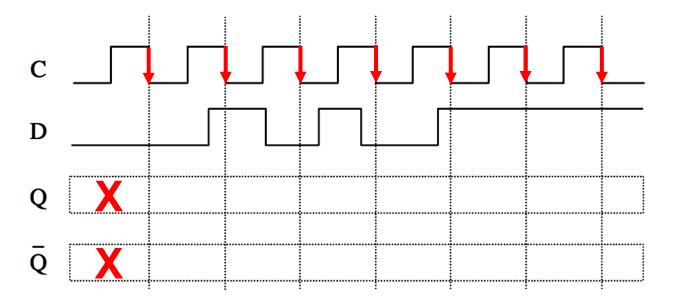


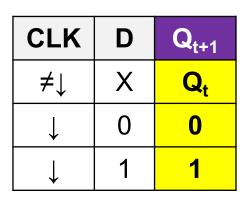


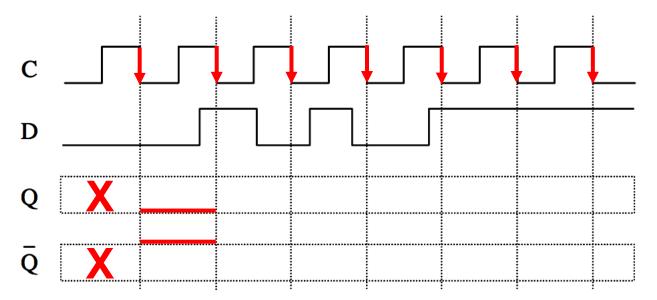


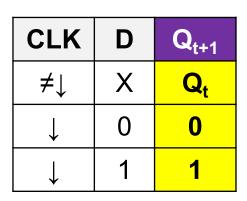
CLK	D	Q _{t+1}
≠↓	X	Qt
	0	0
	1	1

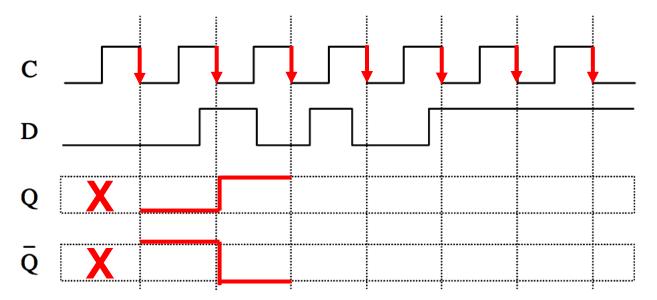
CLK	D	Q _{t+1}
≠↓	X	Qt
	0	0
	1	1

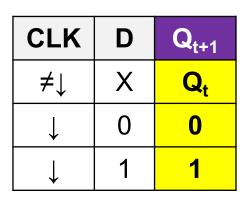


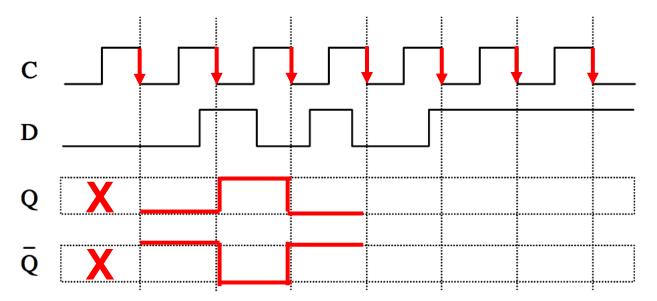


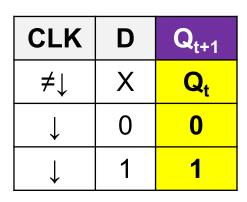


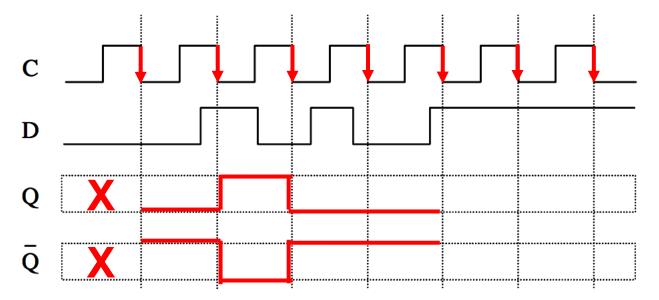


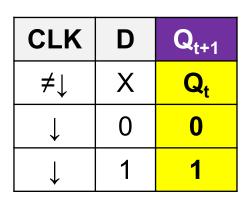


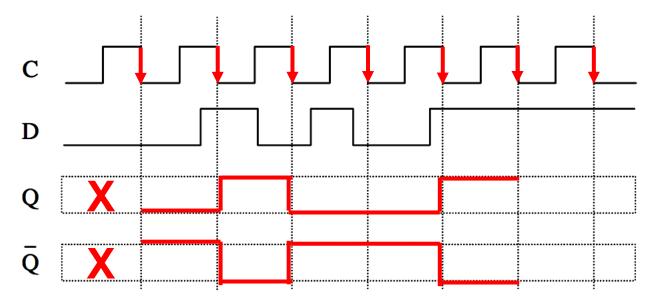




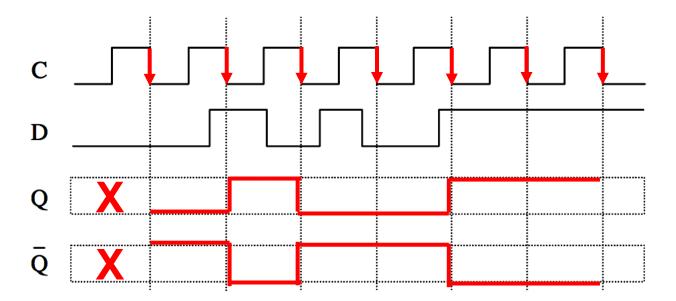


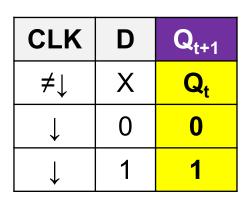


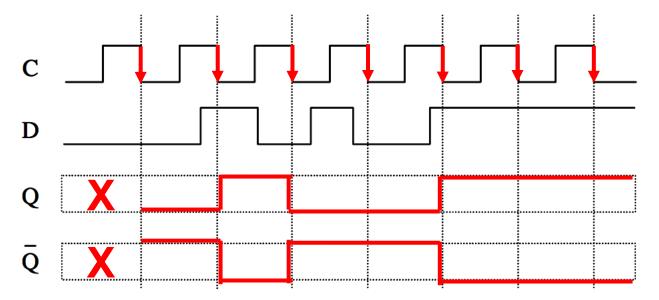


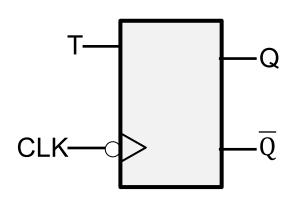


CLK	D	Q _{t+1}
≠↓	X	Qt
	0	0
<u></u>	1	1

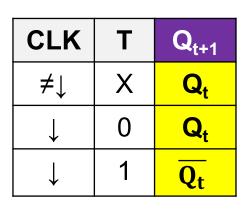


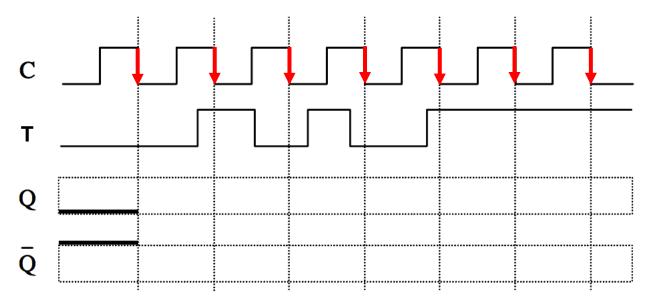


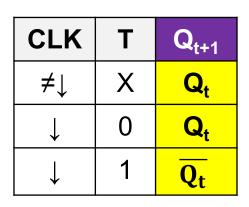


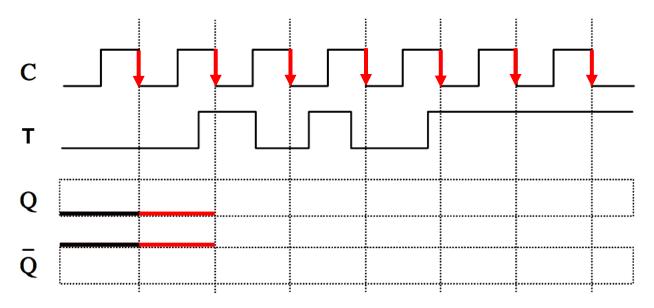


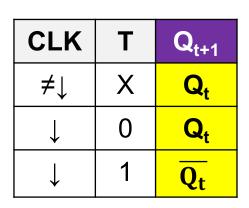
CLK	Т	Q _{t+1}
≠↓	X	Q _t
	0	Q _t
1	1	$\overline{\mathbf{Q}_{\mathrm{t}}}$

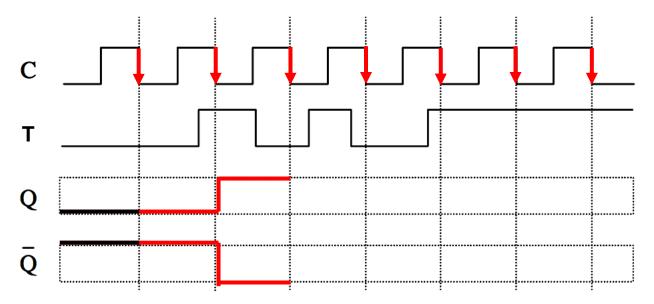


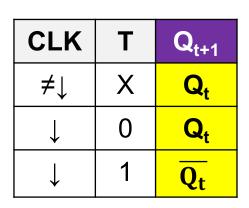


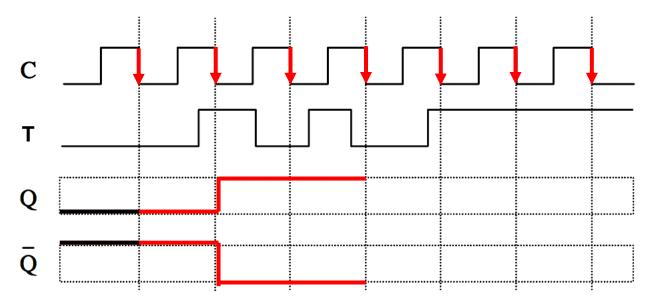


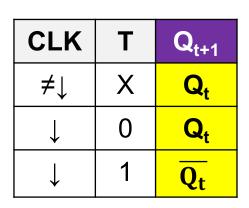


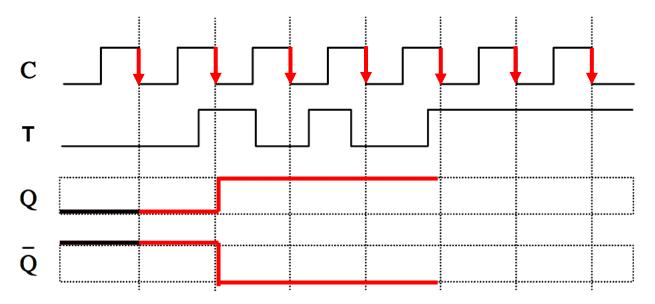


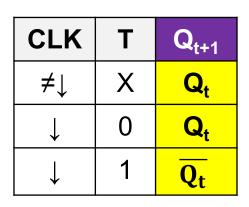


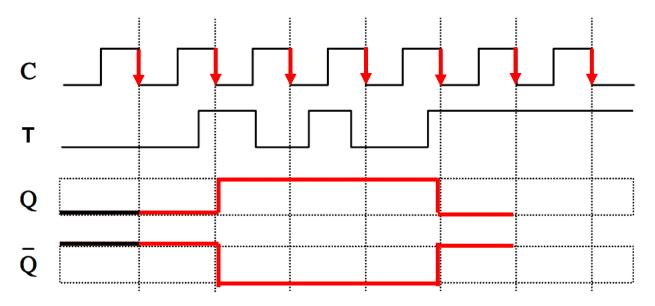


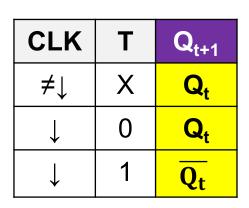


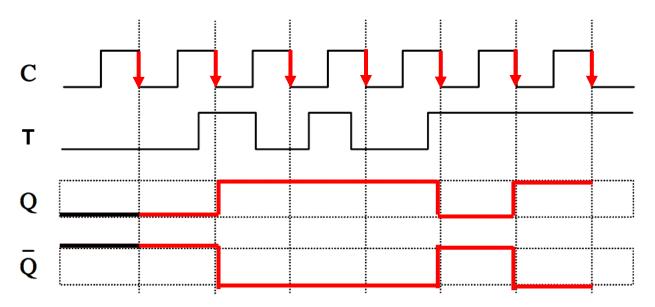


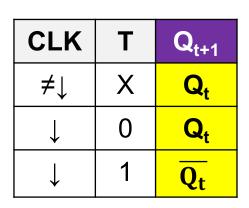


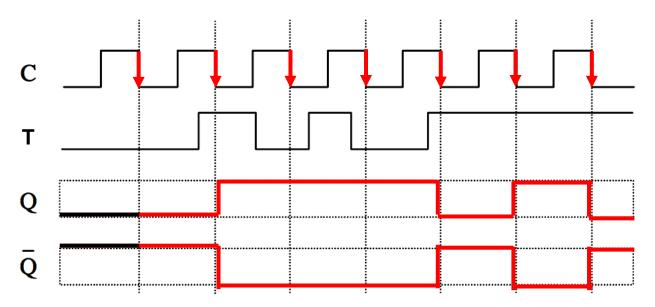


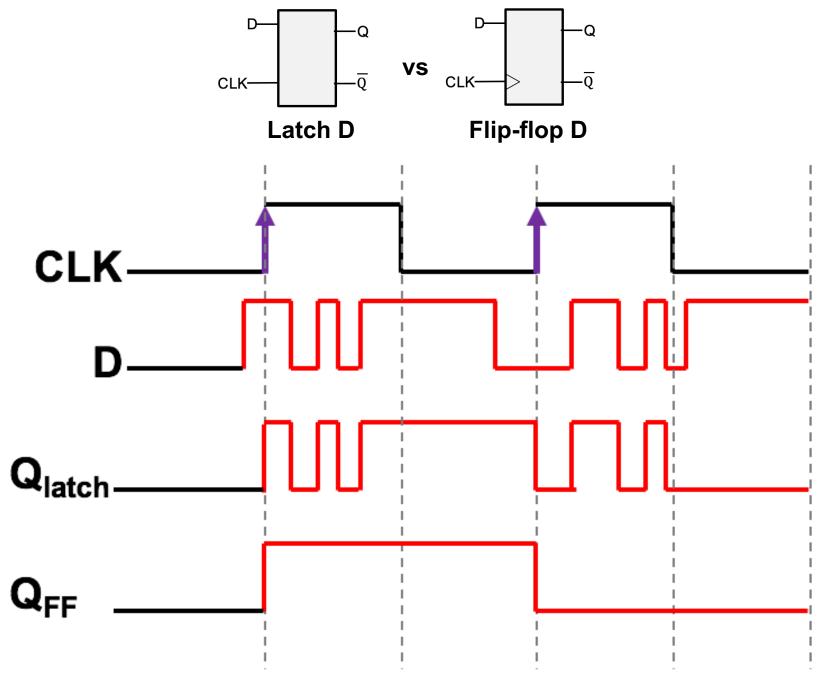






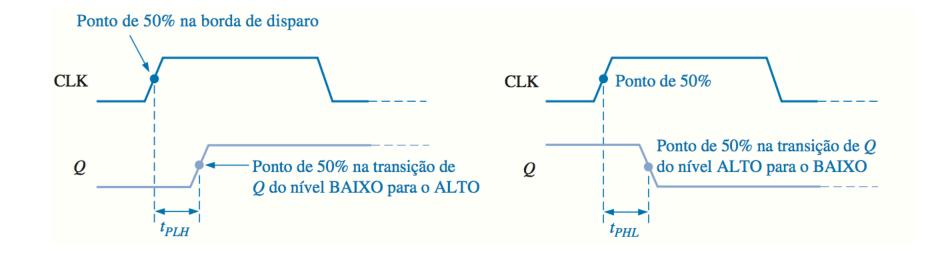






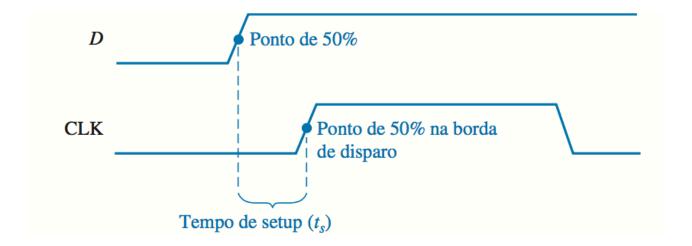
Circuitos Digitais – Prof. Marcelo Grandi Mandelli Slide **32**

Tempo de Atraso de Propagação



Tempo de Setup (Preparação)

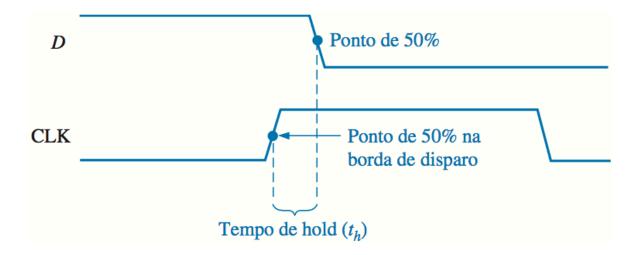
tempo de setup (t_s) → tempo mínimo antes da borda do clock que a entrada deve estar estável



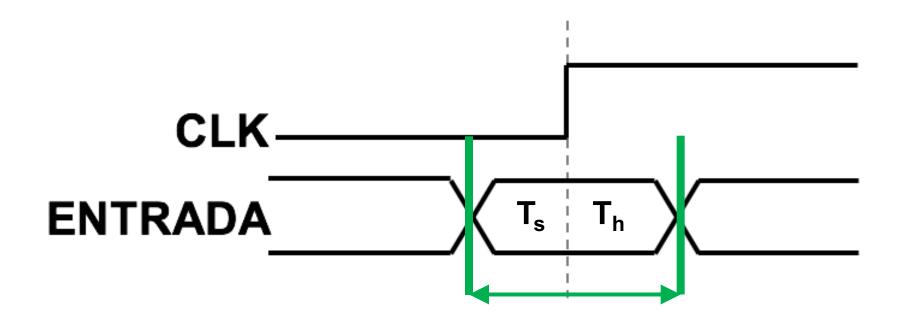
A entrada deve manter o mesmo nível lógico antes da borda de disparo do clock por um tempo igual ou maior que t_s

Tempo de Hold (Manutenção)

tempo de hold (t_h) → tempo mínimo depois da borda do clock que a entrada deve estar estável



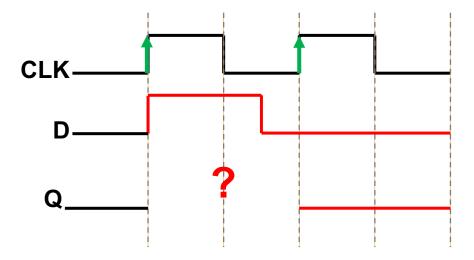
 A entrada deve manter o mesmo nível lógico depois da borda de disparo do clock por um tempo igual ou maior que t_h



A entrada deve permanecer estável dentro da janela

Meta-estabilidade

□ violação nos tempos de setup e hold → flip-flop pode entrar em um estado onde a sua saída é imprevisivel → meta-estabilidade



Após o estado de meta-estabilidade, o flip-flop atinge o valor '1' ou '0'

Janela de tempo

	Metastability Window 74xx74 Flip Flops			
Device	t _(set-up) nS	t _(hold) nS	t _(total) nS	
74C74	100.0	0.0	100.0	
74HC74	20.0	0.0	20.0	
74HCT74	20.0	0.0	20.0	
7474	20.0	5.0	25.0	
74ALS74	15.0	10.0	25.0	
74LS74	20.0	0.0	20.0	
74874	3.0	2.0	5.0	
74LS74	4.5	0.0	4.5	
74AC74	4.0	0.5	4.5	
74ACT74	3.0	1.0	4.0	
74ACTQ74	3.0	1.5	4.5	

Entradas de Preset e Clear

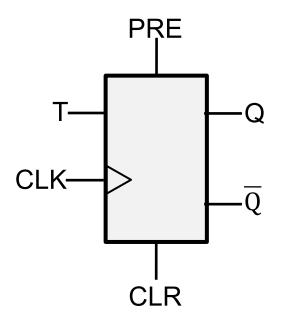
 A maioria dos flip-flops também tem entradas assíncronas

Essas entradas afetam o estado do flip-flop independente do clock

□ Elas são normalmente denominadas **preset** (**PRE**) e **clear** (**CLR**)

Entradas de Preset e Clear

- □ **PRESET** = 1 irá **setar** o flip-flop \rightarrow Q_{t+1} = 1
- □ CLEAR = 1 irá resetar o flip-flop \rightarrow Q_{t+1} = 0

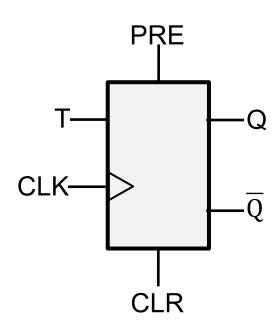


PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO

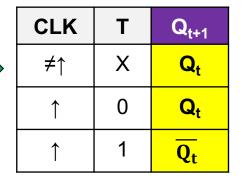
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO

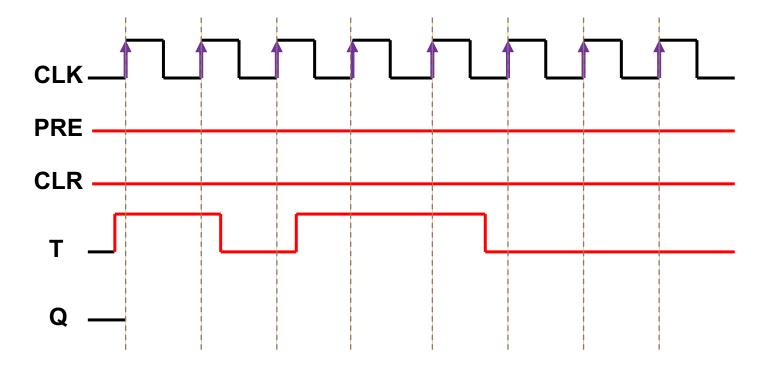


CLK	Т	Q _{t+1}
≠↑	X	Q _t
↑	0	Q _t
↑	1	$\overline{\mathbf{Q}_{t}}$

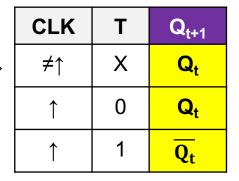


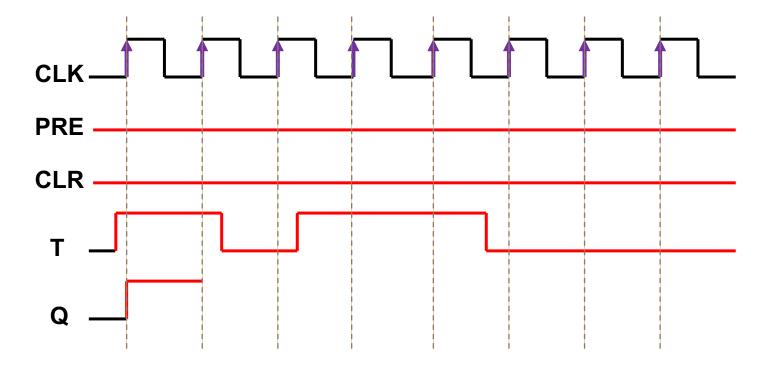
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



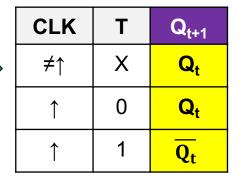


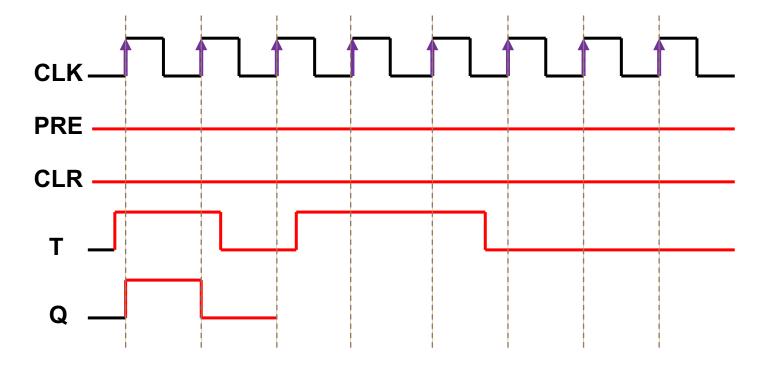
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



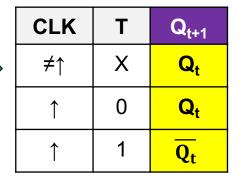


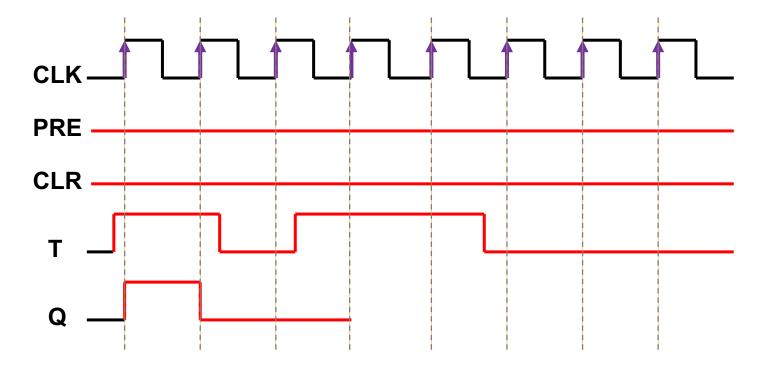
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



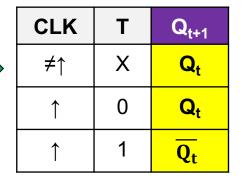


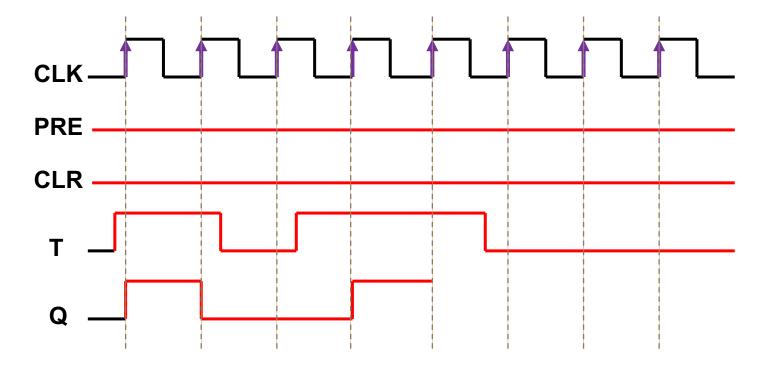
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



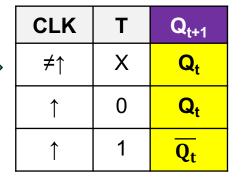


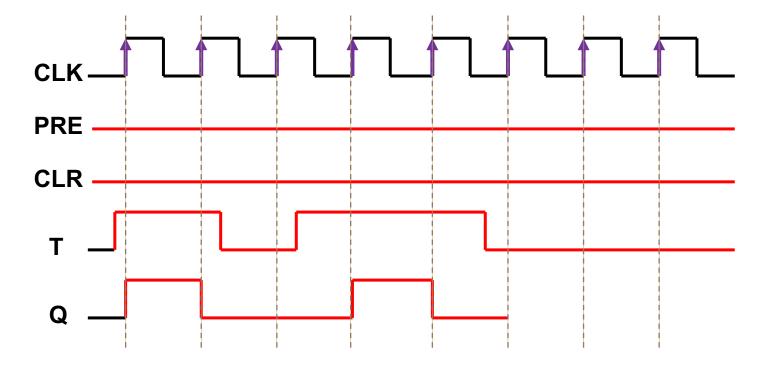
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



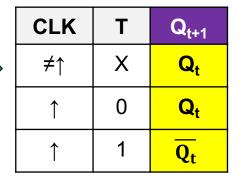


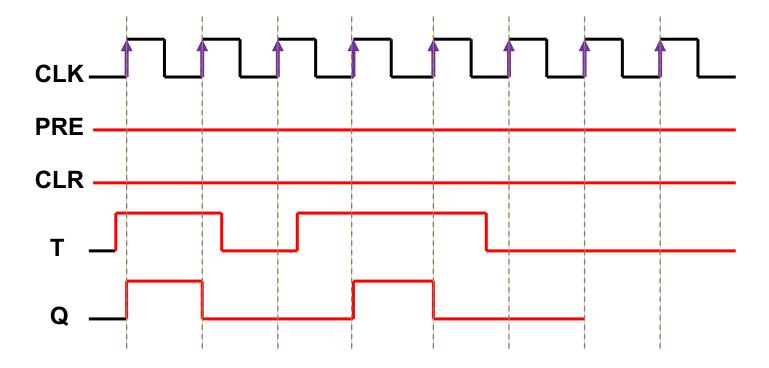
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



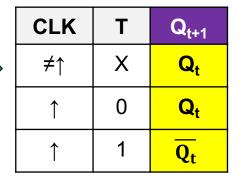


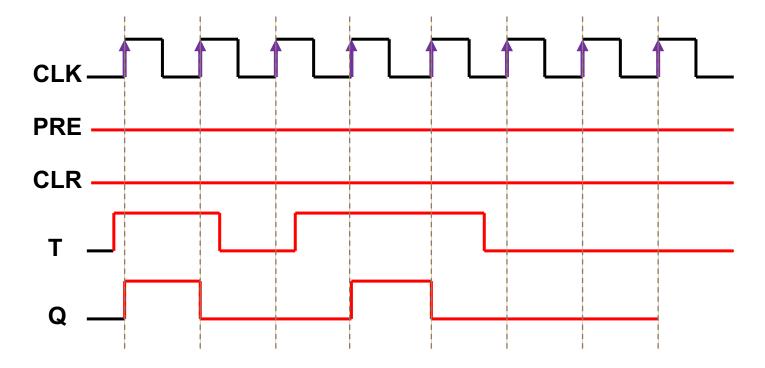
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



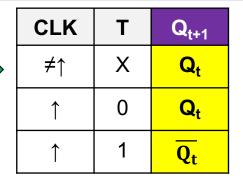


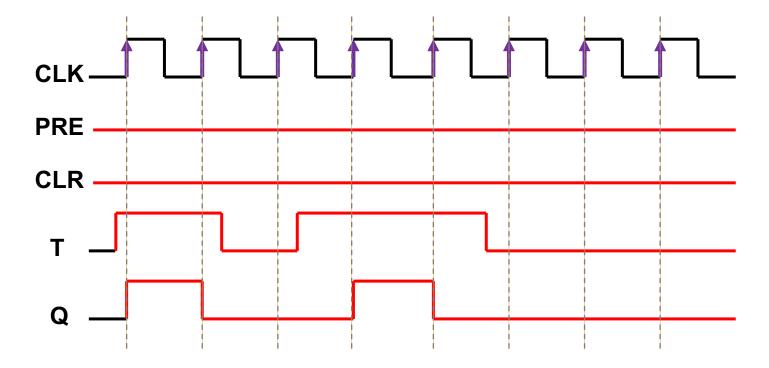
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



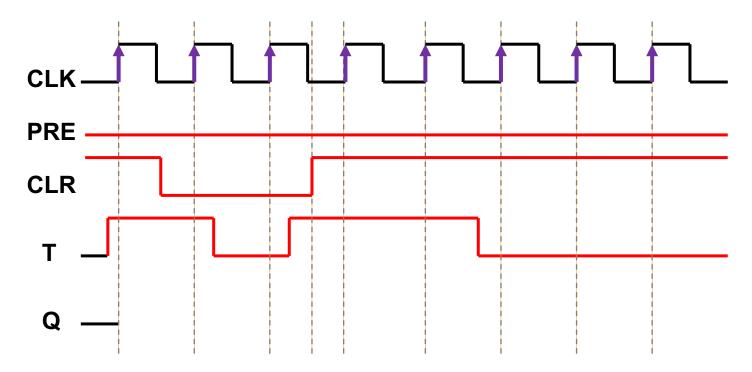


PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO

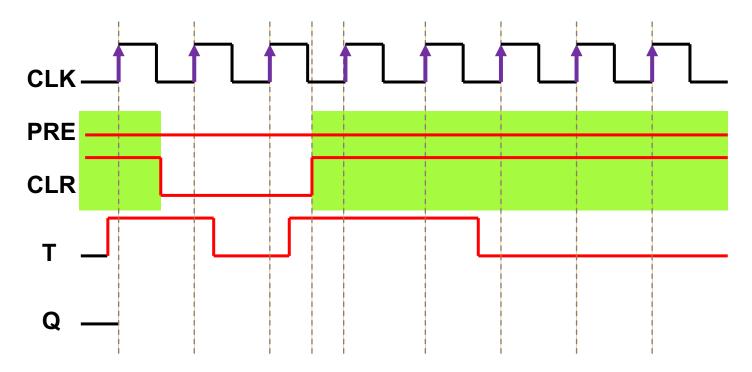




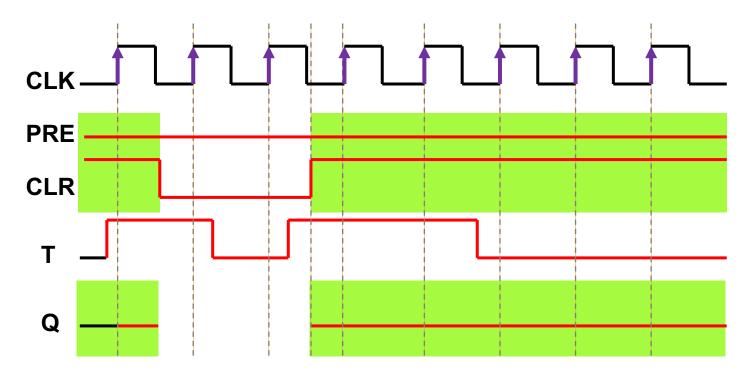
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



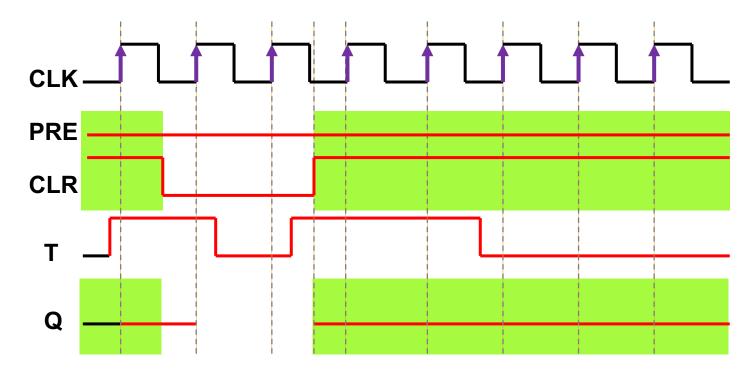
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



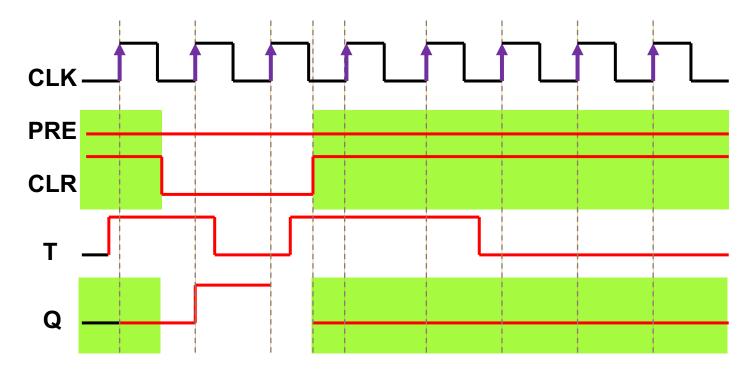
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



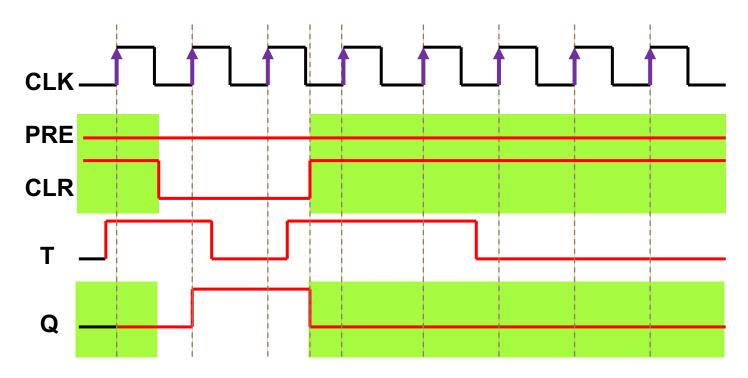
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



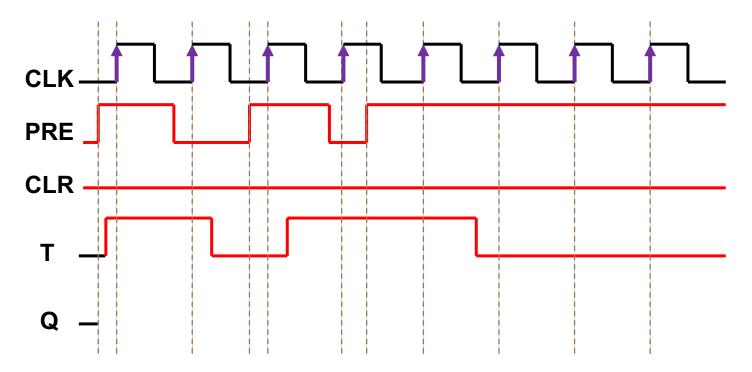
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



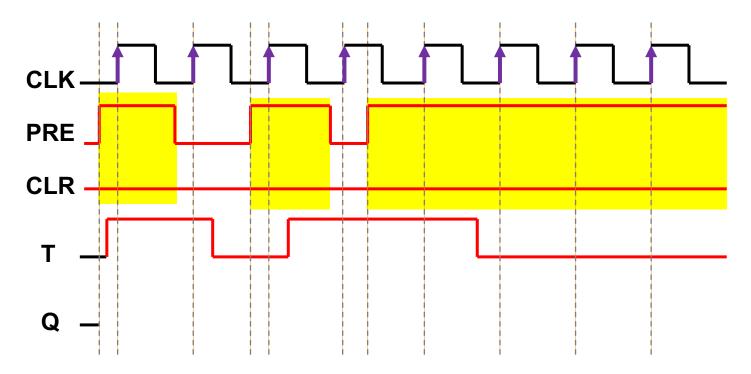
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



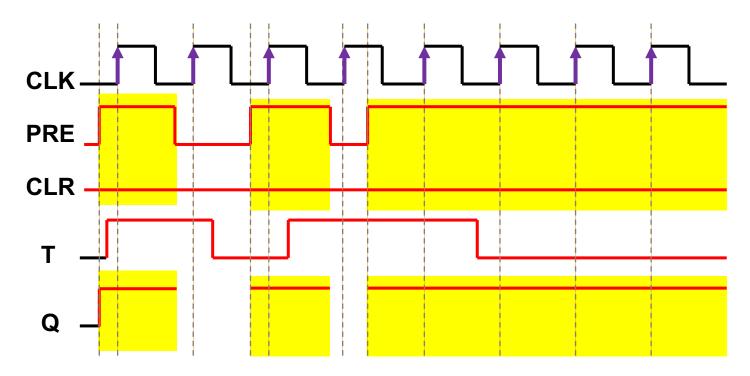
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



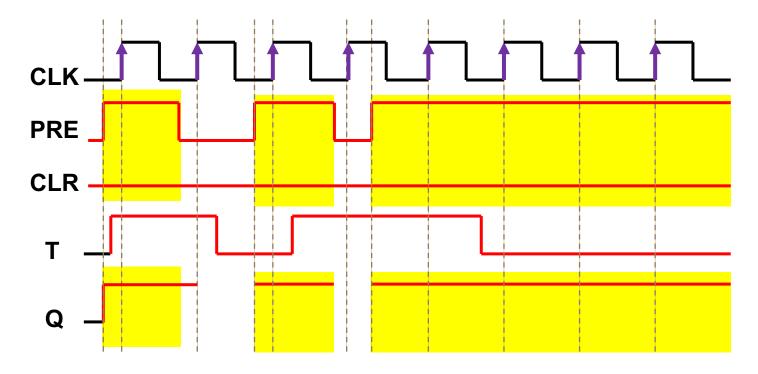
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



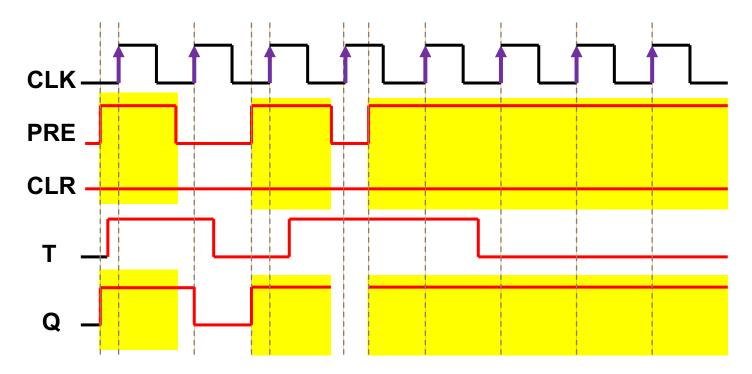
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



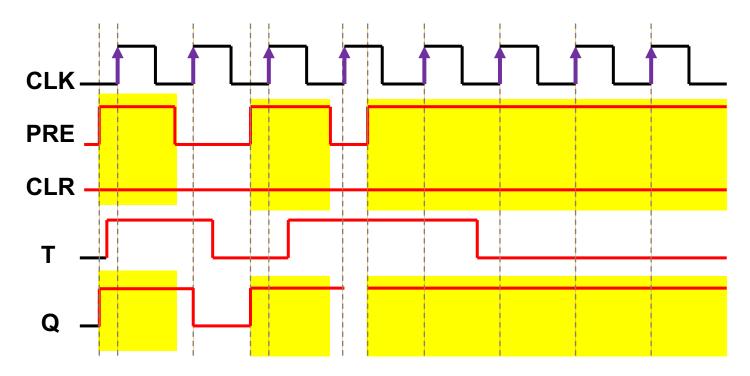
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



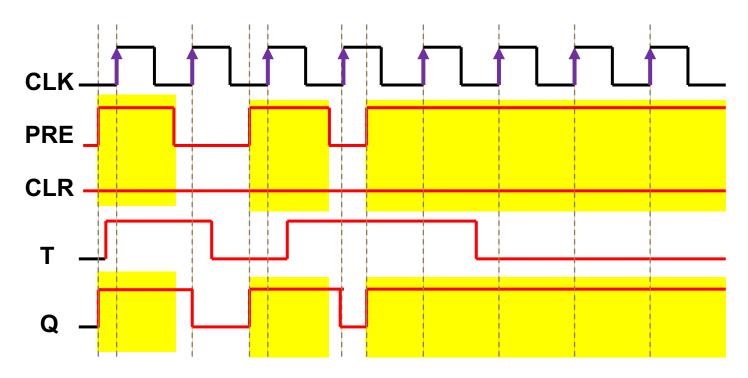
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO

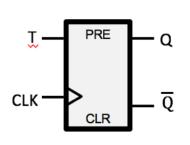


PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO

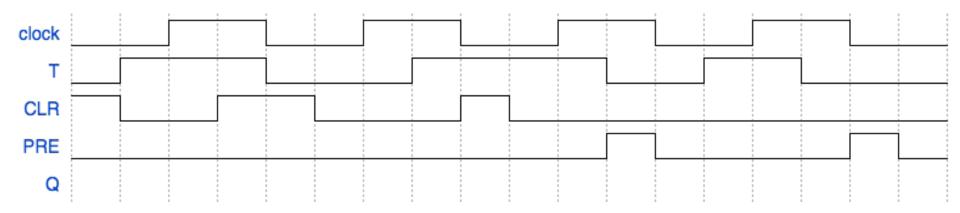


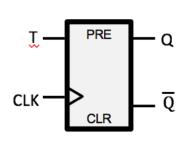
PRE	CLR	Q _{t+1}
0	0	FUNCIONAMENTO NORMAL
0	1	0
1	0	1
1	1	NÃO PERMITIDO



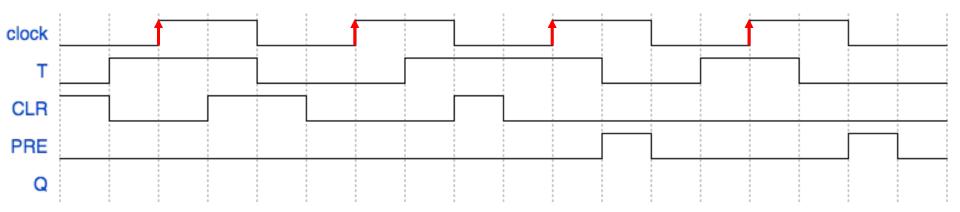


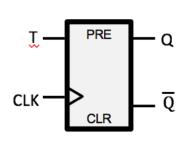
CLK	Т	Q _{t+1}
≠ ↑	X	Q _t
1	0	Q _t
1	1	$\overline{\mathbf{Q_t}}$



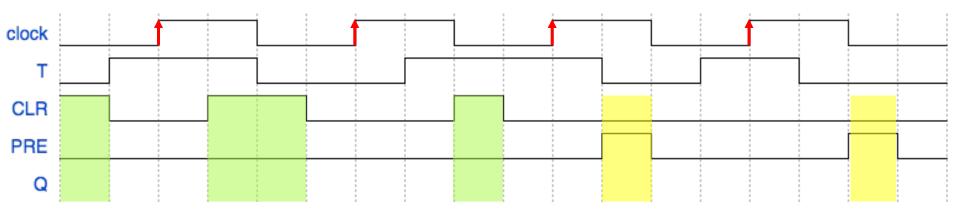


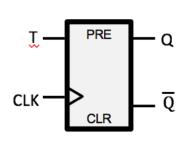
CLK	Т	Q _{t+1}
≠↑	X	Qt
1	0	Q _t
1	1	$\overline{\mathbf{Q}_{t}}$



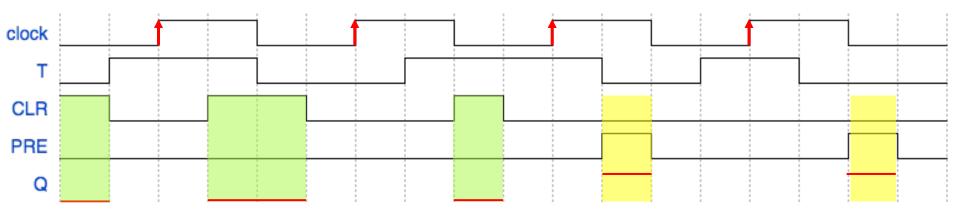


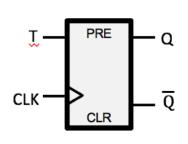
CLK	Т	Q _{t+1}
≠↑	X	Qt
<u> </u>	0	Q _t
1	1	$\overline{\mathbf{Q_t}}$



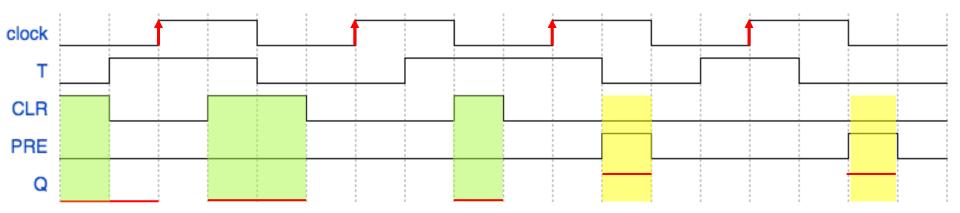


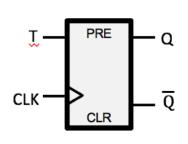
CLK	Т	Q _{t+1}
≠ ↑	X	Q _t
↑	0	Q _t
1	1	$\overline{\mathbf{Q}_{t}}$



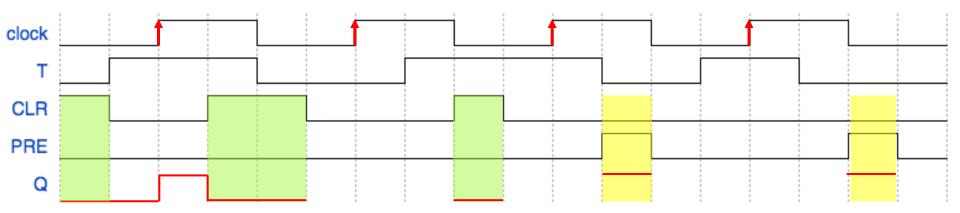


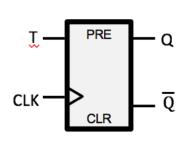
CLK	Т	Q _{t+1}
≠↑	X	Q _t
<u> </u>	0	Q _t
1	1	$\overline{\mathbf{Q_t}}$



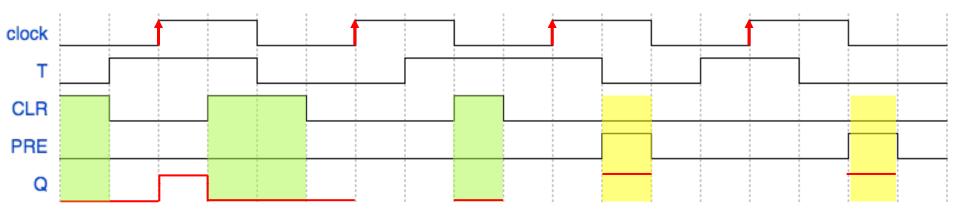


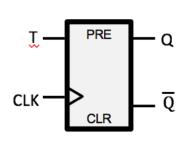
CLK	Т	Q _{t+1}
≠↑	X	Q _t
1	0	Q _t
1	1	$\overline{Q_t}$



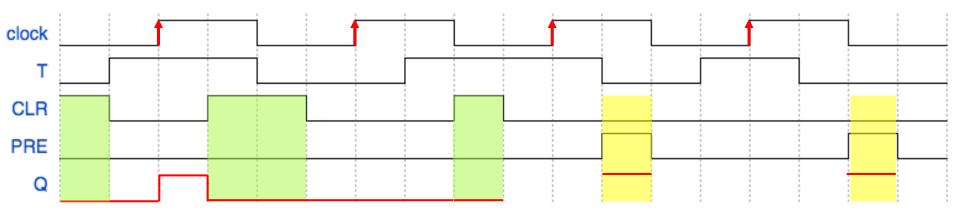


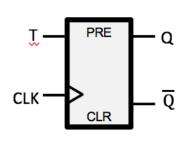
CLK	Т	Q _{t+1}
≠↑	X	Q _t
1	0	Q _t
<u></u>	1	$\overline{Q_t}$



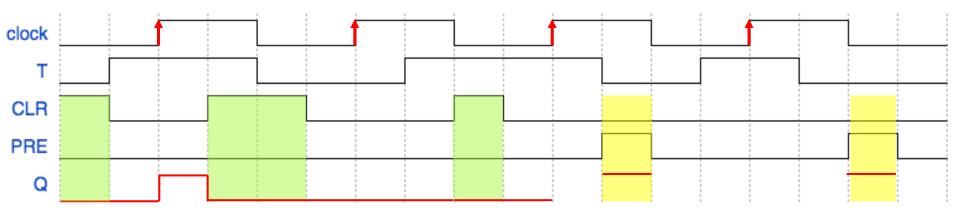


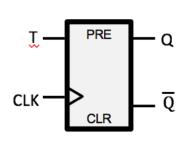
CLK	Т	Q _{t+1}
≠ ↑	X	Q _t
↑	0	Q _t
1	1	$\overline{\mathbf{Q}_{t}}$





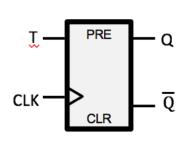
CLK	Т	Q _{t+1}
≠↑	X	Q _t
1	0	Q _t
1	1	$\overline{\mathbf{Q_t}}$



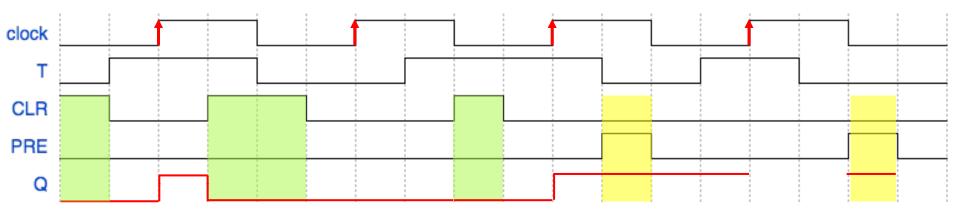


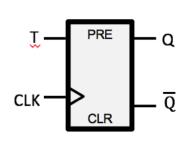
CLK	Т	Q _{t+1}
≠↑	X	Q _t
↑	0	Q _t
\uparrow	1	$\overline{\mathbf{Q}_{t}}$



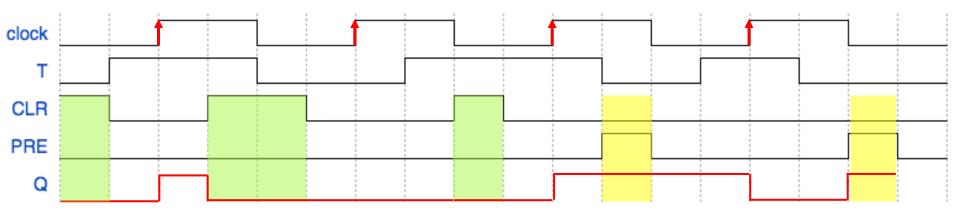


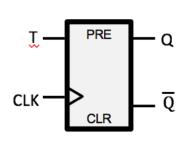
CLK	Т	Q _{t+1}
≠↑	X	Qt
1	0	Q _t
1	1	$\overline{\mathbf{Q_t}}$



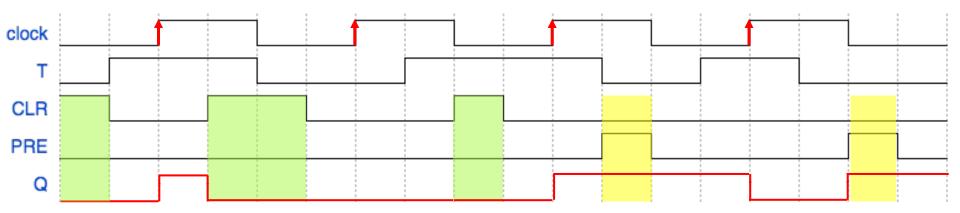


CLK	Т	Q _{t+1}
≠↑	X	Q _t
1	0	Q _t
<u></u>	1	$\overline{Q_t}$





CLK	Т	Q _{t+1}
≠↑	X	Q _t
1	0	Q _t
<u></u>	1	$\overline{Q_t}$



Tempo de Atraso de Propagação

