ENEL 453 LAB 4 – SUBMISSION

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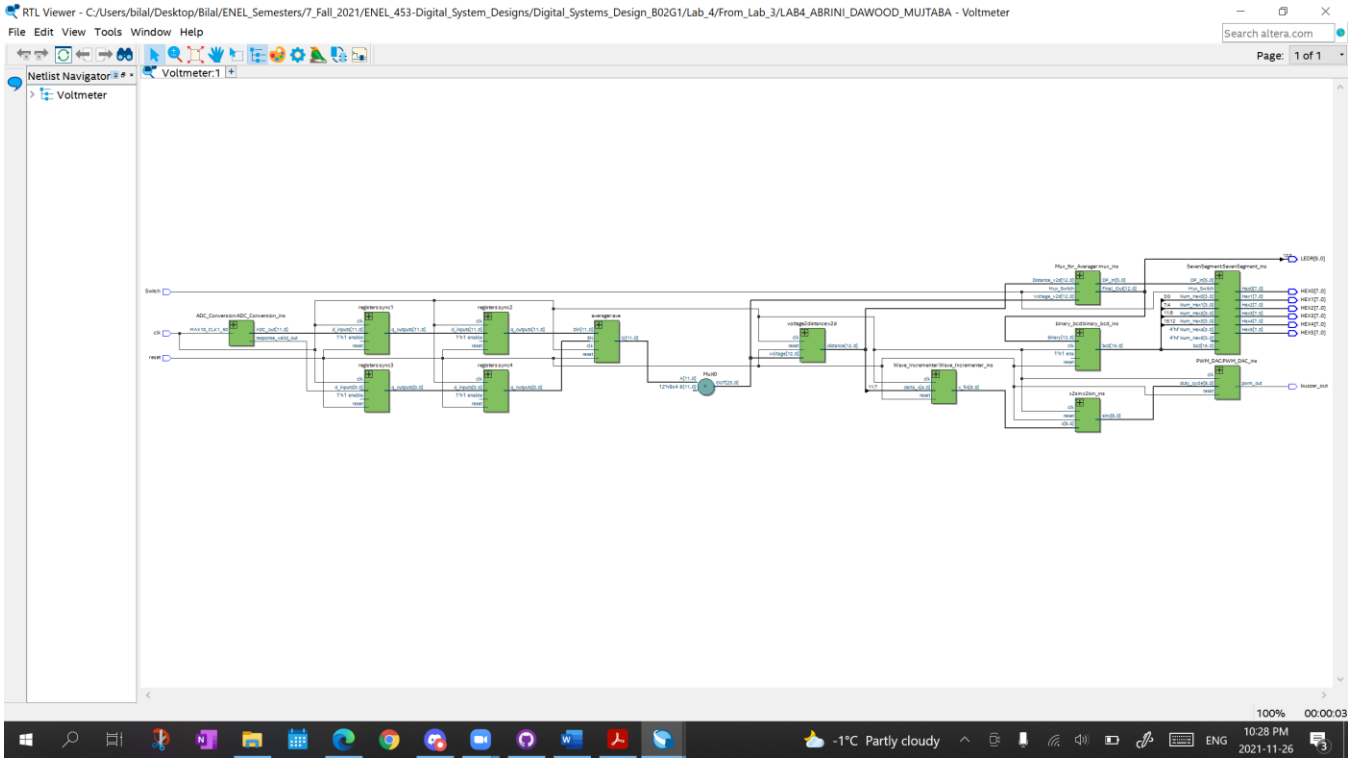
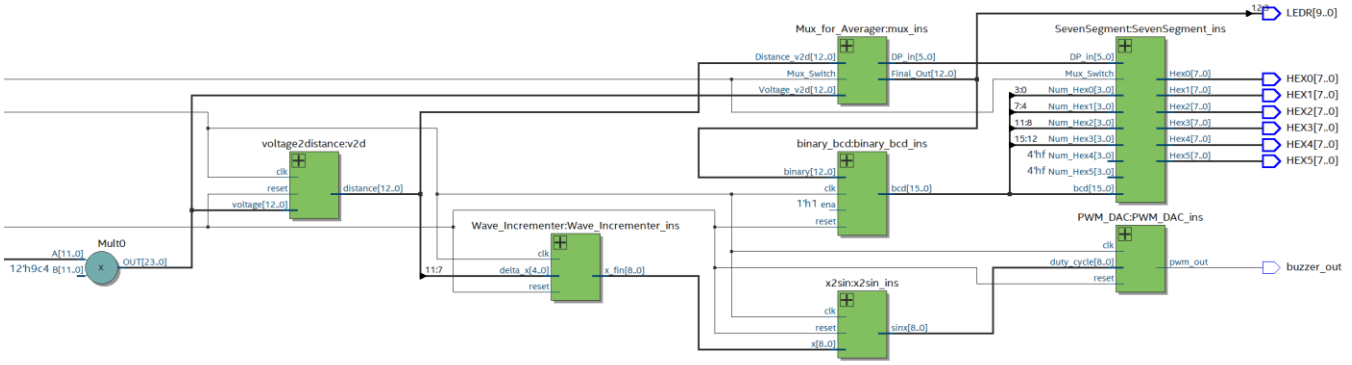
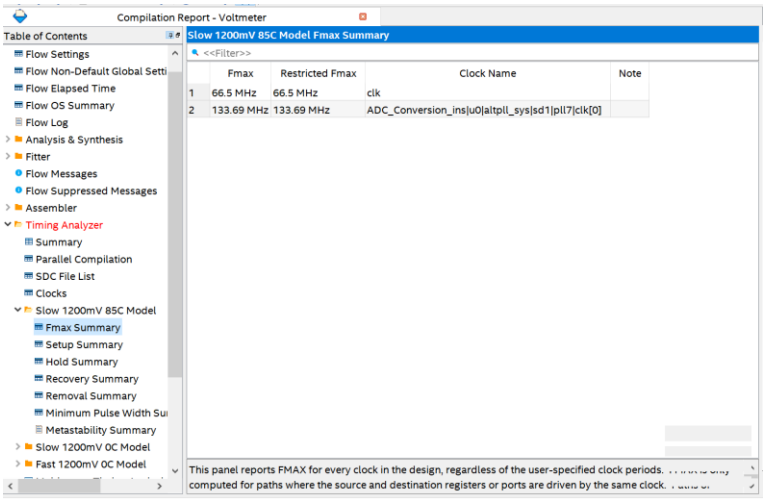
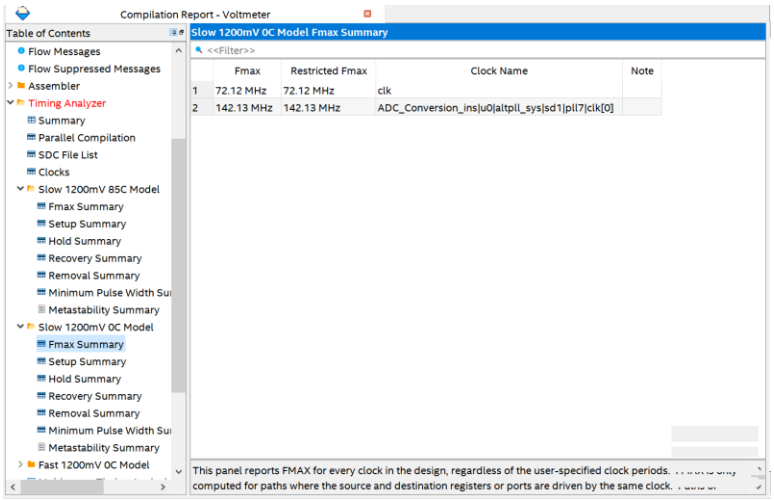
Top level RTL Schematic:

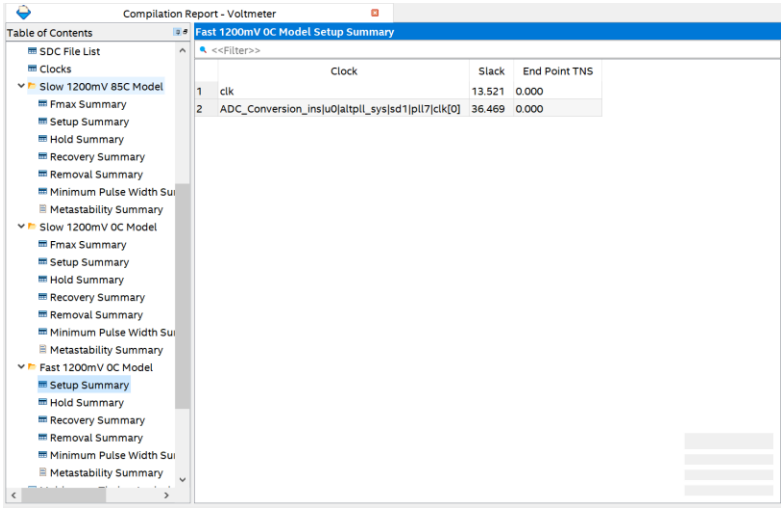




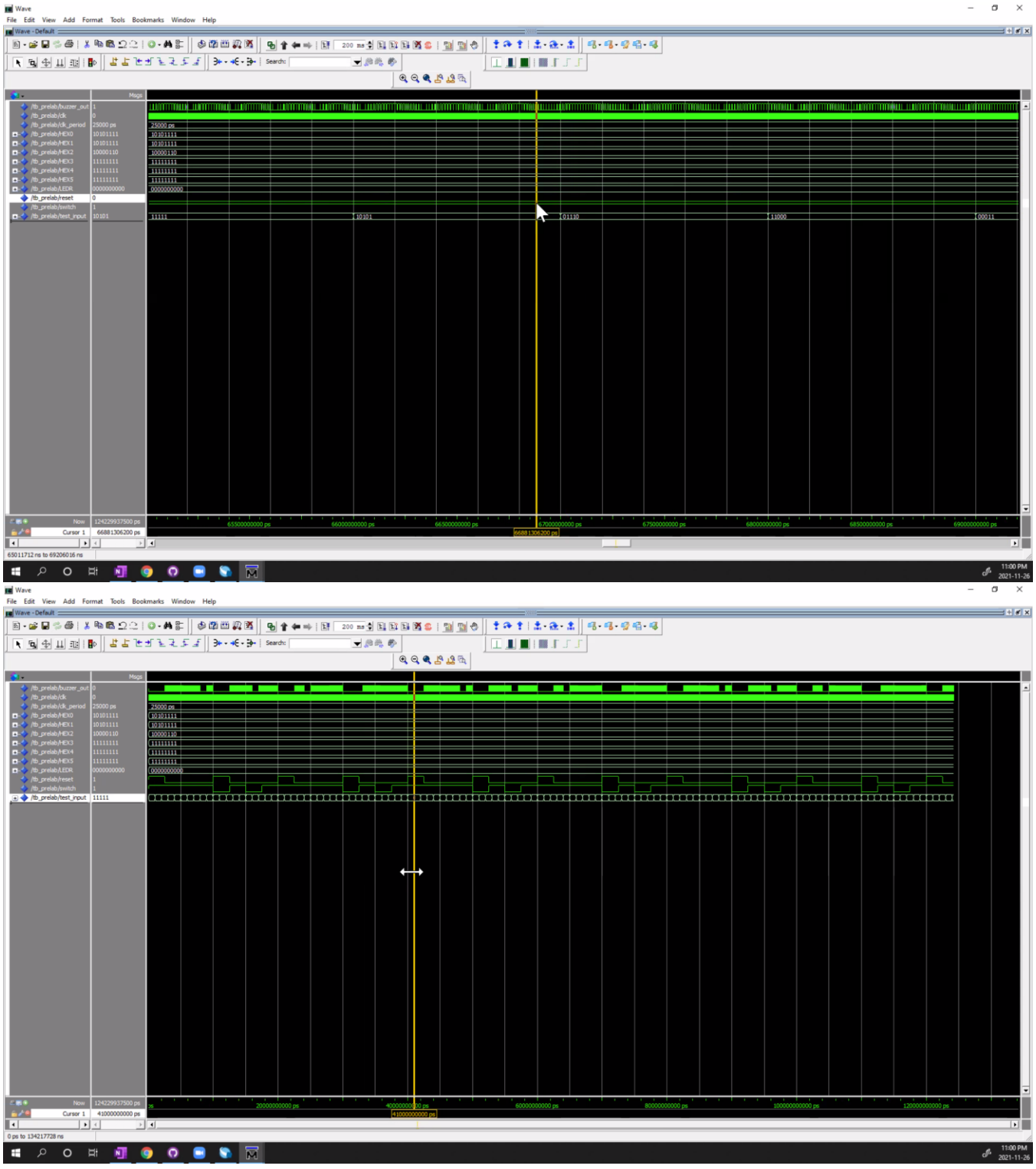
Screenshot of timing analysis:

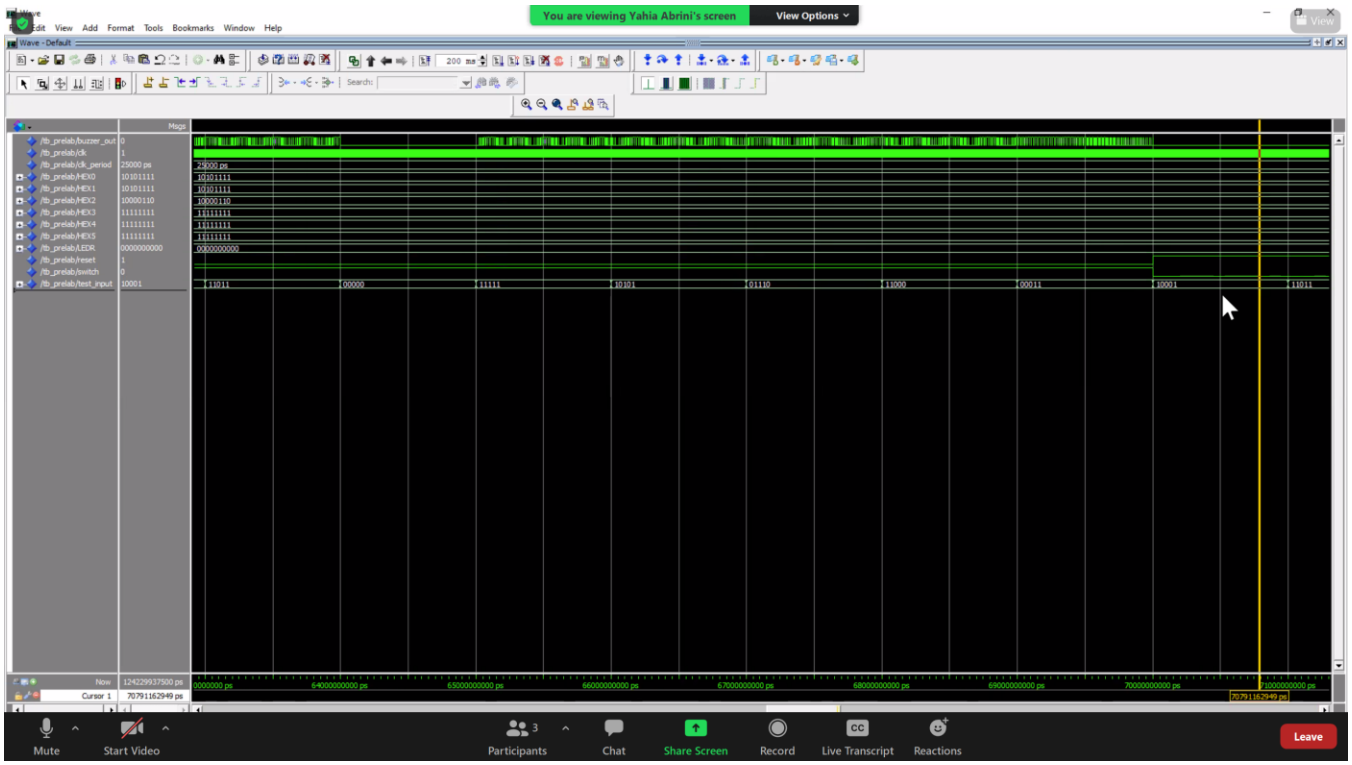






Screenshot of Waveform Simulation:

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