

Reducing the amount of computational overhead of Adaptive Random Testing

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1 Introduction

Adaptive Random Testing (ART) improves the error detection abilities of Random Testing (RT) by enforcing an uniformly even spread of random test cases over the entire input domain. While ART is very efficient at locating faults, the majority of the implementations of ART requires significant computational costs to ensure that the random tests in ART are spread uniformly across the input domain. This can be problematic when software tests are expensive and contain large test sets. Even though ART is extremely efficient in locating faults and produce test cases that increases the likelihood of finding faults, ART is expensive because it considers every single possible test cases in the input domain. Can we reduce the computation cost of ART by leveraging the geometry of the input parameters?

In this proposal, we will try to integrate iterative partitioning into ART to reduce the amount of computation required for each test generation step in ART. Our proposal is inspired by [1], in which we will divide the input domain into grids with equal cell sizes. Then, the grid cells are categorized into three groups. Each group represents how close the test cases are to successful test cases. [1] claims that using this method, they were able to easily identify grid cells that contain test cases that are far away from successful test samples. The method proposed by [1] is effective because it can potentially reduce the number of test cases required to locate faults. Additionally, the grids that are the furthestmost away from the successful test cases can be ruled out immediately using simple search or sorting algorithms.

The testing methods used in [1] can be applied to quickly spot faults in our AVL tree test case. During class, we used breadth and depth first search approach to find faults in our AVL tree. This was demonstrated to be expensive and clumsy. Using the method presented by [1], we are expected to greatly shorten the testing time and the number of test cases to find faults within the AVL tree. For example, using the test method presented in [1], we are expected to greatly shorten the computational time and complexity of the amount of tests required to find faults inside an AVL tree. Even though the storage requirement for the matrix might be big, the algorithm does discard the matrix if there are not failure test cases. To refine the test cases, the matrix's size will be increased to allow the algorithm to obtain more refined test cases.

2 Plan

The plan for this project is as follows:

- April 27-29: Survey papers on ART and related works on RT, ART, and other forms of ART
- May 1-6: Implement [1] with TSTL. This includes finding functions within the TSTL API that supports ART and the methods presented in [1].
- May 9-31: Make the implementation of [1] more efficient. Specifically, make [1] be able to locate faults quicker, optimize the test to include more code coverage, and the test execution time
- May 31 - June 06: Finalize the project, write up documentations, and final optimization.

References

- [1] T. Y. Chen, De Hao Huang, and Zhi Quan Zhou. “Reliable Software Technologies – Ada-Europe 2006: 11th Ada-Europe International Conference on Reliable Software Technologies, Porto, Portugal, June 5-9, 2006. Proceedings”. In: ed. by Luís Miguel Pinho and Michael González Harbour. Berlin, Heidelberg: Springer Berlin Heidelberg, 2006. Chap. Adaptive Random Testing Through Iterative Partitioning, pp. 155–166. ISBN: 978-3-540-34664-7. DOI: 10.1007/11767077_13. URL: http://dx.doi.org/10.1007/11767077_13.
- [2] Johannes Mayer. “Formal Methods and Software Engineering: 7th International Conference on Formal Engineering Methods, ICFEM 2005, Manchester, UK, November 1-4, 2005. Proceedings”. In: ed. by Kung-Kiu Lau and Richard Banach. Berlin, Heidelberg: Springer Berlin Heidelberg, 2005. Chap. Adaptive Random Testing by Bisection with Restriction, pp. 251–263. ISBN: 978-3-540-32250-4. DOI: 10.1007/11576280_18. URL: http://dx.doi.org/10.1007/11576280_18.