

## JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD HYDERABAD - 500 085, TELANGANA STATE, INDIA.

CONSOLIDATED MARKS MEMO / CREDIT SHEET

B.Tech. ELECTRICAL & ELECTRONICS ENGINEERING

CMM. No.: C 0881890

Serial No.: 21290003273

Name BADE SUPREETHA

Hall Ticket No. 14WH1A0237

Year of Admission: 2014-2015

Name of the College: WH-BVRIT,BACHUPALLY

Month & Year of Final Exam: May, 2018

Class Awarded : FIRST CLASS WITH DISTINCTION

II Tic	ket No. :14WH1AU237 Year o	n Adn	nissioi	n : -				Class Awarded :				
S.No.	SUBJECT TITLE	INT	EXT	TOTAL		CREDITS	S.No.	SUBJECT TITLE	INT	EXT	TOTAL	
ŝ	Maximum Marks in Theory	25	75	+		CRE	Ś	Maximum Marks in Lab	25	50	75	
		23	1,3		YE	_1						_
			1 4	4 65	1 4		2	ENGLISH LANGUAGE COMMUNICATION SKILLS LAB.	23	3 43	66	;   4
1	ENGLISH	21		4   63 3   67	1	ı	4	MATHEMATICS - I	2	1 42	63	1 6
3	ENGINEERING WORKSHOP	24				- 1	6	ENGINEERING PHYSICS	23	3 49	72	1
5	MATHEMATICAL METHODS	-		5 67	1	- 1	8	COMPUTER PROGRAMMING	21			-
7	ENGINEERING CHEMISTRY	22		3   65			10	COMPUTER PROGRAMMING LAB.	24	48	72	4
9	ENGINEERING DRAWING		1			- 1						
11	ENGINEERING PHYSICS & ENGINEERING CHEMISTRY LA	25	4	9 / 4	4							
	I SEMESTER			II	YE	AR		II SEMESTER		_	_	_
1	MATHEMATICS - III	22	47	69	4	1	1	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	19	1	48	1
	FLUID MECHANICS AND HYDRAULIC MACHINERY	19	63	82	4	1	2	POWER SYSTEMS - I	24	1	69	
2	ELECTRONIC DEVICES & CIRCUITS	18		60		1	3	ELECTRONIC CIRCUITS	21	42	1	1
3	ELECTRICAL CIRCUITS	21	43	64	4	ı	4	SWITCHING THEORY AND LOGIC DESIGN	20	59	1	4
4	ELECTRICAL CIRCUITS  ELECTROMAGNETIC FIELDS	21		74	4	ı	5	NETWORK THEORY	24	49		
5	ELECTRICAL MACHINES - I	24	47	71	4	١	6	ELECTRICAL MACHINES - II	22	37	59 71	2
6	FLUID MECHANICS AND HYDRAULIC MACHINERY LAB	24	46	70	2	ı	7	ELECTRICAL MACHINES LAB - I	25	46		
7	ELECTRONIC DEVICES AND CIRCUITS LAB	23	49	72	2	ı	8	ELECTRICAL CIRCUITS AND SIMULATION LAB	25	50	75 64	2
8	ELECTRONIC DEVICES AND GINGS IN SERVICE						9	GENDER SENSITIZATION* ^	22	42	04	
	ISEMESTER			Ш	YE	AR		II SEMESTER				
_	IC APPLICATIONS	23	49	72	4	ı	1	ELECTRICAL AND ELECTRONICS INSTRUMENTATION	22	31		4
1	IC APPLICATIONS	21	39	60	4	ŀ	2	POWER ELECTRONICS AND SIMULATION LAB	24	41	65	2
2	MANAGEMENT SCIENCE POWER SYSTEMS - II	19	55	74	4	1	3	STATIC DRIVES	24		65	4
3		21	55	76	4	1	4	COMPUTER METHODS IN POWER SYSTEMS	17		53	4
4	CONTROL SYSTEMS	25	35	60	4	!	- 1	MICROPROCESSORS AND INTERFACING DEVICES	19	43	- 1	4
5	POWER ELECTRONICS	18	26	44	4	6		ENVIRONMENTAL STUDIES	18	35	53	4
6	ELECTRICAL MACHINES - III ELECTRICAL MACHINES LAB - II	24	43	67	2	1	- 1	HUMAN VALUES AND PROFESSIONAL ETHICS	17	33	50	4
7	ADVANCED COMMUNICATION SKILLS LAB	23	48	71	2	8	3	CONTROL SYSTEMS AND SIMULATION LAB	23	45	68	2
В	ADVANCED COMMONICATION SINEES 2 12					L		, and a				
	ISEMESTER			IV	YΕ	AR	_	II SEMESTER		00		_
	SWITCH GEAR AND PROTECTION	20	34		4	1		FUNDAMENTALS OF HVDC AND FACTS DEVICES			44	4
	MICROPROCESSORS AND INTERFACING DEVICES LAB	20	37		2	2	- 1	SEMINAR	48		194	
	ELECTRICAL MEASUREMENTS LAB		44		2	3		PROJECT WORK	45		- 1	
		14	26	40*	4	4	- 1	COMPREHENSIVE VIVA-VOCE	-		90	2
	DIGITAL SIGNAL PROCESSING	18	32	50	4	5		RENEWABLE ENERGY SOURCES		27	50	
		21	10	31*	0	6	- 1	ADVANCED CONTROL SYSTEMS	18	32	50	4
	POWER STSTEM OF LIVETION AND TO		- 1	45	4	7		INDUSTRY ORIENTED MINI PROJECT	-	46	46	2
	HIGH VOLTAGE ENGINEERING	. – 1										
1	HIGH VOLTAGE ENGINEERING		37	54	4	ı						

Number of Credits registered for : 226 Aggregate Marks Secured for best: 218

Aggregate Marks Secured 3682 OUT OF 5250 (70.13%)

5. The kely-

CONTROLLER OF EXAMINATIONS

A indicates 'ABSENT'

(^(Cradita cogniderest for exambolishes resultion of aggregate)