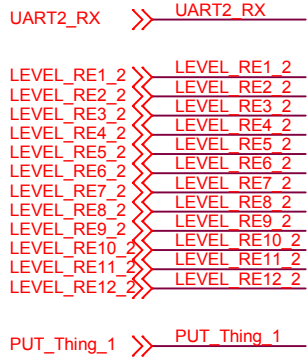
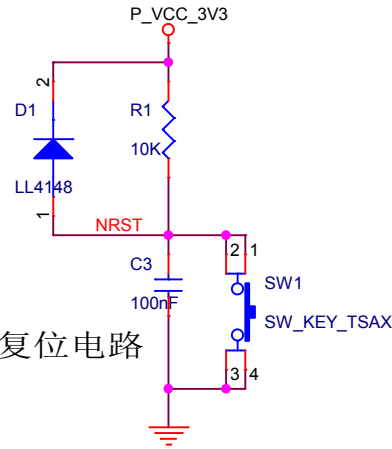


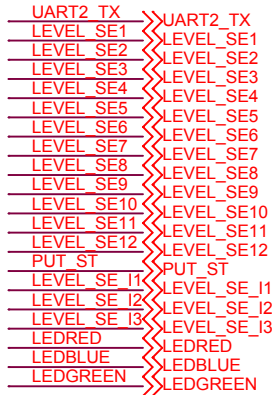
信号输入



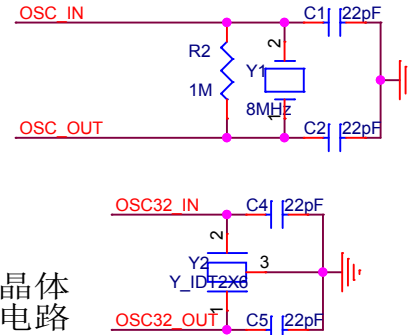
复位电路



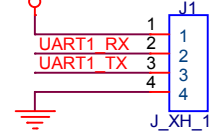
信号输出



晶体电路

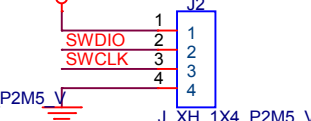


P_VCC_3V3



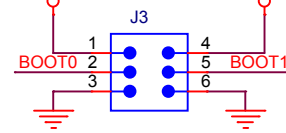
备注：程序下载口
ISP下载接口

P_VCC_3V3



SWD调试

P_VCC_3V3



启动配置电路

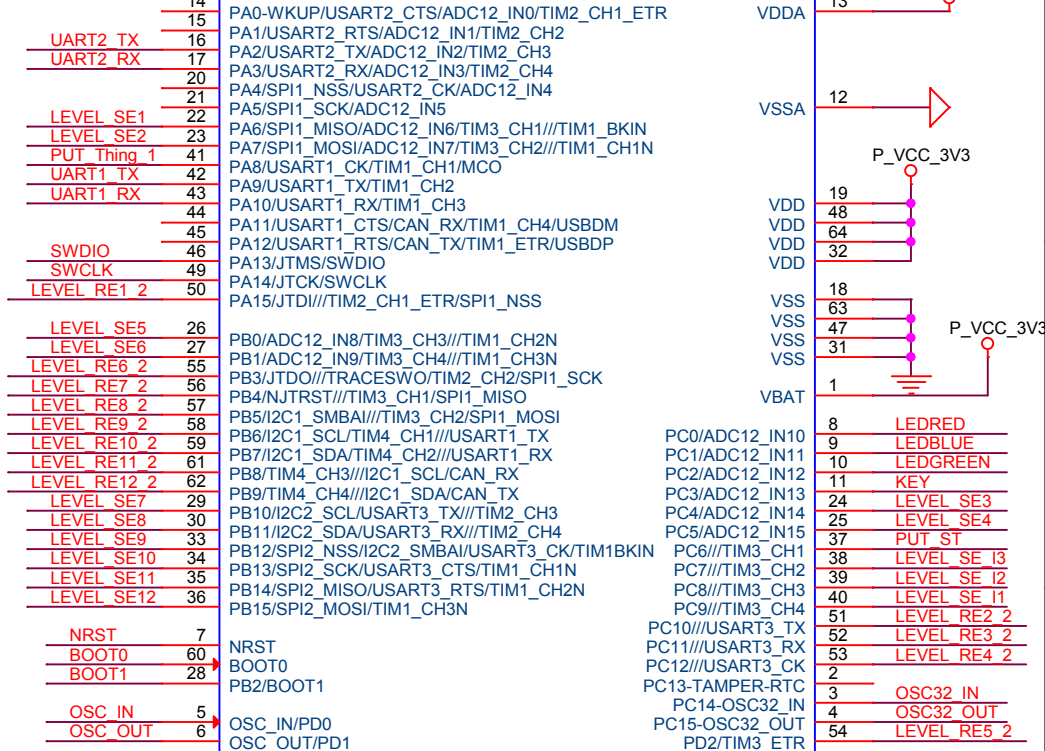
P_VCC_3V3

启动方式解释：
Flash：按下复位键程序从头跑
ISP：用于串口下载
SRAM：用于调试

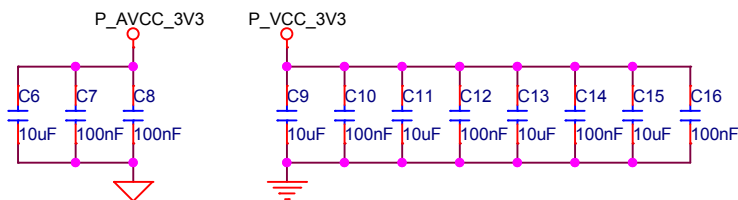
启动配置说明

启动方式	B0	B1
Flash	0	X
ISP	1	0
SRAM	1	1

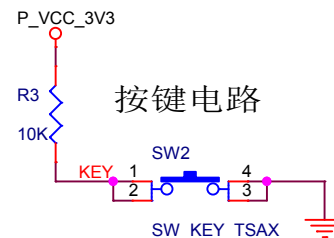
U1



U_STM32F103R8T6

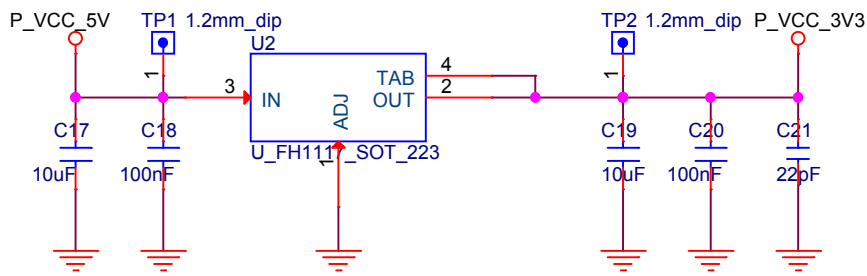


按键电路



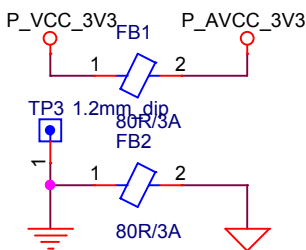
整件设计文件	产品型号	对射信息处理板			
原理图	硬件版本	0.70			
文件编号	<OrgName>				
版次	<OrgAddr1>	页次	1 / 12	日期	20170629
核准	<OrgAddr2>		审核	<OrgAddr3>	
ALE	/		编写	叶海鹏	

UART2_TX>>UART2_TX

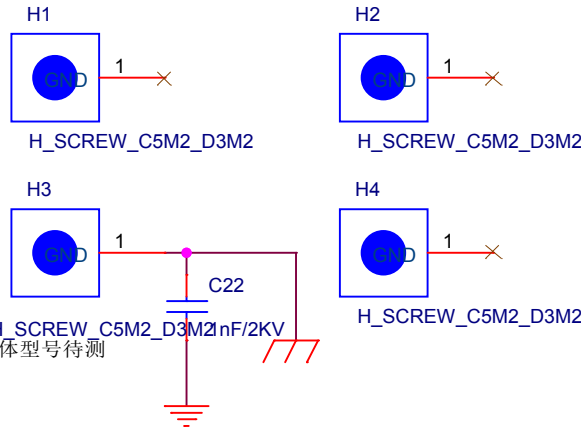


5V转3V3

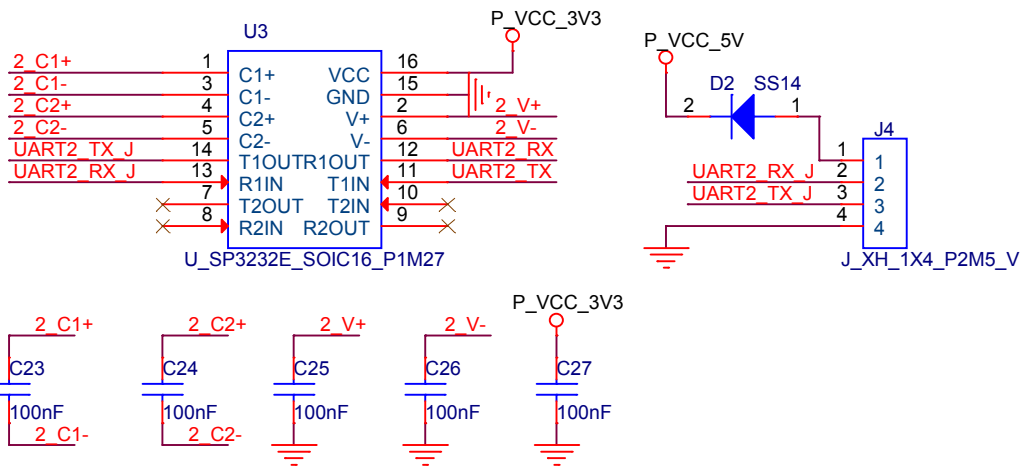
备注：原R53R50，现FB1和2和3可以考虑使用磁珠，具体型号待测



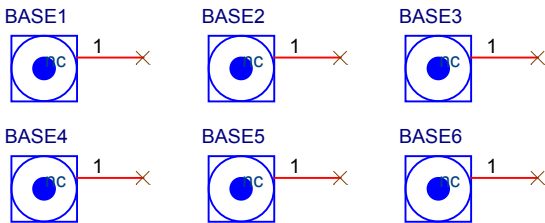
UART2_RX>>UART2_RX



RS232收发电路

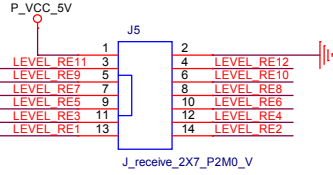


备注：用于与电机驱动板进行通信，告知电机驱动板是否掉货以及层之间对射情况及利用层之间的对射检测是否出货了

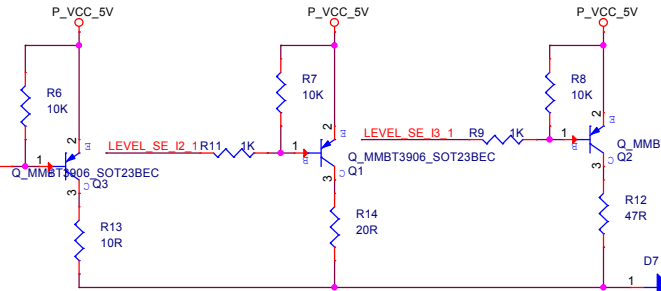
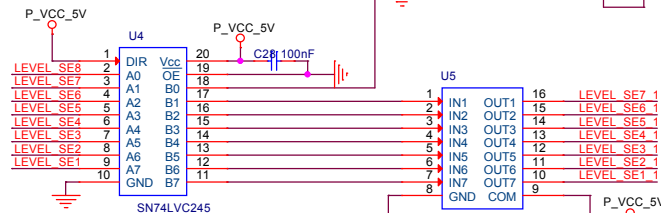
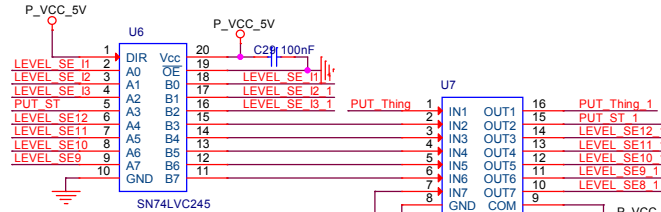
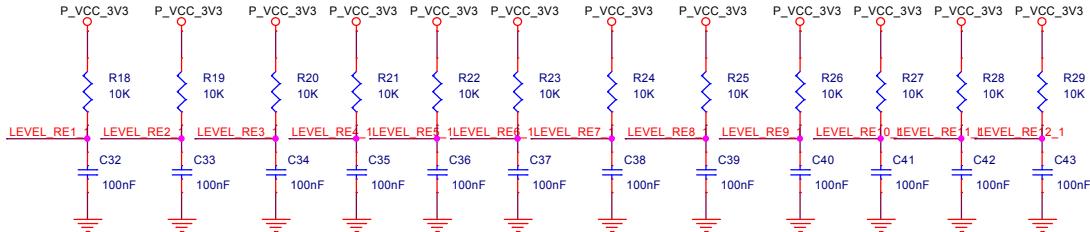
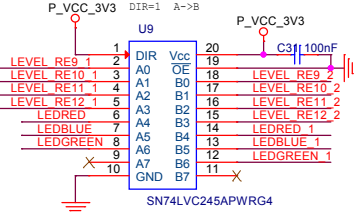
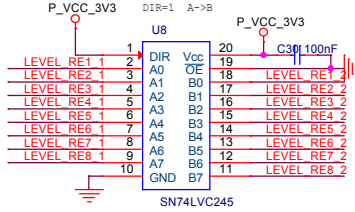
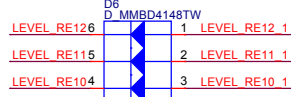
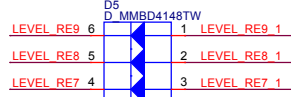
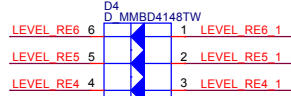
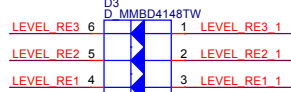


整件设计文件	产品型号	对射信息处理板			
原理图	硬件版本	0.70			
文件编号	<OrgName>				
版次	<OrgAddr1>	页次	2 / 12	日期	20170629
核准	<OrgAddr2>		审核	<OrgAddr3>	
ALE	/		编写	叶海鹏	

LEVEL_SE1 >> LEVEL_SE1
LEVEL_SE2 >> LEVEL_SE2
LEVEL_SE3 >> LEVEL_SE3
LEVEL_SE4 >> LEVEL_SE4
LEVEL_SE5 >> LEVEL_SE5
LEVEL_SE6 >> LEVEL_SE6
LEVEL_SE7 >> LEVEL_SE7
LEVEL_SE8 >> LEVEL_SE8
LEVEL_SE9 >> LEVEL_SE9
LEVEL_SE10 >> LEVEL_SE10
LEVEL_SE11 >> LEVEL_SE11
LEVEL_SE12 >> LEVEL_SE12
PUT_ST >> PUT_ST
LEVEL_SE_I1 >> LEVEL_SE_I1
LEVEL_SE_I2 >> LEVEL_SE_I2
LEVEL_SE_I3 >> LEVEL_SE_I3
LEDRED >> LEDRED
LEDBLUE >> LEDBLUE
LEDGREEN >> LEDGREEN

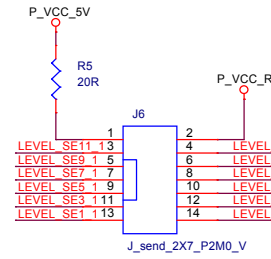


备注：接收端输入 标记为RE_OUT（作为下一级输出）

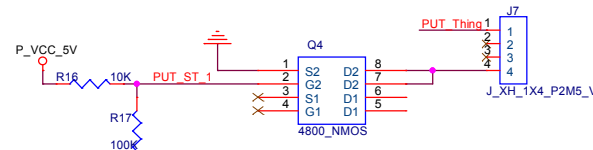


LEVEL_RE1_2 >> LEVEL_RE1_2
LEVEL_RE2_2 >> LEVEL_RE2_2
LEVEL_RE3_2 >> LEVEL_RE3_2
LEVEL_RE4_2 >> LEVEL_RE4_2
LEVEL_RE5_2 >> LEVEL_RE5_2
LEVEL_RE6_2 >> LEVEL_RE6_2
LEVEL_RE7_2 >> LEVEL_RE7_2
LEVEL_RE8_2 >> LEVEL_RE8_2
LEVEL_RE9_2 >> LEVEL_RE9_2
LEVEL_RE10_2 >> LEVEL_RE10_2
LEVEL_RE11_2 >> LEVEL_RE11_2
LEVEL_RE12_2 >> LEVEL_RE12_2

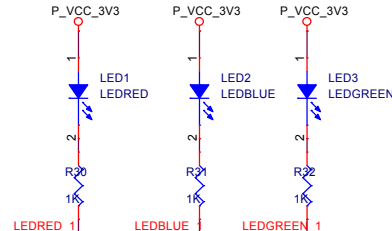
PUT_Thing_1 >> PUT_Thing_1



备注：发送端输出 SE_OUT



备注：掉货检测电源控制电路（控制地的连接）
备注：put_thing 是掉货检测电路输出的控制信号



整件设计文件	产品型号	对射信息处理板
原理图	硬件版本	0.70
文件编号	<OrgName>	
版次	<OrgAddr1>	页次 3 / 12 日期 20170629
核准	<OrgAddr2>	审核 <OrgAddr3>
ALE	/	编写 叶海鹏