**Racebench 2.1\_remarks**

This benchmark is only applicable to the test of single-variable access sequence mode.

This benchmark is an annotated version of the benchmark Racebench 2.1, which gives pre-set bug points and pre-set false positive points.

Note:

* All use cases follow standard C syntax.
* The benchmark does not consider the periodicity of interrupts. ISRs can preempt the main program or ISRs with lower priority when they are enabled.

**The benchmark configuration instructions**

* The ISR number is used in the ISR enabled or disabled function provided in the benchmark. *enable\_isr*(*n*) and *disable\_isr*(*n*) are ISR enabled or disabled primitives, where *n* represents the ISR number. When *n* is -1, it means that all ISRs are enabled or disabled.
* The higher the priority number, the higher the priority

**The benchmark**

There are 31 programs in total.

|  |  |  |  |
| --- | --- | --- | --- |
| **ID** | **Name** | **Main function entry** | **ISR entry/ISR number/priority** |
| Ex1 | svp\_simple\_001\_001.c | svp\_simple\_001\_001\_main | svp\_simple\_001\_001\_isr\_1/1/1 svp\_simple\_001\_001\_isr\_2/2/2 |
| Ex2 | svp\_simple\_002\_001.c | svp\_simple\_002\_001\_main | svp\_simple\_002\_001\_isr\_1/1/1 svp\_simple\_002\_001\_isr\_2/2/2 |
| Ex3 | svp\_simple\_003\_001.c | svp\_simple\_003\_001\_main | svp\_simple\_003\_001\_isr\_1/1/1 svp\_simple\_003\_001\_isr\_2/2/2 |
| Ex4 | svp\_simple\_004\_001.c | svp\_simple\_004\_001\_main | svp\_simple\_004\_001\_isr\_1/1/1 svp\_simple\_004\_001\_isr\_2/2/2 |
| Ex5 | svp\_simple\_005\_001.c | svp\_simple\_005\_001\_main | svp\_simple\_005\_001\_isr\_1/1/1 |
| Ex6 | svp\_simple\_006\_001.c | svp\_simple\_006\_001\_main | svp\_simple\_006\_001\_isr\_1/1/1 |
| Ex7 | svp\_simple\_007\_001.c | svp\_simple\_007\_001\_main | svp\_simple\_007\_001\_isr\_1/1/1 |
| Ex8 | svp\_simple\_008\_001.c | svp\_simple\_008\_001\_main | svp\_simple\_008\_001\_isr\_1/1/1 |
| Ex9 | svp\_simple\_009\_001.c | svp\_simple\_009\_001\_main | svp\_simple\_009\_001\_isr\_1/1/1 |
| Ex10 | svp\_simple\_0010\_001.c | svp\_simple\_0010\_001\_main | svp\_simple\_010\_001\_isr\_1/1/1 |
| Ex11 | svp\_simple\_0011\_001.c | svp\_simple\_0011\_001\_main | svp\_simple\_011\_001\_isr\_1/1/1 |
| Ex12 | svp\_simple\_0012\_001.c | svp\_simple\_0012\_001\_main | svp\_simple\_012\_001\_isr\_1/1/1 |
| Ex13 | svp\_simple\_0013\_001.c | svp\_simple\_0013\_001\_main | svp\_simple\_013\_001\_isr\_1/1/1 svp\_simple\_013\_001\_isr\_2/2/2 svp\_simple\_013\_001\_isr\_3/3/3 |
| Ex14 | svp\_simple\_0014\_001.c | svp\_simple\_0014\_001\_main | svp\_simple\_014\_001\_isr\_1/1/1 svp\_simple\_014\_001\_isr\_2/2/2 svp\_simple\_014\_001\_isr\_3/3/3 |
| Ex15 | svp\_simple\_0015\_001.c | svp\_simple\_0015\_001\_main | svp\_simple\_015\_001\_isr\_1/1/1 |
| Ex16 | svp\_simple\_0016\_001.c | svp\_simple\_0016\_001\_main | svp\_simple\_016\_001\_isr\_1/1/1 |
| Ex17 | svp\_simple\_0017\_001.c | svp\_simple\_0017\_001\_main | svp\_simple\_017\_001\_isr\_1/1/1 |
| Ex18 | svp\_simple\_0018\_001.c | svp\_simple\_0018\_001\_main | svp\_simple\_018\_001\_isr\_1/1/1 svp\_simple\_018\_001\_isr\_2/2/2 |
| Ex19 | svp\_simple\_0019\_001.c | svp\_simple\_0019\_001\_main | svp\_simple\_019\_001\_isr\_1/1/1 |
| Ex20 | svp\_simple\_0020\_001.c | svp\_simple\_0020\_001\_main | svp\_simple\_020\_001\_isr\_1/1/1 svp\_simple\_020\_001\_isr\_2/2/2 |
| Ex21 | svp\_simple\_0021\_001.c | svp\_simple\_0021\_001\_main | svp\_simple\_021\_001\_isr\_1/1/1 |
| Ex22 | svp\_simple\_0022\_001.c | svp\_simple\_0022\_001\_main | svp\_simple\_022\_001\_isr\_1/1/1 |
| Ex23 | svp\_simple\_0023\_001.c | svp\_simple\_0023\_001\_main | svp\_simple\_023\_001\_isr\_1/1/1 |
| Ex24 | svp\_simple\_0024\_001.c | svp\_simple\_0024\_001\_main | svp\_simple\_024\_001\_isr\_1/1/1 |
| Ex25 | svp\_simple\_0025\_001.c | svp\_simple\_0025\_001\_main | svp\_simple\_025\_001\_isr\_1/1/1 |
| Ex26 | svp\_simple\_0026\_001.c | svp\_simple\_0026\_001\_main | svp\_simple\_026\_001\_isr\_1/1/1 svp\_simple\_026\_001\_isr\_2/2/2 |
| Ex27 | svp\_simple\_0027\_001.c | svp\_simple\_0027\_001\_main | svp\_simple\_027\_001\_isr\_1/1/1 svp\_simple\_027\_001\_isr\_2/2/2 svp\_simple\_027\_001\_isr\_3/3/3 |
| Ex28 | svp\_simple\_0028\_001.c | svp\_simple\_0028\_001\_main | svp\_simple\_028\_001\_isr\_1/1/1 svp\_simple\_028\_001\_isr\_2/2/2 svp\_simple\_028\_001\_isr\_3/3/3 |
| Ex29 | svp\_simple\_0029\_001.c | svp\_simple\_0029\_001\_main | svp\_simple\_029\_001\_isr\_1/1/1 |
| Ex30 | svp\_simple\_0030\_001.c | svp\_simple\_0030\_001\_main | svp\_simple\_030\_001\_isr\_1/1/1 svp\_simple\_030\_001\_isr\_2/2/2 svp\_simple\_030\_001\_isr\_3/3/3 |
| Ex31 | svp\_simple\_0031\_001.c | svp\_simple\_0031\_001\_main | svp\_simple\_031\_001\_isr\_1/1/1 |