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- MODULE AfekSimplified
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This module specifies the simplified Afek et al. snapshot algorithm algorithm described in Section 6.3 of the paper "Auxiliary Variables in TLA+". This is a simplified version of an algorithm in the 1993 paper "Atomic snapshots of Shared Memory" by Afek, Attiya, Dolev, Gafni, Merritt, and Shavit. It will be shown to satisfy the safety specification of a linearizable snapshot object in module NewLinearSnapshot. (The actual algorithm by Afek et al. also satisfies the specification's liveness property, but our simplified version does not.)

EXTENDS Integers

We begin by declaring and defining the same constants as in module NewLinearSnapshot.

CONSTANTS Readers, Writers, RegVals, InitRegVal

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ASSUME \land Readers \cap Writers = {}
\land InitRegVal \in RegVals

MemVals \triangleq [Writers \rightarrow RegVals]

InitMem \triangleq [i \in Writers \mapsto InitRegVal]

NotMemVal \triangleq CHOOSE v : v \notin MemVals

NotRegVal \triangleq CHOOSE v : v \notin RegVals
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Instead of the internal variable mem of the specification, the algorithm maintains an internal variable imem such that for each writer i, the value of imem[i] is a pair $\langle v, k \rangle$, where v is the last register value written by i, and k is the number of times the register has been written by i. The purpose of the second component of imem[i] is to ensure that values written to imem[i] by writer i in different write operations are different.

We now define some constants, including the set *IMemVals* of all possible values of *imem*.

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 \begin{array}{ll} \mathit{IRegVals} & \triangleq \mathit{RegVals} \times \mathit{Nat} \\ \mathit{IMemVals} & \triangleq [\mathit{Writers} \rightarrow \mathit{IRegVals}] \\ \mathit{InitIMem} & \triangleq [\mathit{i} \in \mathit{Writers} \mapsto \langle \mathit{InitRegVal}, 0 \rangle] \\ \end{array}
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In addition to imem, the algorithm has three internal variables: wrNum, rdVal1, and rdVal2. Each writer i records in wrNum[i] the number of times it has written imem[i]. Writer i acts pretty much like the writer in the specification, except that DoWr(i) writes a pair of values in imem[i] and increments wrNum[i]. The writer needs no other internal information because it knows that it has performed a BeginWr(i, cmd) step but not the subsequent DoWr(i) step if wrNum[i] is different from imem[i][2]; and it doesn't have to remember the command cmd because that's in interface[i].

Reader i keeps performing the following scan procedure until the procedure succeeds in computing an output, whereupon the read operation terminates by producing that output. The scan procedure reads imem by reading the elements imem[j] one at a time in any order, and it then reads imem again by reading its elements in any order. The scan procedure succeeds if both reads obtain the same value of imem, in which case it produces the output consisting of the register values of that value of imem. (This procedure produces a correct output only because a writer j cannot write the same value twice in imem[j].) It's possible for the scan procedure never to succeed, in which case the read operation never terminates. Afek et al. have a method for terminating after a finite number of unsuccessful scans, but it complicates the algorithm without significantly changing the structure of its correctness proof.

Reader i keeps in rdVal1[i][j] and rdVal2[i][j] the values of imem[j] that it has read so far during the current scan procedure's reads of imem. The values of rdVal1[i] and rdVal2[i] are each a function that maps a subset of the writers to the values it has read for those writer's registers. They both equal the function $\langle \rangle$ with empty domain when the writer is not performing a scan.

With this explanation of how the algorithm works, it should be easy to understand its TLA+specification.

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VARIABLES imem, interface, wrNum, rdVal1, rdVal2 vars \triangleq \langle imem, interface, wrNum, rdVal1, rdVal2 \rangle
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We define PartialFcns(U, V) to be the set of functions from a subset of U to V. It is used only in the type-correctness invariant.

```
PartialFcns(U, V) \stackrel{\Delta}{=} Union \{ [D \rightarrow V] : D \in SUBSET U \}
TypeOK \triangleq \land imem \in IMemVals
                \land \land DOMAIN interface = Readers \cup Writers
                   \land \forall i \in Readers : interface[i] \in Mem Vals \cup \{NotMem Val\}
                   \land \forall i \in Writers : interface[i] \in RegVals \cup \{NotRegVal\}
                \land wrNum \in [Writers \rightarrow Nat]
                \land rdVal1 \in [Readers \rightarrow PartialFcns(Writers, IRegVals)]
                \land rdVal2 \in [Readers \rightarrow PartialFcns(Writers, IRegVals)]
Init \stackrel{\triangle}{=} \land imem = InitIMem
          \land interface = [i \in Readers \cup Writers \mapsto
                              If i \in Readers then InitMem else NotRegVal
          \land wrNum = [i \in Writers \mapsto 0]
          \land rdVal1 = [i \in Readers \mapsto \langle \rangle]
          \land rdVal2 = [i \in Readers \mapsto \langle \rangle]
BeginWr(i, cmd) \triangleq \land interface[i] = NotRegVal
                           \land wrNum' = [wrNum \ EXCEPT \ ![i] = wrNum[i] + 1]
                           \land interface' = [interface \ EXCEPT \ ![i] = cmd]
                           ∧ UNCHANGED ⟨imem, rdVal1, rdVal2⟩
DoWr(i) \stackrel{\Delta}{=} \land interface[i] \in RegVals
                 \land imem[i][2] \neq wrNum[i]
                 \land imem' = [imem \ EXCEPT \ ![i] = \langle interface[i], wrNum[i] \rangle]
                 ∧ UNCHANGED ⟨interface, wrNum, rdVal1, rdVal2⟩
EndWr(i) \stackrel{\Delta}{=} \land interface[i] \in RegVals
                  \land imem[i][2] = wrNum[i]
                  \land interface' = [interface \ EXCEPT \ ![i] = NotRegVal]
                  ∧ UNCHANGED ⟨imem, wrNum, rdVal1, rdVal2⟩
BeginRd(i) \stackrel{\triangle}{=} \land interface[i] \in MemVals
                   \land interface' = [interface \ EXCEPT \ ![i] = NotMem Val]
                   ∧ UNCHANGED ⟨imem, wrNum, rdVal1, rdVal2⟩
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If x is not in the domain of the function v, then AddToFcn(f, x, v) is the function obtained from f by adding x to its domain and letting x be mapped to v. This could be written as as f @@ (x:>v), where the operators :> and @@ are defined in the standard TLC module.

```
AddToFcn(f, x, v) \triangleq
  [y \in (\text{DOMAIN } f) \cup \{x\} \mapsto \text{IF } y = x \text{ THEN } v \text{ ELSE } f[y]]
Rd1(i) \stackrel{\triangle}{=} \wedge interface[i] = NotMemVal
               \land \exists j \in Writers \setminus DOMAIN \ rdVal1[i] :
                   rdVal1' = [rdVal1 \text{ EXCEPT } ![i] = AddToFcn(rdVal1[i], j, imem[j])]
               ∧ UNCHANGED ⟨interface, imem, wrNum, rdVal2⟩
Rd2(i) \stackrel{\triangle}{=} \wedge interface[i] = NotMemVal
               \land DOMAIN rdVal1[i] = Writers
               \land \exists j \in Writers \setminus DOMAIN \ rdVal2[i] :
                   rdVal2' = [rdVal2 \text{ EXCEPT } ![i] = AddToFcn(rdVal2[i], j, imem[j])]
               ∧ UNCHANGED ⟨interface, imem, wrNum, rdVal1⟩
TryEndRd(i) \stackrel{\triangle}{=} \land interface[i] = NotMemVal
                       \land DOMAIN rdVal1[i] = Writers
                       \land DOMAIN rdVal2[i] = Writers
                       \wedge IF rdVal1[i] = rdVal2[i]
                             THEN \wedge interface' =
                                           [interface Except
                                             ![i] = [j \in Writers \mapsto rdVal1[i][j][1]]]
                             ELSE \land interface' = interface
                       \wedge rdVal1' = [rdVal1 \text{ EXCEPT } ![i] = \langle \rangle]
                       \wedge rdVal2' = [rdVal2 \text{ EXCEPT } ![i] = \langle \rangle]
                       \land UNCHANGED \langle imem, wrNum \rangle
Next \triangleq \forall \exists i \in Readers : BeginRd(i) \lor Rd1(i) \lor Rd2(i) \lor TryEndRd(i)
            \vee \exists i \in Writers : \vee \exists cmd \in RegVals : BeginWr(i, cmd)
                                    \vee DoWr(i) \vee EndWr(i)
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Since a read need never terminate, the algorithm doesn't satisfy the NewLinearSnapshot specification's liveness requirements, so we don't bother specifying any fairness of the actions.

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Spec \triangleq Init \wedge \Box [Next]_{vars}
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^{*} Modification History

^{*} Last modified Sat Oct 22 01:58:50 PDT 2016 by lamport

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