

1 MODULE *AfekSimplified*

This module specifies the simplified *Afek* et al. snapshot algorithm described in Section 6.3 of the paper “Auxiliary Variables in TLA+”. This is a simplified version of an algorithm in the 1993 paper “Atomic snapshots of Shared Memory” by *Afek, Attiya, Dolev, Gafni*, Merritt, and *Shavit*. It will be shown to satisfy the safety specification of a linearizable snapshot object in module *NewLinearSnapshot*. (The actual algorithm by *Afek* et al. also satisfies the specification’s liveness property, but our simplified version does not.)

13 EXTENDS *Integers*

We begin by declaring and defining the same constants as in module *NewLinearSnapshot*.

19 CONSTANTS *Readers*, *Writers*, *RegVals*, *InitRegVal*

21 ASSUME $\wedge \text{Readers} \cap \text{Writers} = \{\}$

22 $\wedge \text{InitRegVal} \in \text{RegVals}$

24 $\text{MemVals} \triangleq [\text{Writers} \rightarrow \text{RegVals}]$

25 $\text{InitMem} \triangleq [i \in \text{Writers} \mapsto \text{InitRegVal}]$

26 $\text{NotMemVal} \triangleq \text{CHOOSE } v : v \notin \text{MemVals}$

27 $\text{NotRegVal} \triangleq \text{CHOOSE } v : v \notin \text{RegVals}$

Instead of the internal variable *mem* of the specification, the algorithm maintains an internal variable *imem* such that for each writer *i*, the value of *imem*[*i*] is a pair $\langle v, k \rangle$, where *v* is the last register value written by *i*, and *k* is the number of times the register has been written by *i*. The purpose of the second component of *imem*[*i*] is to ensure that values written to *imem*[*i*] by writer *i* in different write operations are different.

We now define some constants, including the set *IMemVals* of all possible values of *imem*.

41 $\text{IRegVals} \triangleq \text{RegVals} \times \text{Nat}$

42 $\text{IMemVals} \triangleq [\text{Writers} \rightarrow \text{IRegVals}]$

43 $\text{InitIMem} \triangleq [i \in \text{Writers} \mapsto \langle \text{InitRegVal}, 0 \rangle]$

In addition to *imem*, the algorithm has three internal variables: *wrNum*, *rdVal1*, and *rdVal2*. Each writer *i* records in *wrNum*[*i*] the number of times it has written *imem*[*i*]. Writer *i* acts pretty much like the writer in the specification, except that *DoWr*(*i*) writes a pair of values in *imem*[*i*] and increments *wrNum*[*i*]. The writer needs no other internal information because it knows that it has performed a *BeginWr*(*i*, *cmd*) step but not the subsequent *DoWr*(*i*) step if *wrNum*[*i*] is different from *imem*[*i*][2]; and it doesn’t have to remember the command *cmd* because that’s in *interface*[*i*].

Reader *i* keeps performing the following scan procedure until the procedure succeeds in computing an output, whereupon the read operation terminates by producing that output. The scan procedure reads *imem* by reading the elements *imem*[*j*] one at a time in any order, and it then reads *imem* again by reading its elements in any order. The scan procedure succeeds if both reads obtain the same value of *imem*, in which case it produces the output consisting of the register values of that value of *imem*. (This procedure produces a correct output only because a writer *j* cannot write the same value twice in *imem*[*j*].) It’s possible for the scan procedure never to succeed, in which case the read operation never terminates. *Afek* et al. have a method for terminating after a finite number of unsuccessful scans, but it complicates the algorithm without significantly changing the structure of its correctness proof.

Reader i keeps in $rdVal1[i][j]$ and $rdVal2[i][j]$ the values of $imem[j]$ that it has read so far during the current scan procedure's reads of $imem$. The values of $rdVal1[i]$ and $rdVal2[i]$ are each a function that maps a subset of the writers to the values it has read for those writer's registers. They both equal the function $\langle \rangle$ with empty domain when the writer is not performing a scan.

With this explanation of how the algorithm works, it should be easy to understand its TLA+ specification.

81 VARIABLES $imem, interface, wrNum, rdVal1, rdVal2$
 82 vars $\triangleq \langle imem, interface, wrNum, rdVal1, rdVal2 \rangle$

We define $PartialFcns(U, V)$ to be the set of functions from a subset of U to V . It is used only in the type-correctness invariant.

88 $PartialFcns(U, V) \triangleq \text{UNION } \{[D \rightarrow V] : D \in \text{SUBSET } U\}$
 89 $TypeOK \triangleq \wedge imem \in IMemVals$
 90 $\wedge \wedge \text{DOMAIN } interface = Readers \cup Writers$
 91 $\wedge \forall i \in Readers : interface[i] \in MemVals \cup \{NotMemVal\}$
 92 $\wedge \forall i \in Writers : interface[i] \in RegVals \cup \{NotRegVal\}$
 93 $\wedge wrNum \in [Writers \rightarrow Nat]$
 94 $\wedge rdVal1 \in [Readers \rightarrow PartialFcns(Writers, IRegVals)]$
 95 $\wedge rdVal2 \in [Readers \rightarrow PartialFcns(Writers, IRegVals)]$

 97 $Init \triangleq \wedge imem = InitIMem$
 98 $\wedge interface = [i \in Readers \cup Writers \mapsto$
 99 $\text{IF } i \in Readers \text{ THEN } InitMem \text{ ELSE } NotRegVal]$
 100 $\wedge wrNum = [i \in Writers \mapsto 0]$
 101 $\wedge rdVal1 = [i \in Readers \mapsto \langle \rangle]$
 102 $\wedge rdVal2 = [i \in Readers \mapsto \langle \rangle]$

 104 $BeginWr(i, cmd) \triangleq \wedge interface[i] = NotRegVal$
 105 $\wedge wrNum' = [wrNum \text{ EXCEPT } ![i] = wrNum[i] + 1]$
 106 $\wedge interface' = [interface \text{ EXCEPT } ![i] = cmd]$
 107 $\wedge \text{UNCHANGED } \langle imem, rdVal1, rdVal2 \rangle$

 109 $DoWr(i) \triangleq \wedge interface[i] \in RegVals$
 110 $\wedge imem[i][2] \neq wrNum[i]$
 111 $\wedge imem' = [imem \text{ EXCEPT } ![i] = \langle interface[i], wrNum[i] \rangle]$
 112 $\wedge \text{UNCHANGED } \langle interface, wrNum, rdVal1, rdVal2 \rangle$

 114 $EndWr(i) \triangleq \wedge interface[i] \in RegVals$
 115 $\wedge imem[i][2] = wrNum[i]$
 116 $\wedge interface' = [interface \text{ EXCEPT } ![i] = NotRegVal]$
 117 $\wedge \text{UNCHANGED } \langle imem, wrNum, rdVal1, rdVal2 \rangle$

 119 $BeginRd(i) \triangleq \wedge interface[i] \in MemVals$
 120 $\wedge interface' = [interface \text{ EXCEPT } ![i] = NotMemVal]$
 121 $\wedge \text{UNCHANGED } \langle imem, wrNum, rdVal1, rdVal2 \rangle$

If x is not in the domain of the function v , then $AddToFcn(f, x, v)$ is the function obtained from f by adding x to its domain and letting x be mapped to v . This could be written as $f @@ (x \mapsto v)$, where the operators \mapsto and $@@$ are defined in the standard *TLC* module.

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129  $AddToFcn(f, x, v) \triangleq$ 
130    $[y \in (\text{DOMAIN } f) \cup \{x\} \mapsto \text{IF } y = x \text{ THEN } v \text{ ELSE } f[y]]$ 

132  $Rd1(i) \triangleq$   $\wedge \text{interface}[i] = \text{NotMemVal}$ 
133    $\wedge \exists j \in \text{Writers} \setminus \text{DOMAIN } rdVal1[i] :$ 
134      $rdVal1' = [rdVal1 \text{ EXCEPT } ![i] = AddToFcn(rdVal1[i], j, imem[j])]$ 
135    $\wedge \text{UNCHANGED } \langle \text{interface}, imem, wrNum, rdVal2 \rangle$ 

137  $Rd2(i) \triangleq$   $\wedge \text{interface}[i] = \text{NotMemVal}$ 
138    $\wedge \text{DOMAIN } rdVal1[i] = \text{Writers}$ 
139    $\wedge \exists j \in \text{Writers} \setminus \text{DOMAIN } rdVal2[i] :$ 
140      $rdVal2' = [rdVal2 \text{ EXCEPT } ![i] = AddToFcn(rdVal2[i], j, imem[j])]$ 
141    $\wedge \text{UNCHANGED } \langle \text{interface}, imem, wrNum, rdVal1 \rangle$ 

143  $TryEndRd(i) \triangleq$   $\wedge \text{interface}[i] = \text{NotMemVal}$ 
144    $\wedge \text{DOMAIN } rdVal1[i] = \text{Writers}$ 
145    $\wedge \text{DOMAIN } rdVal2[i] = \text{Writers}$ 
146    $\wedge \text{IF } rdVal1[i] = rdVal2[i]$ 
147      $\text{THEN } \wedge \text{interface}' =$ 
148        $[interface \text{ EXCEPT }$ 
149          $![i] = [j \in \text{Writers} \mapsto rdVal1[i][j][1]]]$ 
150        $\text{ELSE } \wedge \text{interface}' = interface$ 
151    $\wedge rdVal1' = [rdVal1 \text{ EXCEPT } ![i] = \langle \rangle]$ 
152    $\wedge rdVal2' = [rdVal2 \text{ EXCEPT } ![i] = \langle \rangle]$ 
153    $\wedge \text{UNCHANGED } \langle imem, wrNum \rangle$ 

155  $Next \triangleq$   $\vee \exists i \in \text{Readers} : \text{BeginRd}(i) \vee Rd1(i) \vee Rd2(i) \vee TryEndRd(i)$ 
156    $\vee \exists i \in \text{Writers} : \vee \exists cmd \in \text{RegVals} : \text{BeginWr}(i, cmd)$ 
157    $\vee DoWr(i) \vee EndWr(i)$ 

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Since a read need never terminate, the algorithm doesn't satisfy the *NewLinearSnapshot* specification's liveness requirements, so we don't bother specifying any fairness of the actions.

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164  $Spec \triangleq Init \wedge \Box [Next]_{vars}$ 
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\ * Modification History
\ * Last modified Sat Oct 22 01:58:50 PDT 2016 by lamport
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