

TLA+ TLC

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Outline

Overview of TLA/TLA+
Subset of TLA+ supported by TLC
Alternating Bit Protocol example
Model checking
Demo

L. Lamport, *Specifying Concurrent Systems with TLA+*,
Dec. 2000.

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Overview

TLA+

stylized way of expressing TLA models

TLC

takes subset of TLA+ & configuration file
invariance, step simulation, temporal checking
outputs no error, or counterexample trace

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TLA

TLA model

$$\phi = \text{init} \wedge [M]_{\langle \text{vars} \rangle} \wedge \text{Temporal}$$

$$M = A1 \vee A2 \vee \dots \vee An$$

Semantics

$$\langle s_0, s_1, \dots \rangle \models \phi \iff s_0 \models \text{init} \wedge$$

$$\forall n: s_n \models [M \vee (\text{vars}' = \text{vars})] \wedge s_{n+1} \wedge$$

$$\langle s_0, s_1, \dots \rangle \models \text{Temporal}$$

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TLA (cont.)

Property to check P

$$\phi \Rightarrow P$$

Refinement Mapping ψ

$$\phi \Rightarrow \psi$$

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TLA+

```
-----Module M -----
EXTENDS    M1, ... ,Mn
CONSTANTS C1, ... , Cn
VARIABLES  x1, ... , xn
ASSUME     P
F (x1, ... , xn) == exp
f [x \in S]   == exp
-----
THEOREM P
=====
```

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Small Example

```

----- MODULE HourClock -----
EXTENDS Naturals
VARIABLE hr
HCini == hr \in (1 .. 12)
HCnxt == hr' = IF hr # 12 THEN hr + 1 ELSE 1
HC == HCini \wedge [HCnxt]_hr
-----
THEOREM HC => Hcini
=====

```

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Acceptable Subset of TLA+: Values

TLC values:

bool, int, string, model values

finite set of comparable TLC values

function with TLC domain and range

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Acceptable Subset of TLA+: Expressions

Left to right evaluation:

$(x \# <>) \wedge (x[1] = 0)$	valid
$(x[1] = 0) \wedge (x \# <>)$	not valid

Sets:

$\{i \in 0 .. 5 : i < 4\}$	valid
$\{i \in \text{Nat} : i < 4\}$	not valid

Functions:

$[n \in \text{Nat} \rightarrow n * (n+1)][3]$	valid
$[n \in \text{Nat} \rightarrow n * (n+1)]$	not valid

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Acceptable Subset of TLA+: Actions

Left to right evaluation

$A \equiv x' \in 1 .. \text{Len}(y) \wedge$	
$y' = \text{Append}(\text{Tail}(y), x')$	valid

$A \equiv y' = \text{Append}(\text{Tail}(y), x') \wedge$	
$x' \in 1 .. \text{Len}(y)$	not valid

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Actions (cont.)

Evaluation of all branches:

$A \equiv ((x = <>) \vee (x[1] = 0))$	
$\wedge x' = \text{Append}(x, 0)$	not valid

Evaluated as 2 actions:

$(x = <>)$	$(x[1] = 0)$
$\wedge x' = \text{Append}(x, 0)$	$\wedge x' = \text{Append}(x, 0)$

$A \equiv ((x = <>) \vee$	
$(x \# <>) \wedge (x[1] = 0))$	
$\wedge x' = \text{Append}(x, 0)$	valid

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Examples

Alternating Bit Protocol

Chapter 13

[tlatk/examples/alternatingbit/](#)

Caching Memory

Chapter 5

[tlatk/examples/fm99/](#)

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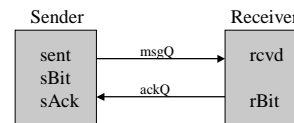
Alternating Bit Protocol

Protocol description
TLA+ specification
Configuration file

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Alternating Bit Protocol

Sending messages over lossy channel



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ABP: protocol description

Sender A, receiver B
A repeatedly sends the current message tagged with a protocol bit (sBit)
B acks with the same bit
A moves on to the next message once it receives sAck=sBit and flips sBit

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ABP: TLA+ specification

```

MODULE AlternatingBit
CONSTANT Data
VARIABLES msgQ, ackQ, sBit, sAck, rBit, sent, rcvd

ABInit (initial condition)
TypeInv (type-correctness invariant)
ABNext (next-state relation)
Inv (invariant)
  
```

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ABP: configuration file

Specifies the names of
the initial predicate
next-state relation
invariants
the temporal formula
Assigns values to the constants
Further constrains the model to make it finite

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ABP: configuration file (cont.)

```

MCAAlternatingBit.tla file
MCAAlternatingBit.cfg file:
  CONSTANTS Data = {d1, d2}
             msgQLen = 2
             ackQLen = 2
             sentLen = 3
  INIT       ABInit
  NEXT       ABNext
  INVARIANTS Inv
             TypeInv
  CONSTRAINT SeqConstraint
  
```

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TLC Capabilities

Can check these types of conditions:

initial condition: $\text{Init} \Rightarrow \text{ImpliedInit}$
invariance: $\text{Init} \wedge [\text{Next}]_{\langle \text{vars} \rangle} \Rightarrow \text{Invariant}$
step simulation: $\text{Init} \wedge [\text{Next}]_{\langle \text{vars} \rangle} \Rightarrow [\text{ImpliedAction}]_{\langle \text{vars} \rangle}$
temporal: $\text{Init} \wedge [\text{Next}]_{\langle \text{vars} \rangle} \wedge \text{Temporal} \Rightarrow \text{ImpliedTemporal}$

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Model Checking

A *state* is an assignment of values to variables.

First generate all initial states by enumerating all possible initial assignments.

Do BFS for new states.

For each state s , action A , check if A on s leads to a new state.

Keep track of states already seen.

If invariant or temporal properties is violated, TLC stops and reports the error.

Stop when all states lead to seen states.

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Internals

Memory management

Bit-state hashing

Symmetry wrt variable permutations

Writes states to disk, uses prefetch for access

Written in Java, multi-threaded

Models are interpreted, not compiled

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TLC Limitations

Cannot check some temporal formulas.

e.g. $\text{EvenSpec} \equiv (x=0) \wedge [x'=x+2]_x \wedge \text{WF}_x(x'=x+2)$

$\text{OddProp} \equiv \text{WF}_x(x'=x+2) \Rightarrow \Diamond(x=1)$

OddProp is false, but TLC doesn't report an error.

TLC only generates finite behaviors, so $\text{WF}_x(x'=x+2)$ is false, and OddProp is true.

Note EvenSpec isn't finite state.

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Conclusion

Advantages:

TLA+ expressive, relatively simple
multiple levels of abstraction, refinement mapping
infinite state models

Disadvantages:

no uniform way of making TLA+ models suitable for TLC
configuration files
order of evaluation
evaluation of all branches
sublanguage bound to analysis method

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