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- Module ABSpec
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2 EXTENDS Integers

 $^{1}$   $_{\sqcap}$ 

- 4 CONSTANT Data The set of all possible data values.
- 6 VARIABLES AVar, The last  $\langle value, bit \rangle$  pair A decided to send.
- 7 BVar The last  $\langle value, bit \rangle$  pair B received.

Type correctness means that AVar and BVar are tuples  $\langle d, i \rangle$  where  $d \in Data$  and  $i \in \{0, 1\}$ .

13 
$$TypeOK \triangleq \land AVar \in Data \times \{0, 1\}$$
  
14  $\land BVar \in Data \times \{0, 1\}$ 

It's useful to define vars to be the tuple of all variables, for example so we can write  $[Next]\_vars$  instead of  $[Next]\_\langle \dots \rangle$ 

 $vars \triangleq \langle AVar, BVar \rangle$ 

Initially AVar can equal  $\langle d, 1 \rangle$  for any Data value d, and BVar equals AVar.

$$\begin{array}{rcl}
27 & Init & \stackrel{\triangle}{=} & \wedge AVar \in Data \times \{1\} \\
28 & & \wedge BVar = AVar
\end{array}$$

When AVar = BVar, the sender can "send" an arbitrary data d item by setting AVar[1] to d and complementing AVar[2]. It then waits until the receiver "receives" the message by setting BVar to AVar before it can send its next message. Sending is described by action A and receiving by action B.

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37 A \triangleq \land AVar = BVar

38 \land \exists d \in Data : AVar' = \langle d, 1 - AVar[2] \rangle

39 \land BVar' = BVar

41 B \triangleq \land AVar \neq BVar

42 \land BVar' = AVar

43 \land AVar' = AVar

44 \land AVar' = AVar

45 \land AVar \triangleq A \lor B

47 \land AVar \triangleq A \lor B
```

For understanding the spec, it's useful to define formulas that should be invariants and check that they are invariant. The following invariant Inv asserts that, if AVar and BVar have equal second components, then they are equal (which by the invariance of TypeOK implies that they have equal first components).

56 
$$Inv \triangleq (AVar[2] = BVar[2]) \Rightarrow (AVar = BVar)$$

58 
$$DeliveryLiveness \stackrel{\Delta}{=} \forall v \in Data \times \{0, 1\} : (AVar = v) \leadsto (BVar = v)$$

FairSpec is Spec with the addition requirement that it keeps taking steps

64  $FairSpec \triangleq Spec \wedge WF_{vars}(Next)$ 

FairABSpec is Spec with the additional requirement that both A and B keep taking steps.

70  $FairABSpec \triangleq Spec \wedge WF_{vars}(A) \wedge WF_{vars}(B)$ 

FairBSpec is Spec with the additional requirement that every sent value to be received, but allows

the sender to stop sending. 76  $FairBSpec \triangleq Spec \wedge WF_{vars}(B)$ 

- $\backslash * \ {\bf Modification} \ {\bf History}$
- \ \* Last modified Sat Aug 11 22:07:13 CST 2018 by hengxin \ \* Last modified Sat May 12 20:51:26 CST 2018 by hengxin
- \\* Last modified Wed Oct 18 04:07:37 PDT 2017 by lamport
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