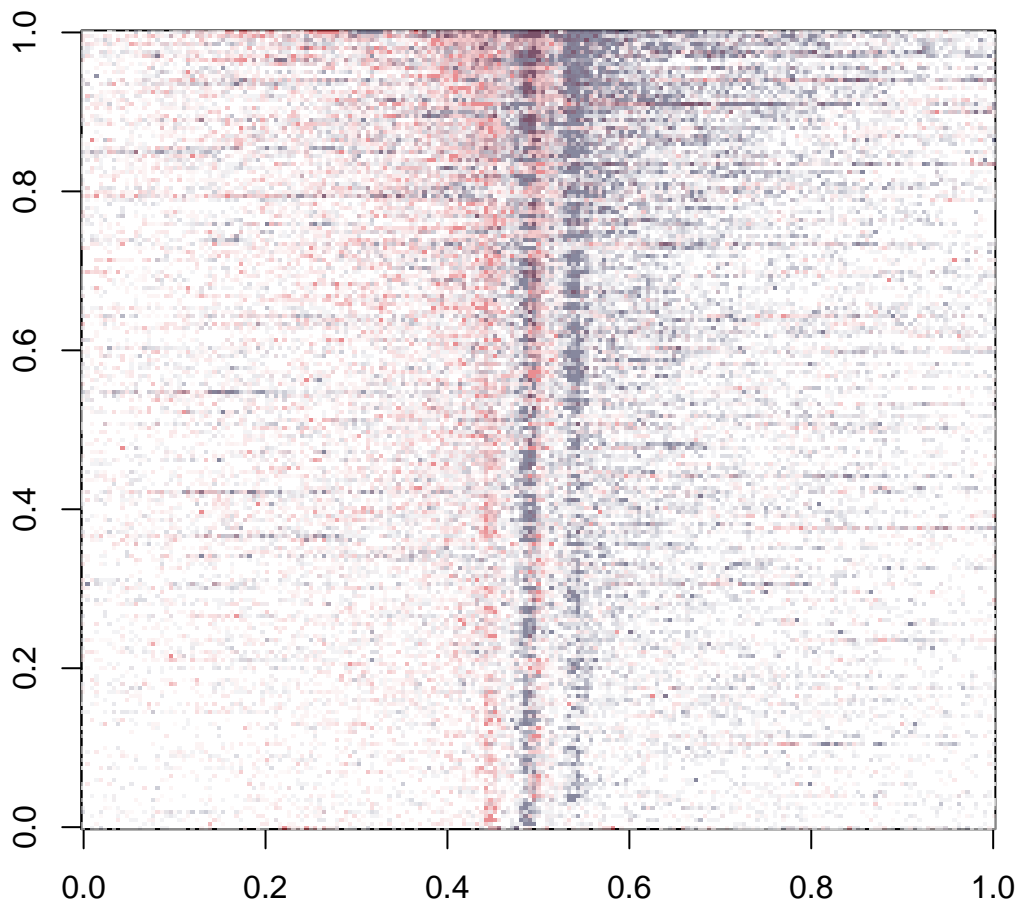
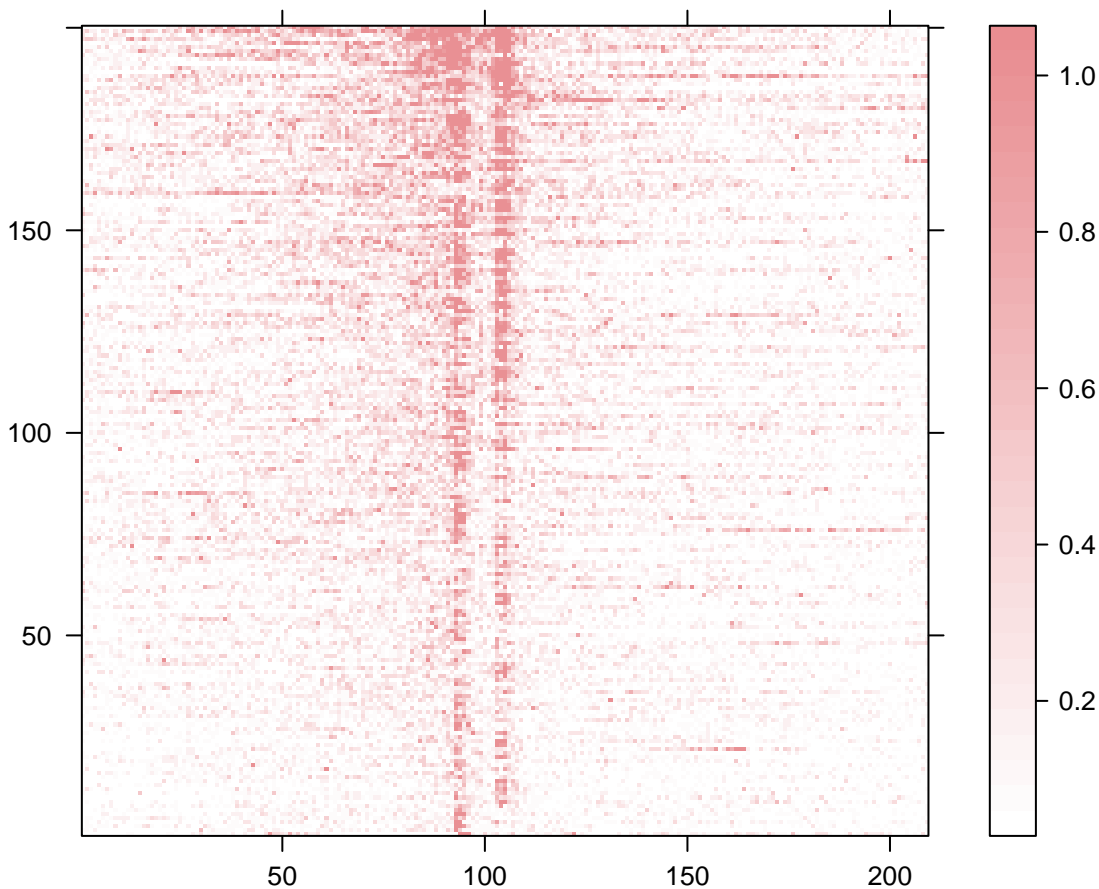


dmel_s2_max_chipnexus_01



dmel_s2_max_chipnexus_01 – positive strand



dmel_s2_max_chipnexus_01 – negative strand

