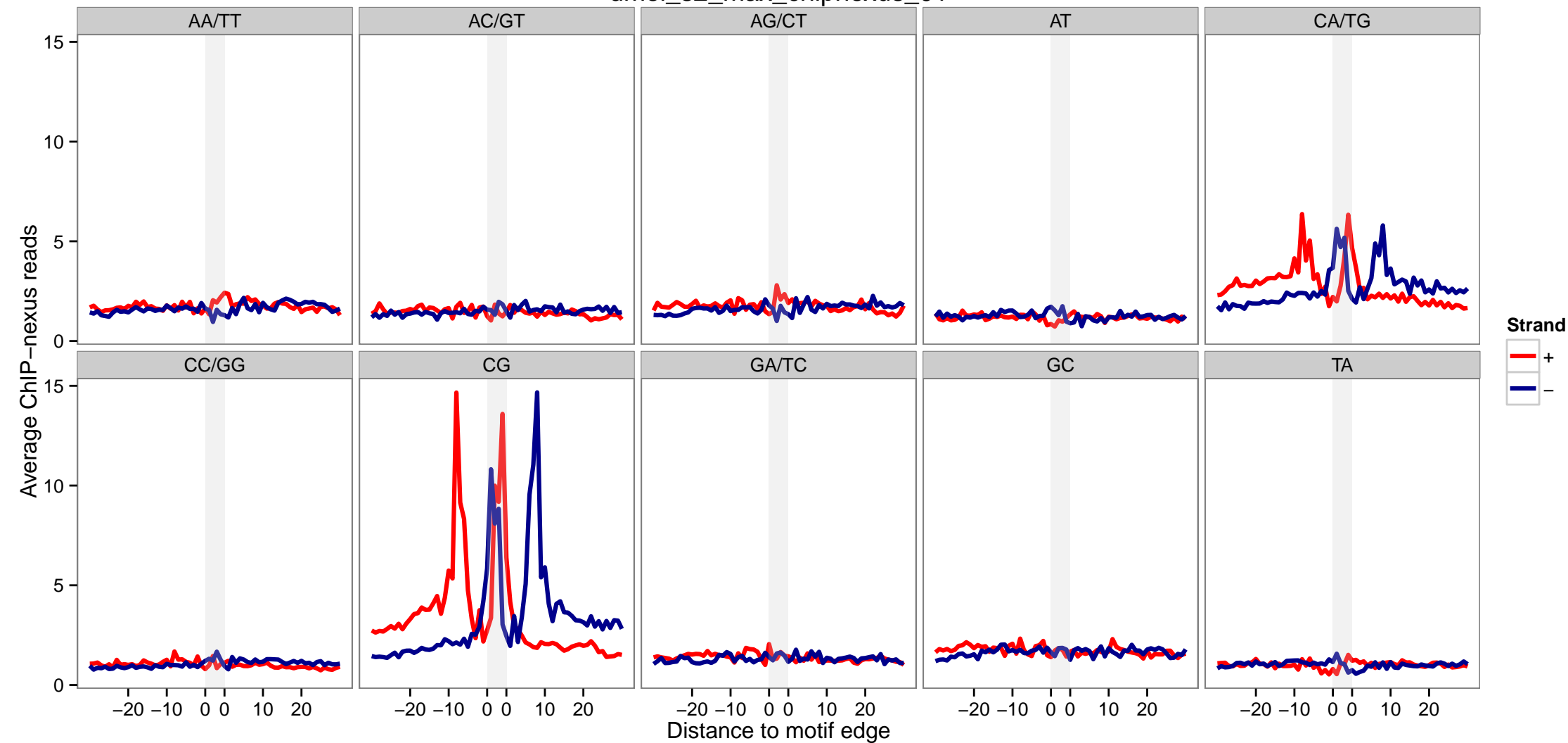


dmel_s2_max_chipnexus_01



dmel_s2_max_chipnexus_02

