Lecture 10 i think

pushf - push all the flags into the stack

pop eax ; the configuration of the flags register is in eax

……..

push eax

popf

4.1.2. Type conversion instructions (destructive)

example

movzx d, s - loads into d the unsigned contents of s (zero extension)

d - has to be a register

s - source

d has to be of size largel than s

```
movsx ah, [v] ; syntax error, the assembler can't fit
              ; it because ah is a byte (smallest rep)
movzx ax, [v] ; movzx ax, byte ptr[v]
movsx eax, [v] ; syntax error, operation size not
               ; specified, both posibilites are valid
               ; v could've been byte or word
movzx eax, -1 ; syntax error, source opperand is not
              ; a register nor a memory operand, it
              ; is a constant
movsx eax, v ; syntax error, v is a constant (it's the
             ; address)
             ; the source operand must be a register
             ; or a memory operand
v without [] can fit into a word, but we can't
be sure that it is a word and not a doubleword
```

c-jump.com - un fel de wikipedia or some shit care aparent este gresit so nvm

4.3.4. Arithmetic operations

conditional jumps can be performed more than 127 bytes in nasm

loop can't do that