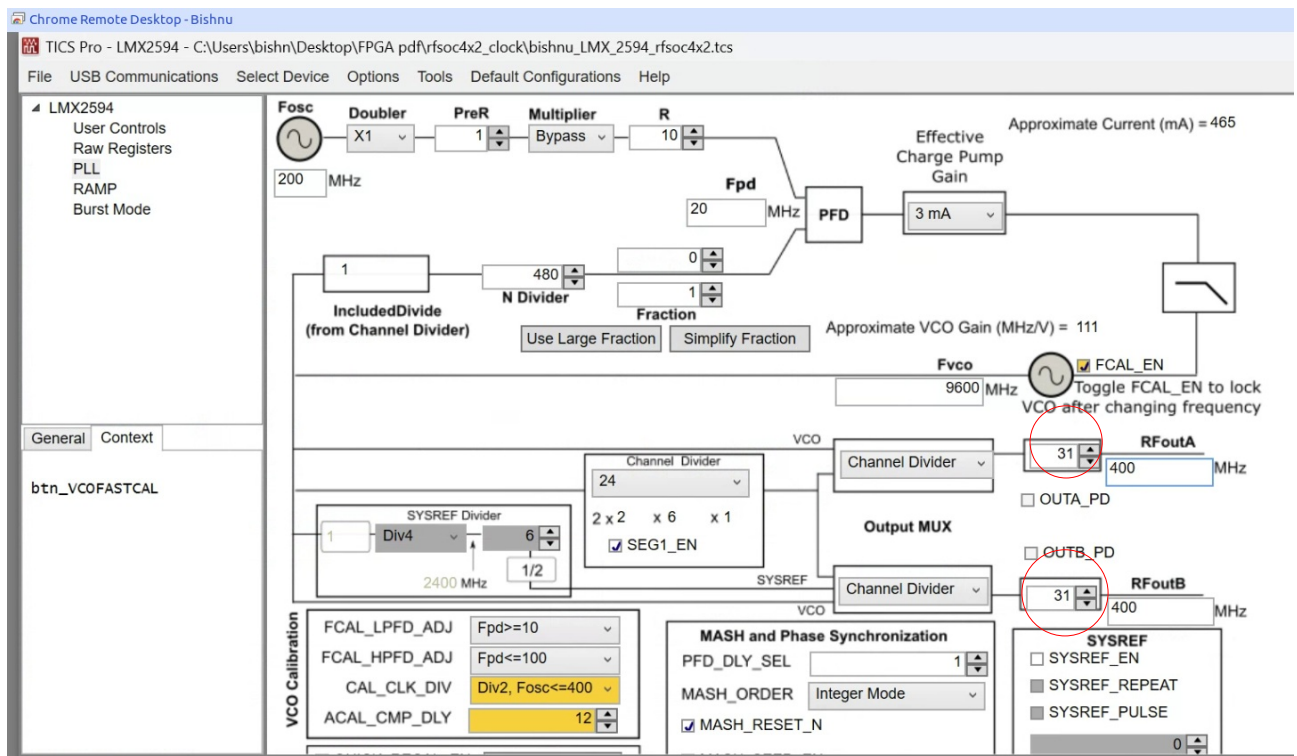
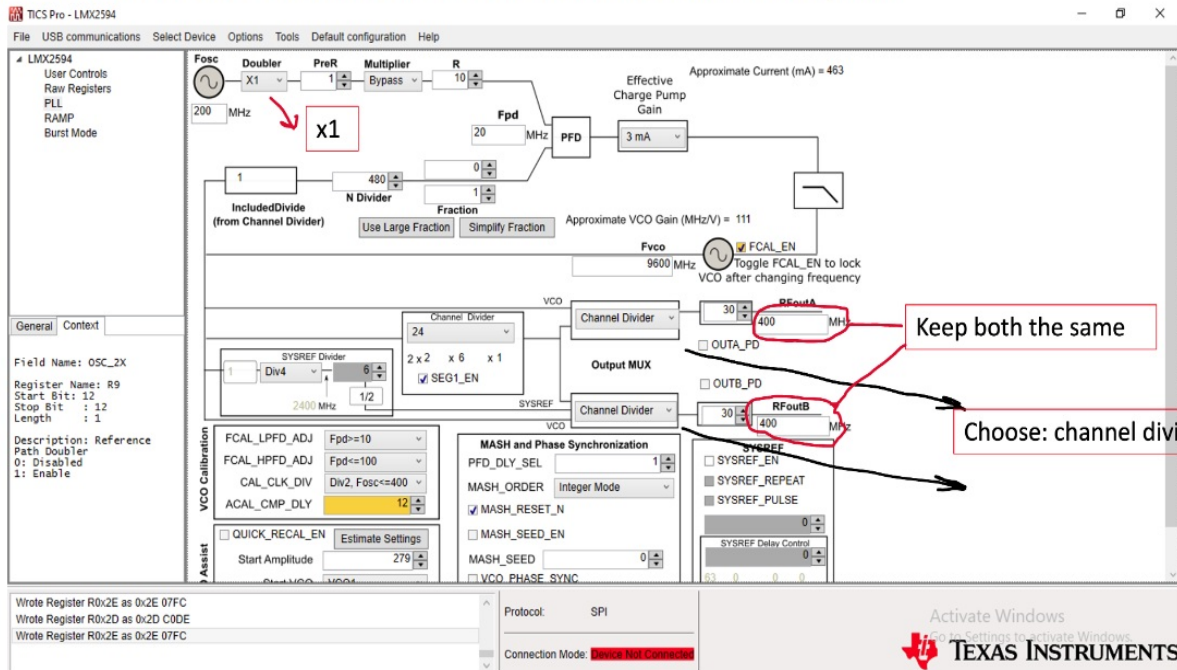


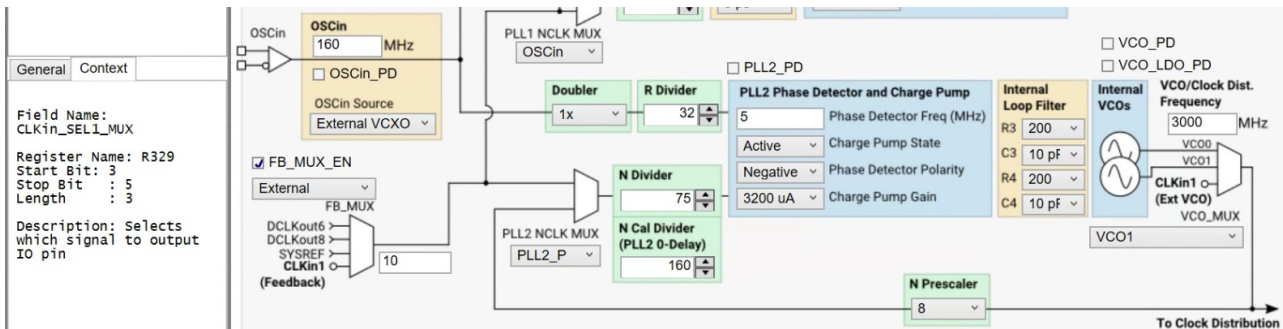
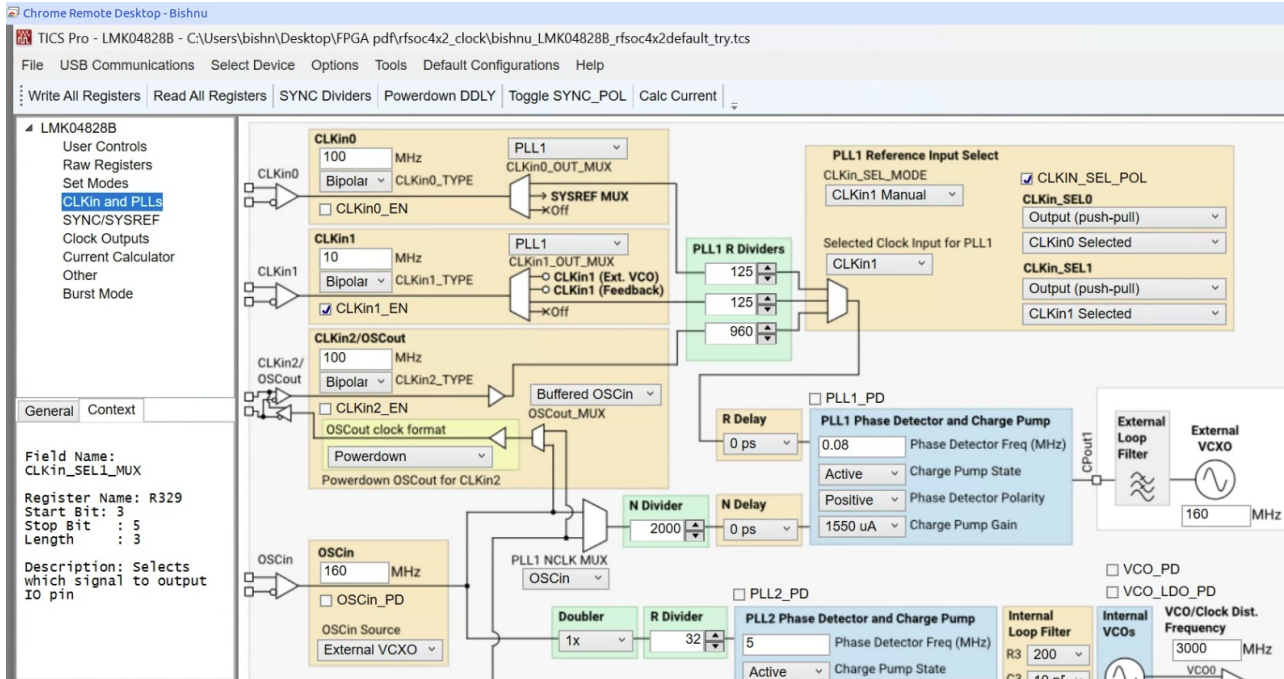
The TICS pro interface of clock files: LMX2594

* LMX 2594

- Refer to LMX2594 datasheet, check on the block diagram.



LMK04828Bclockfile



TICS Pro - LMK04828B - C:\Users\bishn\Desktop\FPGA pdf\rfsoc4x2_clock\bishnu_LMK04828B_rfsoc4x2default_try.tcs

File USB Communications Select Device Options Tools Default Configurations Help

Write All Registers Read All Registers SYNC Dividers Powerdown DDLY Toggle SYNC_POL Calc Current

LMK04828B

- User Controls
- Raw Registers
- Set Modes
- CLKin and PLLs
- SYNC/SYSREF**
- Clock Outputs
- Current Calculator
- Other
- Burst Mode

General Context

Field Name: SYNC_MODE

Register Name: R323

Start Bit: 0

Stop Bit: 1

Length: 2

Description: Enable SYNC.

Set SYNC/JESD204B SYSREF Output Mode

Normal Re-Clocked Pulser Continuous SYSREF Request

Set SYNC/JESD204B SYSREF Input Mode/Source

None SYNC Pin CLKin0 CLKin0 Bypass

SYNC Dividers

PLL1

CLKin0_OUT_MUX

CLKin0

SYNC

SYNC_POL

SYNC SPI (Pulser)

SYNC Pin Disabled

SPI (Pulser)

Send Pulses

SYNREF_REQ_EN

SYNREF Pulser

SYNREF_MUX

Normal

Re-clocked

Pulser

Continuous

SYNREF_CLKin0_MUX

From SYNREF_MUX

SYNREF Distribution Path

Clock Distribution Path

Other SYNC Controls

☒ SYNC_EN

☐ SYNC_PLL1_DLD

☐ SYNC_PLL2_DLD

☐ SYNC_1SHOT_EN

☐ SYSREF_CLR

SYNC Disable Bits

☐ SYNC_DISSYSREF

DC On DC Off

☒ SYNC_DIS0

☒ SYNC_DIS2

☒ SYNC_DIS4

☒ SYNC_DIS6

☒ SYNC_DIS8

☒ SYNC_DIS10

☒ SYNC_DIS12

Pulser

☐ SYSREF_PLRSR_PD

SYNREF_PULSE_CNT 8

Field Name: SYNC_MODE

Register Name: R323

Start Bit: 0

Stop Bit: 1

Length: 2

Description: Enable SYNC.

VCO

VCO1

Internal VCO Frequency

3000 MHz

☐ SYSREF_PD

SYNREF Digital Delay

1

☐ SYSREF_DDLY_PD

SYNREF Clock Divider

480

☐ SYNC_DISSYSREF

SYNREF Frequency 6.25 MHz

☐ SYSREF_GBL_PD

SDCLKoutY_DIS_MODE

7 Conditional Low

1 Active

9 Active

3 Active

11 Active

5 Active

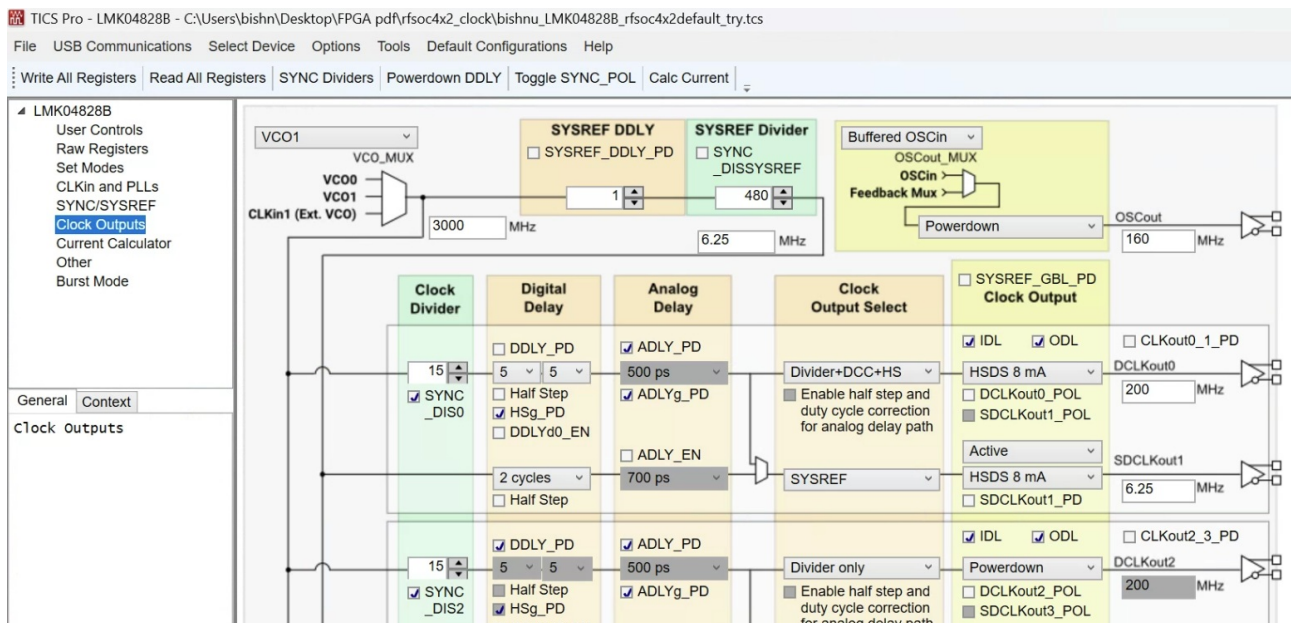
13 Conditional Low

Condition is if SYSREF_GBL_PD = 1

☒ SYNC_DIS8

☒ SYNC_DIS10

☒ SYNC_DIS12



These are the screenshots of the clock files from TICS pro software.