#### COMP 122/L Lecture 4

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Slides adapted from Dr. Kyle Dewey

#### Outline

- Operations on binary values
  - Addition
  - Subtraction
- Floating point introduction

# Addition

1	
	6
	+3
	<del>-</del> -
	?

• Question: how might we add the following, in decimal?

7

	8	6
	+2	+3
	?	9

• Question: how might we add the following, in decimal?

7

Carry: 1	8	6
	+2	+3
	<del></del>	
	0	9

1	8	6
9	+2	+3
+1		
	0	9
?		

Carry:1	1	8	6
	9	+2	+3
	+1		
		0	9
	1		

1+0	1	8	6
	9	+2	+3
	+1		
1	 1	0	9

#### Core Concepts

- We have a "primitive" notion of adding single digits, along with an idea of carrying digits
- We can build on this notion to add numbers together that are more than one digit long

# Now in Binary

Arguably simpler - fewer one-bit possibilities

0	0	1	1
+0	+1	+0	+1
?	?	3	3

# Now in Binary

Arguably simpler - fewer one-bit possibilities

0	0	1	1
+0	+1	+0	+1
0	1	1	0
			Carry:1

#### Chaining the Carry

Also need to account for any input carry

0	0		0		0	
0	0		1		1	
+0	+1		+0		+1	
0	1		1		0	Carry: 1
1	1		1		1	
0	0		1		1	
+0	+1		+0		+1	
1	0	Carry: 1	0	Carry: 1	1	Carry: 1

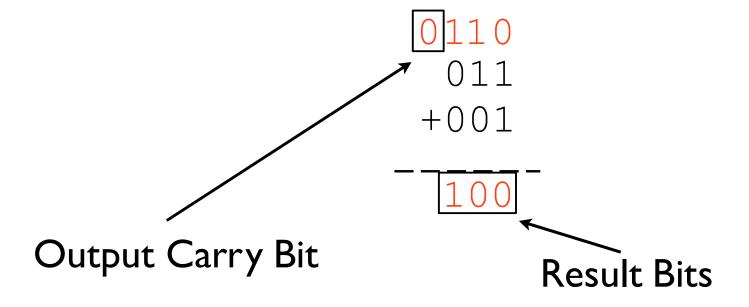
• How might we add the numbers below?

011 +001 ----

• How might we add the numbers below?

0 011 +001

$$\begin{array}{r}
0110 \\
011 \\
+001 \\
-----
100
\end{array}$$

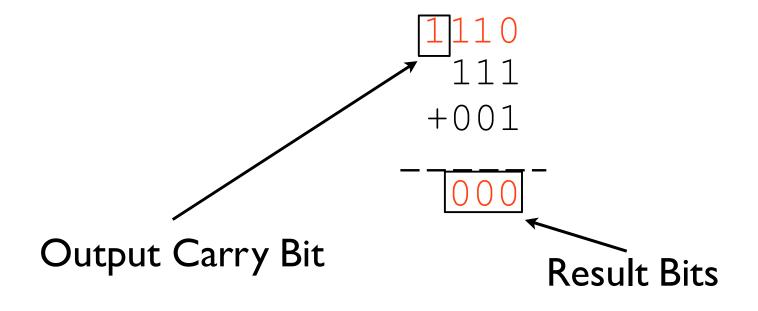


111+001

0 111 +001

```
10
111
+001
----
```

```
110
111
+001
-----
```



# Output Carry Bit Significance

- For unsigned numbers, it indicates if the result did not fit all the way into the number of bits allotted
- May be an error condition for software

#### Signed Addition

• Question: what is the result of the following operation?

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• Question: what is the result of the following operation?

011 +011 ----

<sup>-</sup>If these are treated as signed numbers in two's complement, then we need a leading 0 to indicate that this is a positive number

<sup>-</sup>Truncated to three bits, the result is a negative number!

#### Overflow

• In this situation, overflow occurred: this means that both the operands had the same sign, and the result's sign differed

Possibly a software error

# Overflow vs. Carry

- These are different ideas
  - Carry is relevant to unsigned values
  - Overflow is relevant to signed values

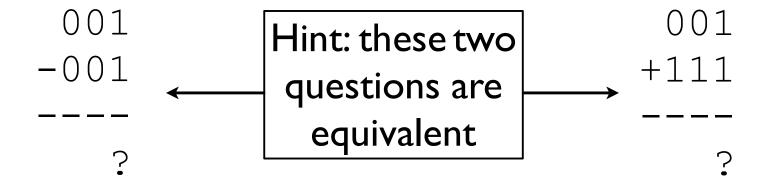
111 +001	011	111	001
	+011	+100	+001
000	110	011	010
No Overflow;	Overflow;	Overflow;	No Overflow;
Carry	No Carry	Carry	No Carry

<sup>-</sup>As to when is it a problem, this all depends on exactly what it is you're doing

# Subtraction

#### Subtraction

- Have been saying to invert bits and add one to second operand
- Could do it this way in hardware, but there is a trick



#### Subtraction Trick

- Assume we can cheaply invert bits, but we want to avoid adding twice (once to add I and once to add the other result)
- How can we do this easily?

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- Assume we can cheaply invert bits, but we want to avoid adding twice (once to add I and once to add the other result)
- How can we do this easily?
  - Set the initial carry to 1 instead of 0

0101

-0011

\_\_\_\_

0101 -0011 Invert 0011

```
0101 Invert 0011 ______ 1100
```

