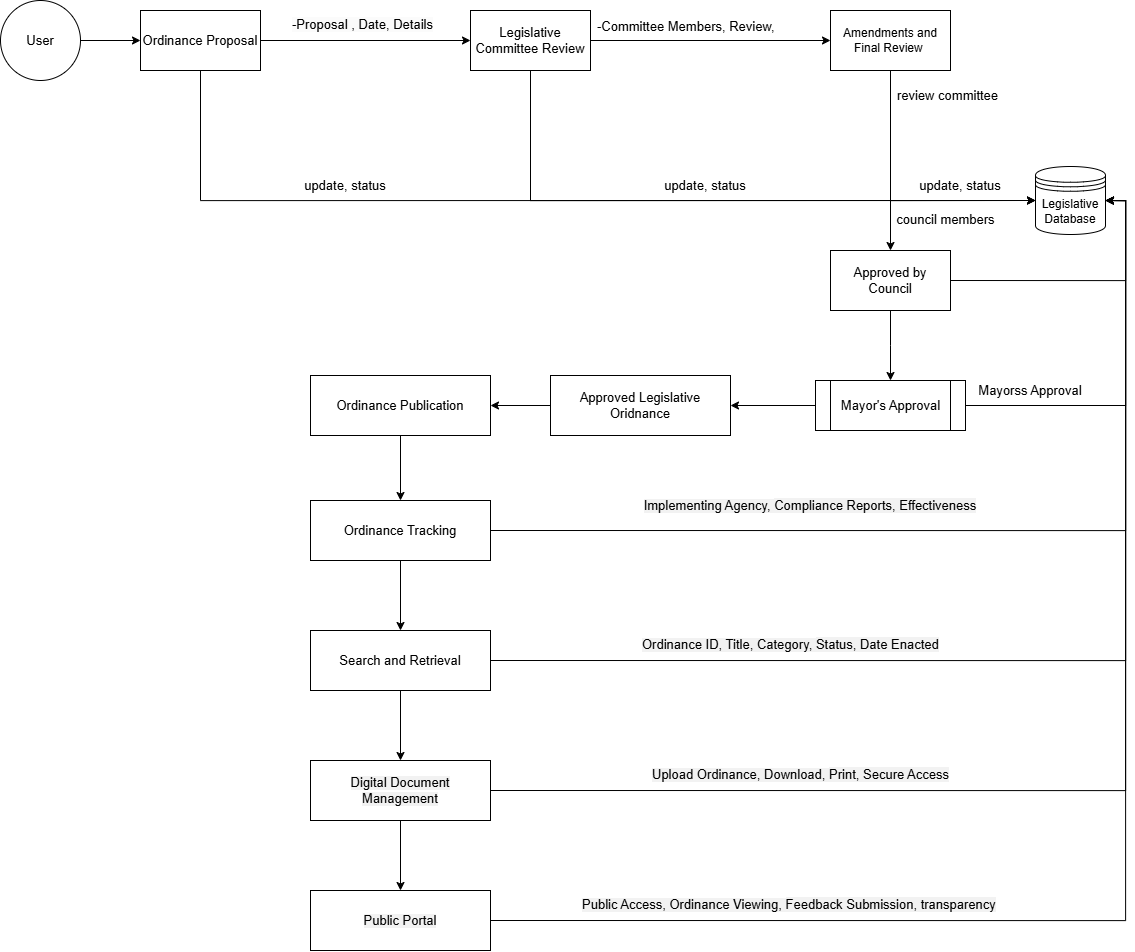
**LOGIC DESIGN**