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| Biswajit | • Mono<br>  ○ Implemented Resampling, downsampling<br>  ○ mono debugging<br>  ○ Helped verifying functions<br>  ○ setting up the modes<br>• Stereo<br>  ○ Helped implement pll and debugging it.<br>  ○ Implemented delay/all pass filter<br>  ○ setting up the modes<br>  ○ Helped verifying functions<br>• RDS<br>  ○ Setting up the modes<br>  ○ Rational resampler<br>  ○ Helped with Manchester decoding and pll<br>  ○ Helped verifying functions |
| Maham | • Mono<br>  ○ reformatted python code to cpp for RF front end<br>  ○ Wrote the base code in project.cpp (mono function and parameters in main function)<br>  ○ Researched the theory to help implementation of code and debugging<br>• Stereo<br>  ○ Researched the theory to help implementation of code and debugging<br>• RDS<br>  ○ wrote code for RDS Channel Extraction, carrier recovery and a bit of demodulation<br>  ○ Helped jibin debug segmentation error (arguments for RDS pll were incorrect since there were 2 float vectors, it did not throw compilation error but since the wrong vector was being called in the wrong place the sizes did not match up)<br>  ○ Researched on RCC, CDR Manchester encoding and decoding to help teammates attempt implementation |
| Jibin | • Mono:<br>  ○ Refactoring lab 3 to C++ from python for front end<br>  ○ Implemented demod, downsampler and lpf<br>  ○ setting up the modes<br>  ○ debugging and testing<br>• Stereo:<br>  ○ Implemented the stereo carrier recovery and stereo channel extraction( bandpass filters and stereo pll), mixer<br>  ○ setting up the modes<br>  ○ Debugging and testing |

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| | • Threading for front end, mono, stereo, rds<br>• RDS:<br>  ○ setting up the modes<br>  ○ Allpass and bandpass<br>  ○ Implemented squaring nonlinearity<br>  ○ Implemented Manchester and differential Decoding<br>  ○ Debugging and testing |
| Sai | • Mono<br>  ○ Base Code Implementation (Writing base code, pseudo code, converting theory into code)<br>  ○ Working with block size implementation (had to adjust the errors/noise).<br>  ○ Helped verifying functions<br>• Stereo<br>  ○ Base Code Implementation (Writing base code, pseudo code, converting theory into code)<br>  ○ PLL code implementation/theory conversion<br>  ○ Helped verifying functions<br>  ○<br>• RDS<br>  ○ Base Code Implementation (Writing base code, pseudo code, converting theory into code)<br>  ○ PLL and RRC<br>  ○ Helped verifying functions<br>  ○<br>• Threading<br>  ○ Base Code Implementation (Writing base code, pseudo code, converting theory into code)<br>  ○ Helped verifying functions |