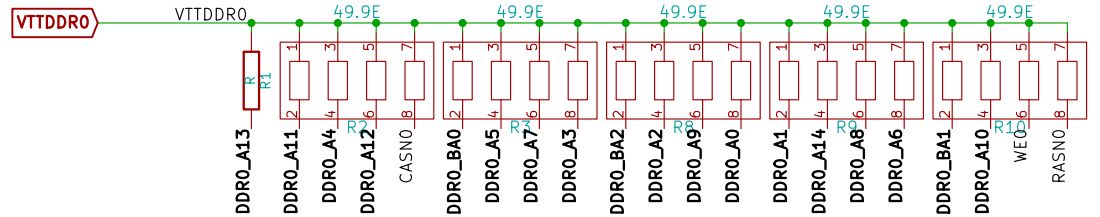
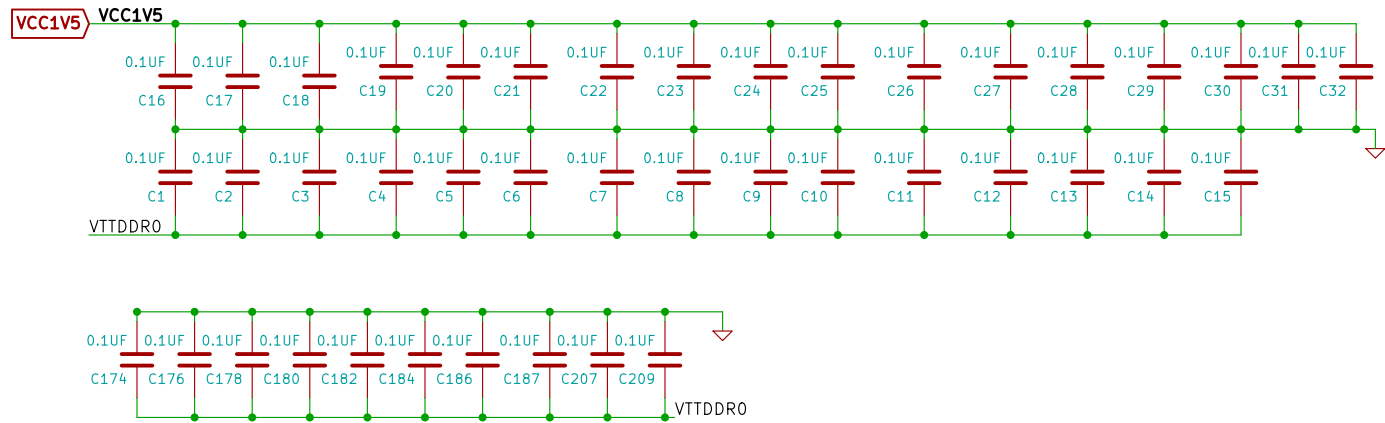
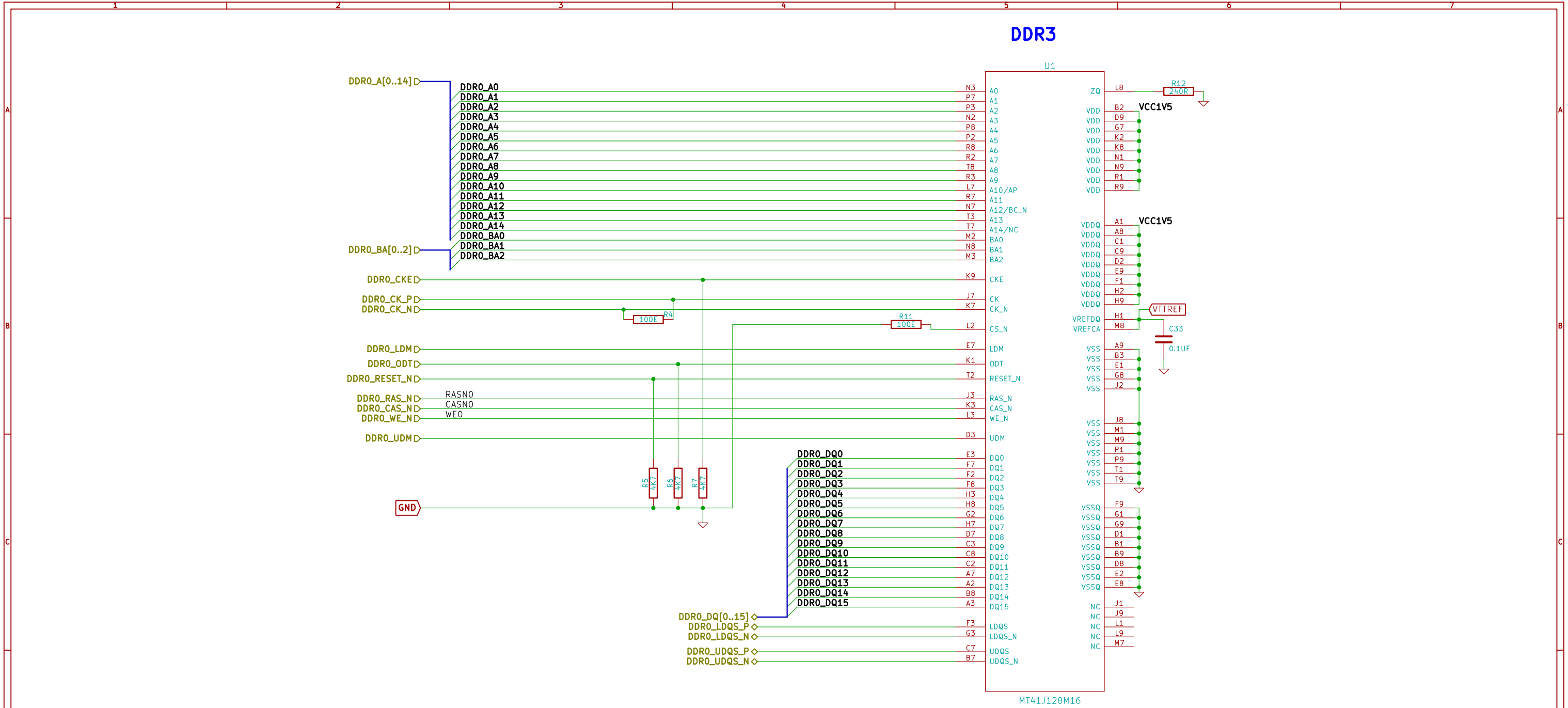


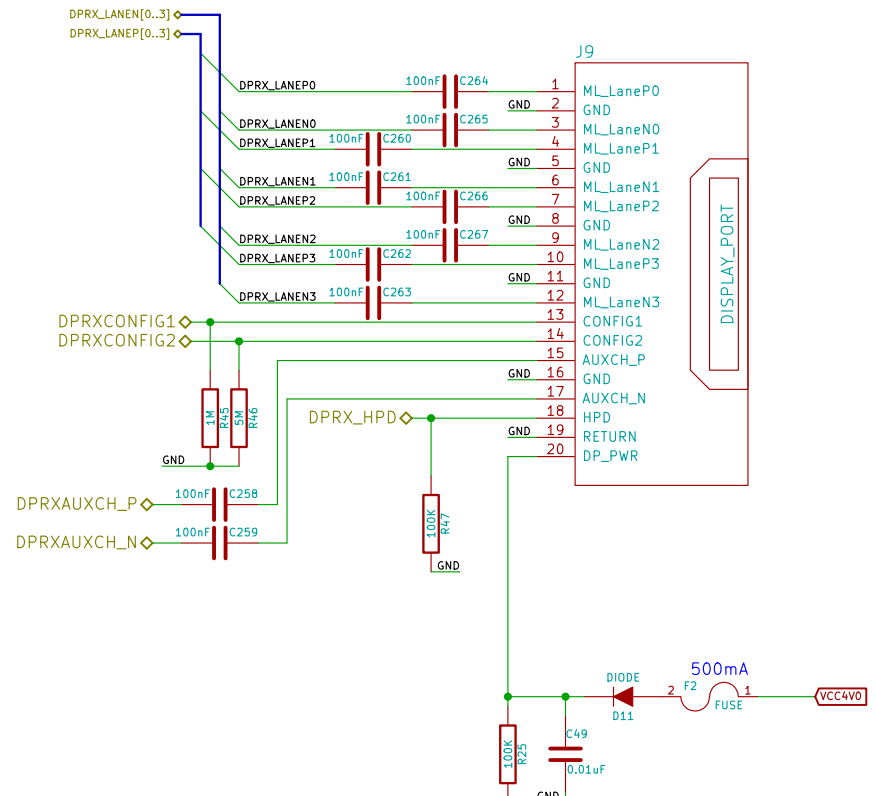
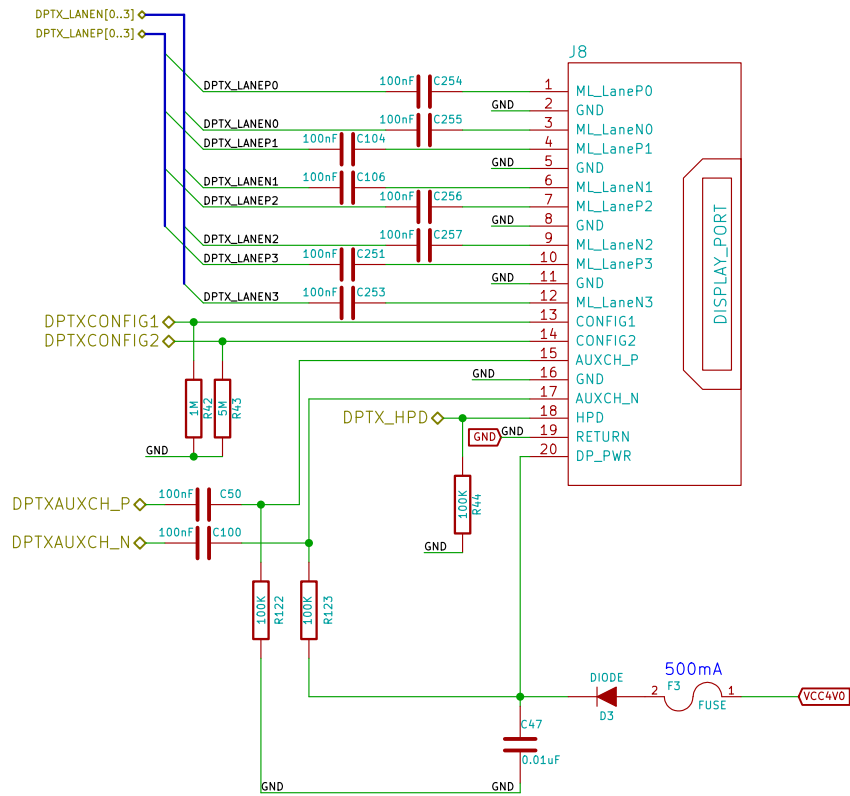
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Title: HDMI2USB		
Size: A4	Date: 15 oct 2015	Rev: Rev2
KiCad E.D.A.		Id: 4/12

DDR3

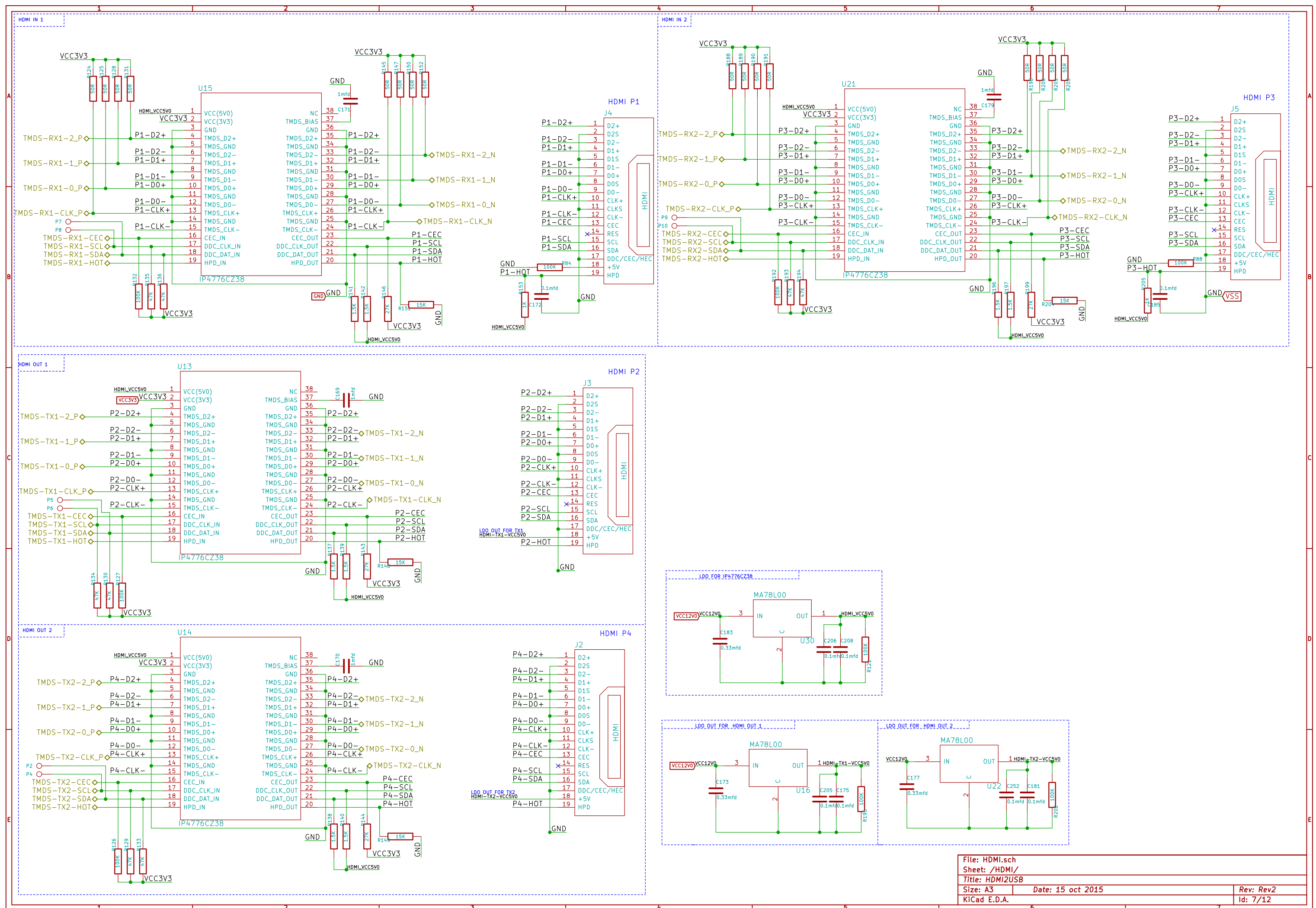


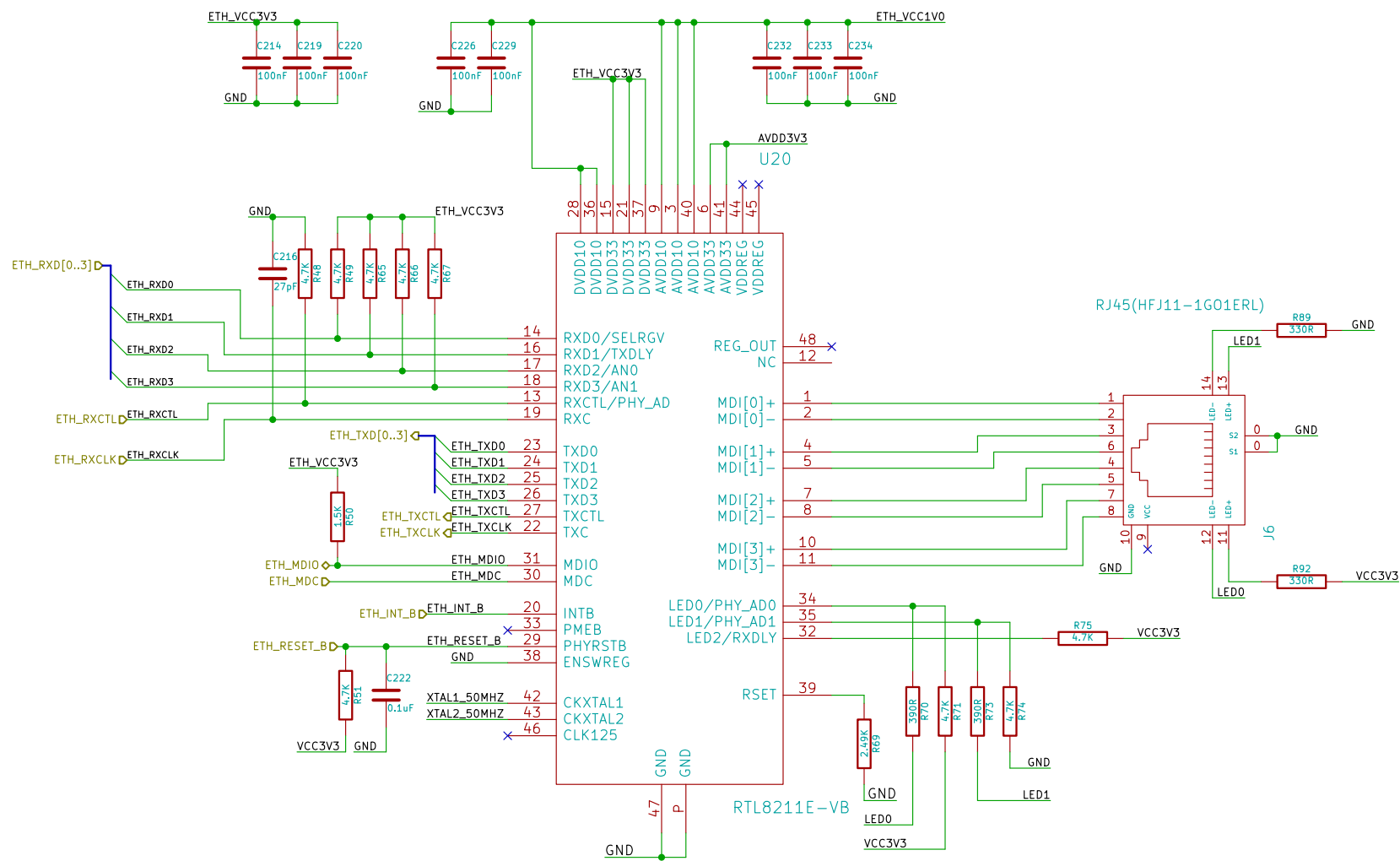
DISPLAY PORT TX

DISPLAY PORT RX

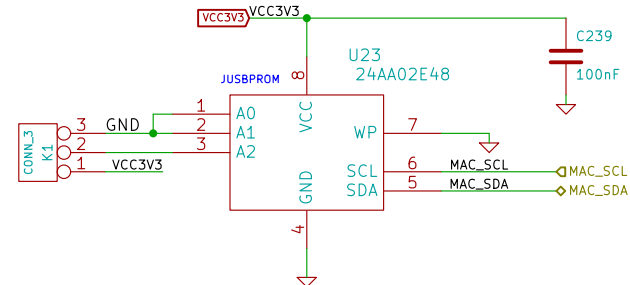
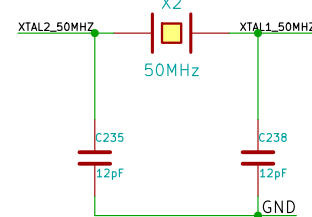
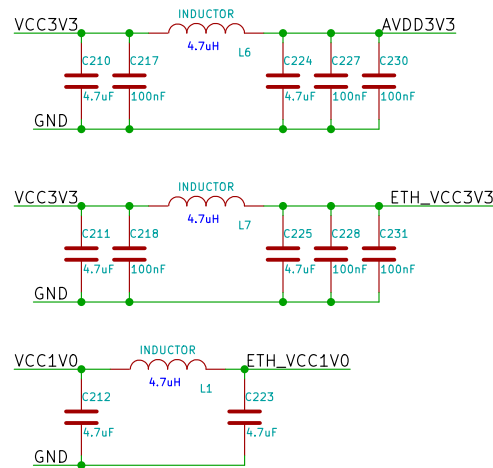
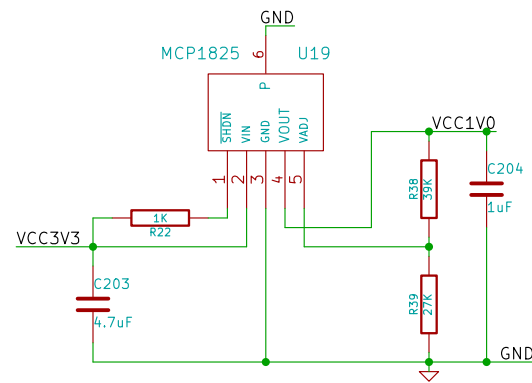


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KiCad E.D.A.		Id: 6/12

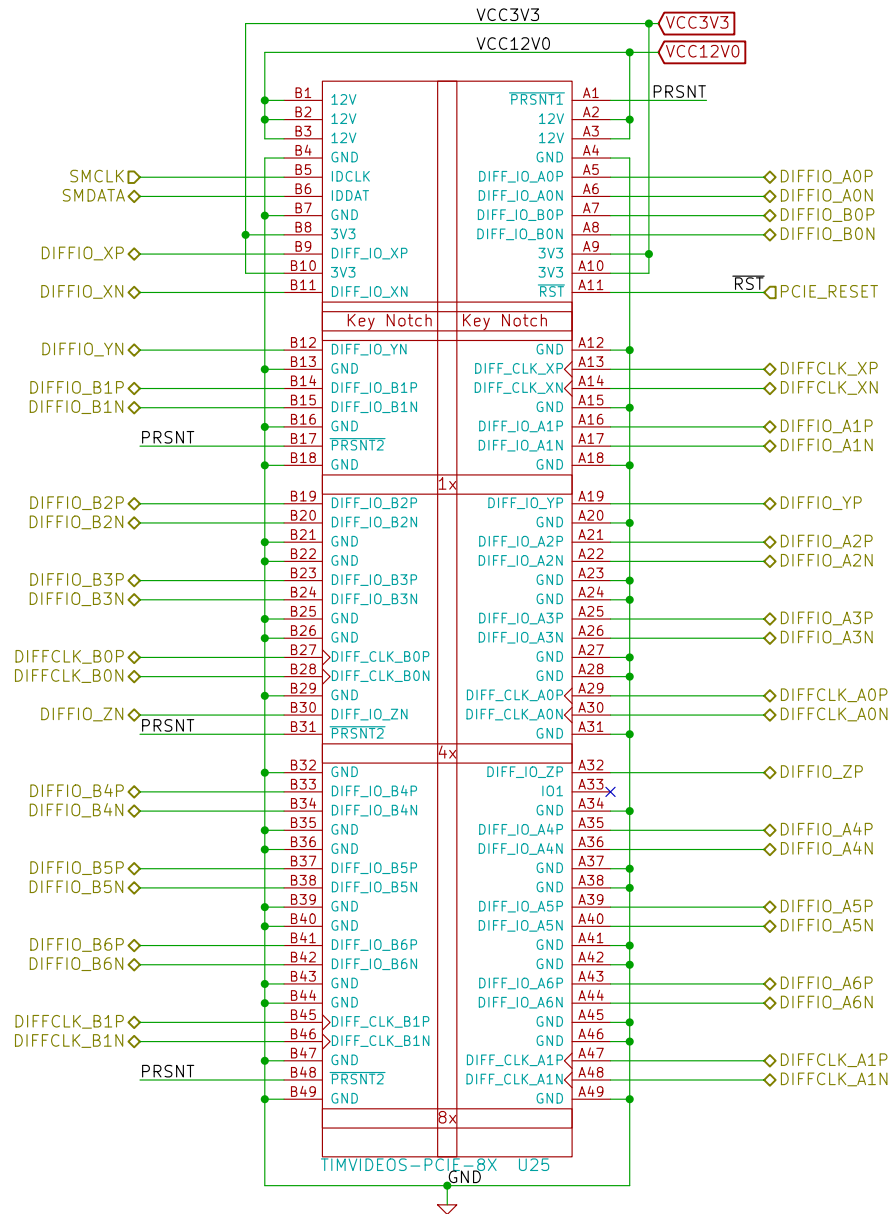




VCC1V0

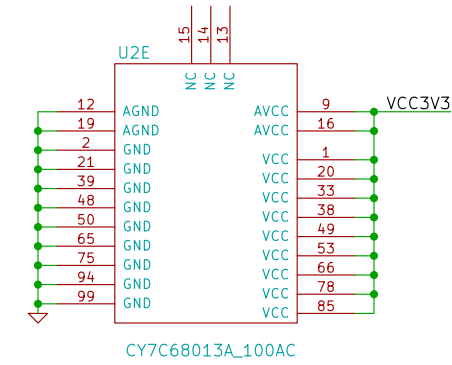
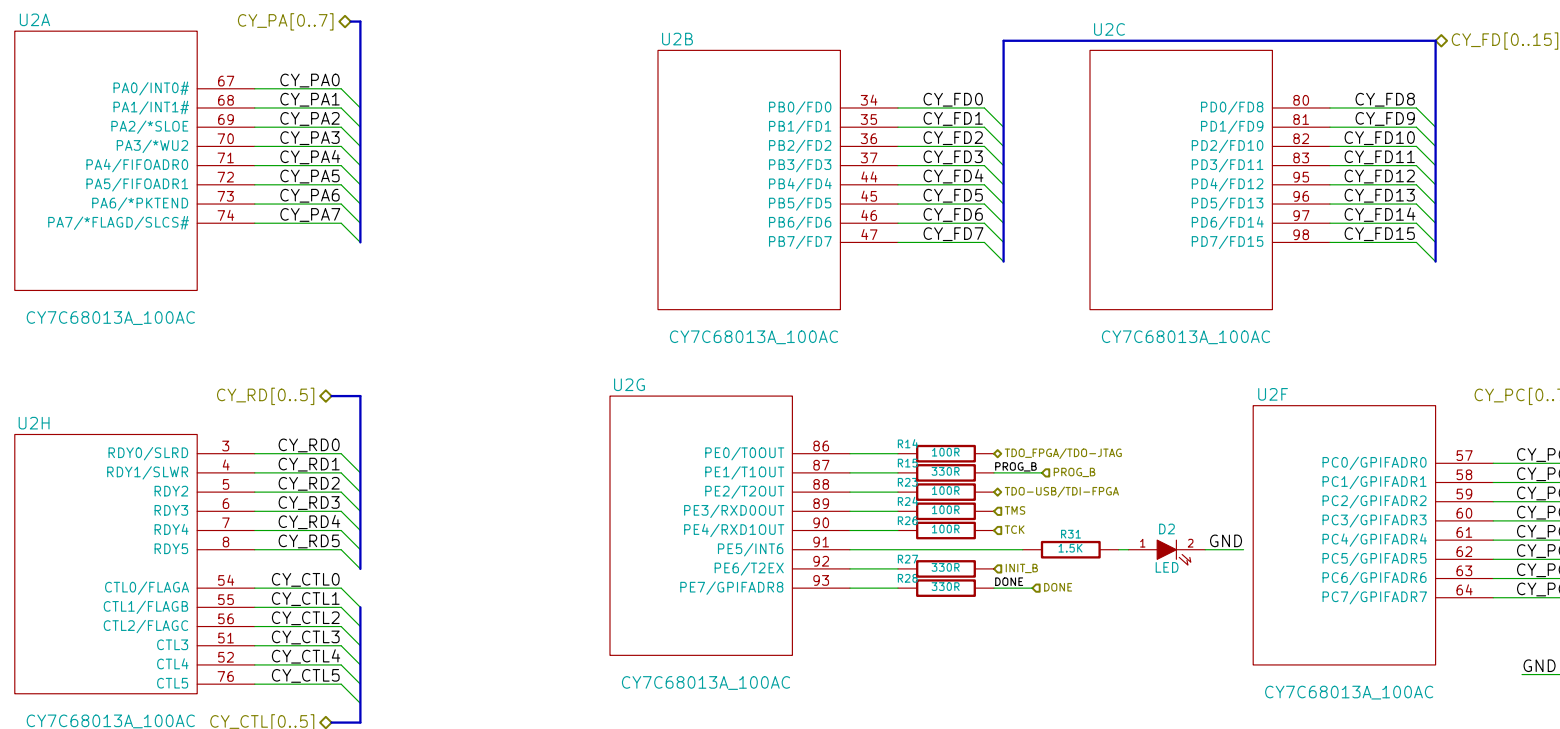


PCIe

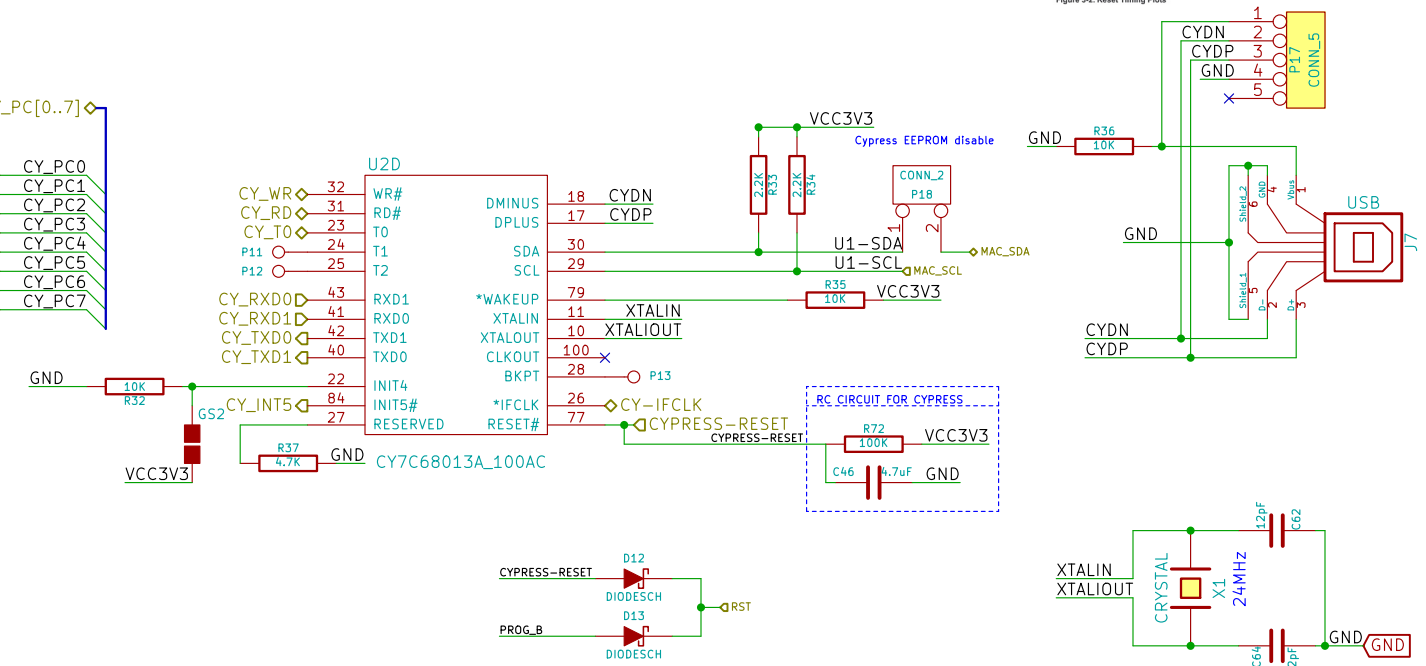
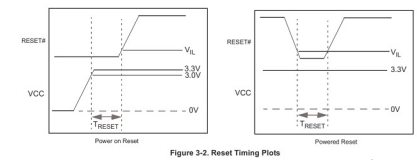


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KiCad E.D.A.		Id: 9/12

USB 2.0 Device Port JTAG Programmer and "Easy" High Speed USB 2.0 Interface Cypress FX2 based

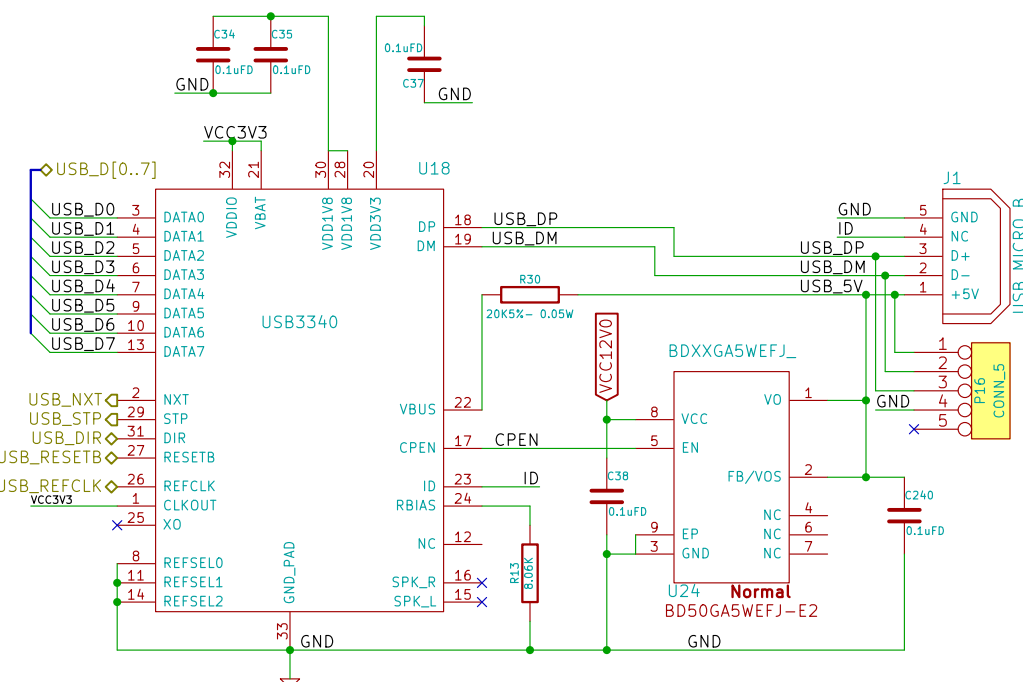


RESET TIMING VALUES	
Condition	TRESET
Power-on Reset with crystal	5 ms
Power-on Reset with external clock	200 μ s + Clock stability time
Powered Reset	200 μ s

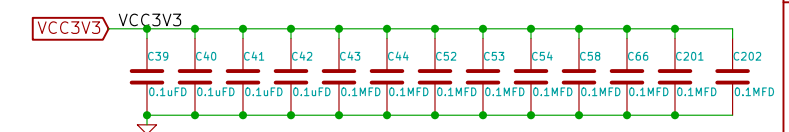


USB 2.0 OTG Port (Host + Device Support)
UTMI+/ULPI IC – Require full FPGA USB2.0 stack
Microchip USB3440
FOSS Cores:

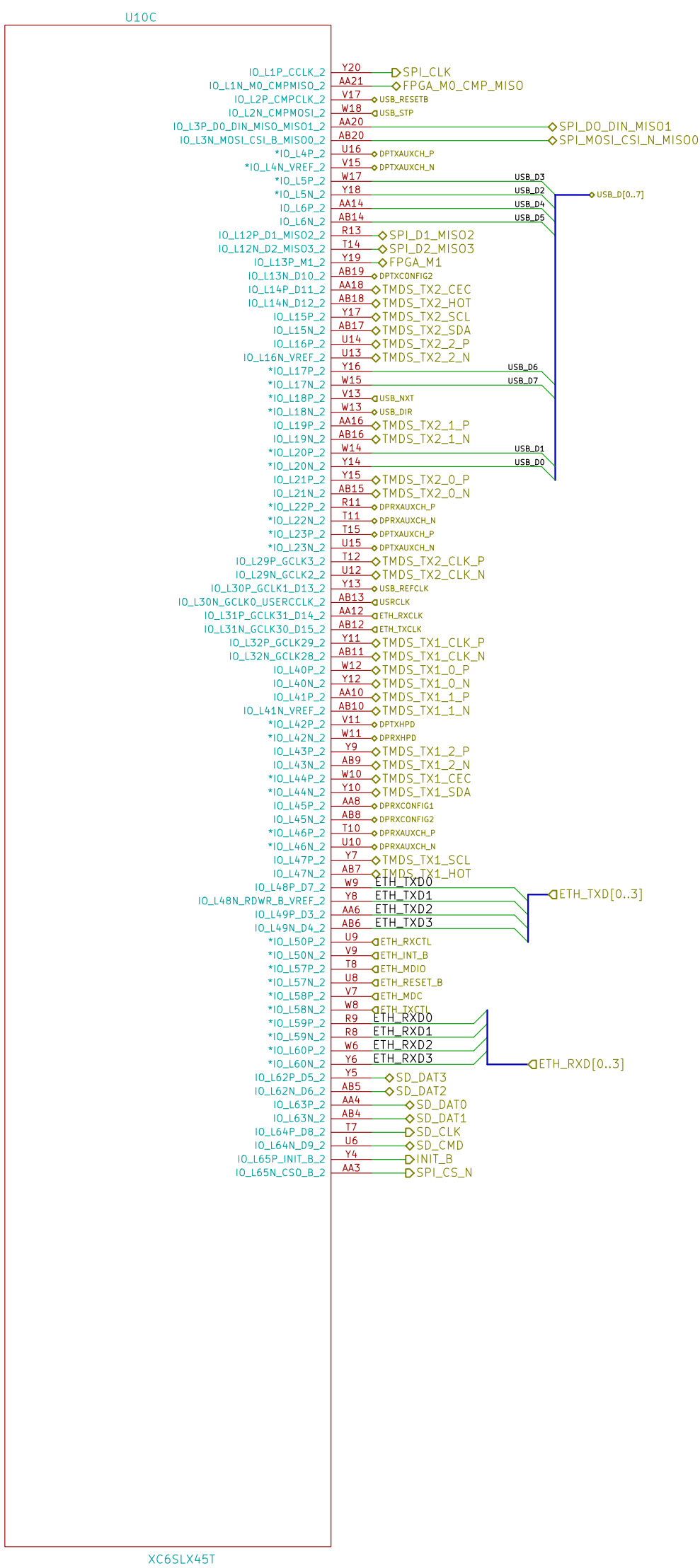
- * Daisho – <http://goo.gl/eSwTeb>
- * joris_vr – <http://jorisvr.nl/usb/>



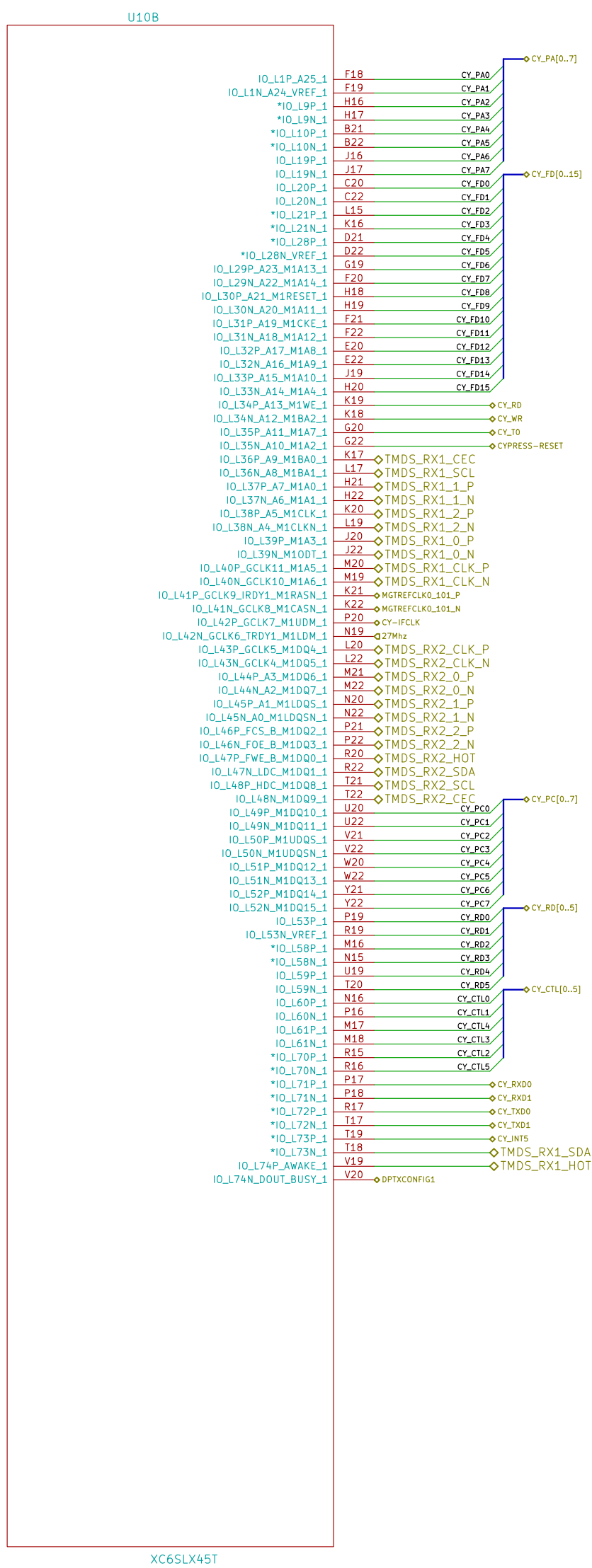
Cypress CY7C68013A Decoupling Capacitors



BANK 2



BANK 1



BANK 3

BANK 0

