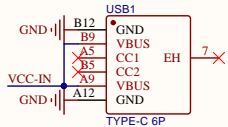
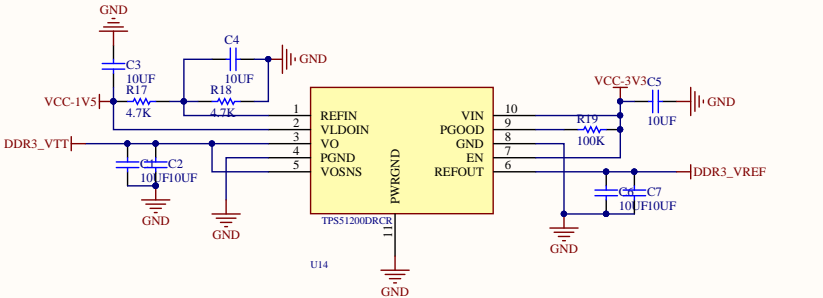
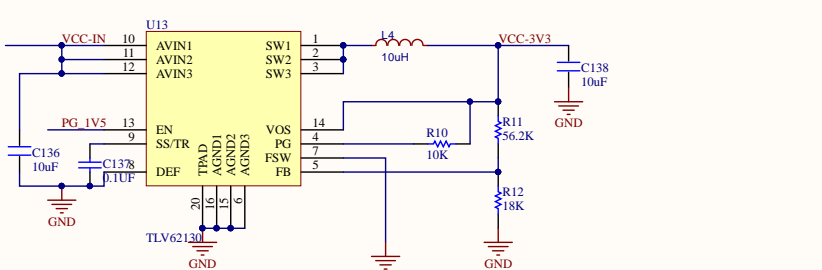
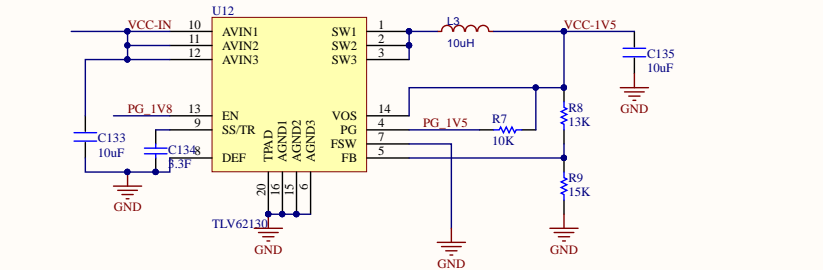
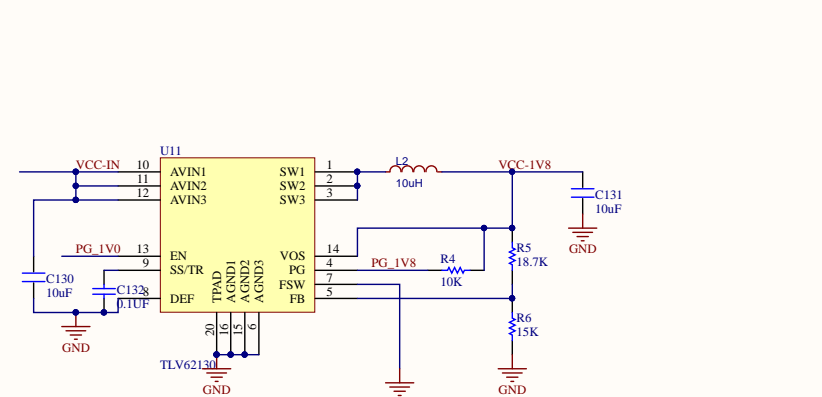
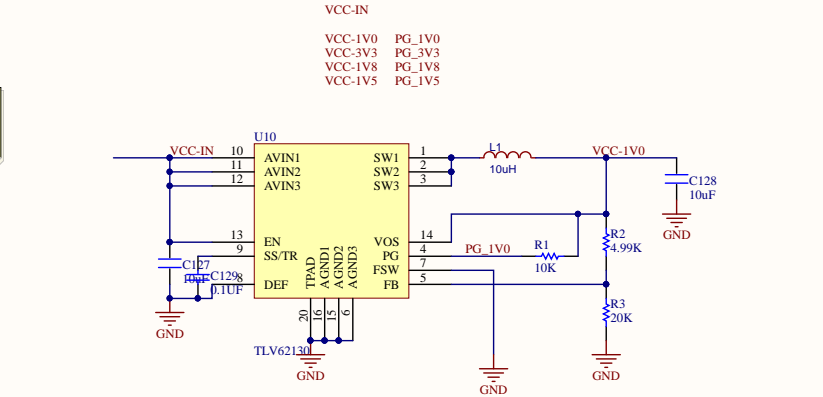


1、	1V8	1.5A
2、	1V5	1.5A
3、	1V0	4A

power sequence		
1.0V	-> 1.8V	->1.5V
		3.3V

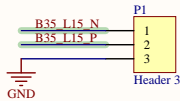


Title		
Size	Number	Revision
A3		
Date:	2020/7/26	Sheet of
File:	E:\个人资料\sch1.02\sch\SYS-PS_SchDoc	Drawn By:

A	1	2	3	4	5	6	7	8
B								
C								
D								

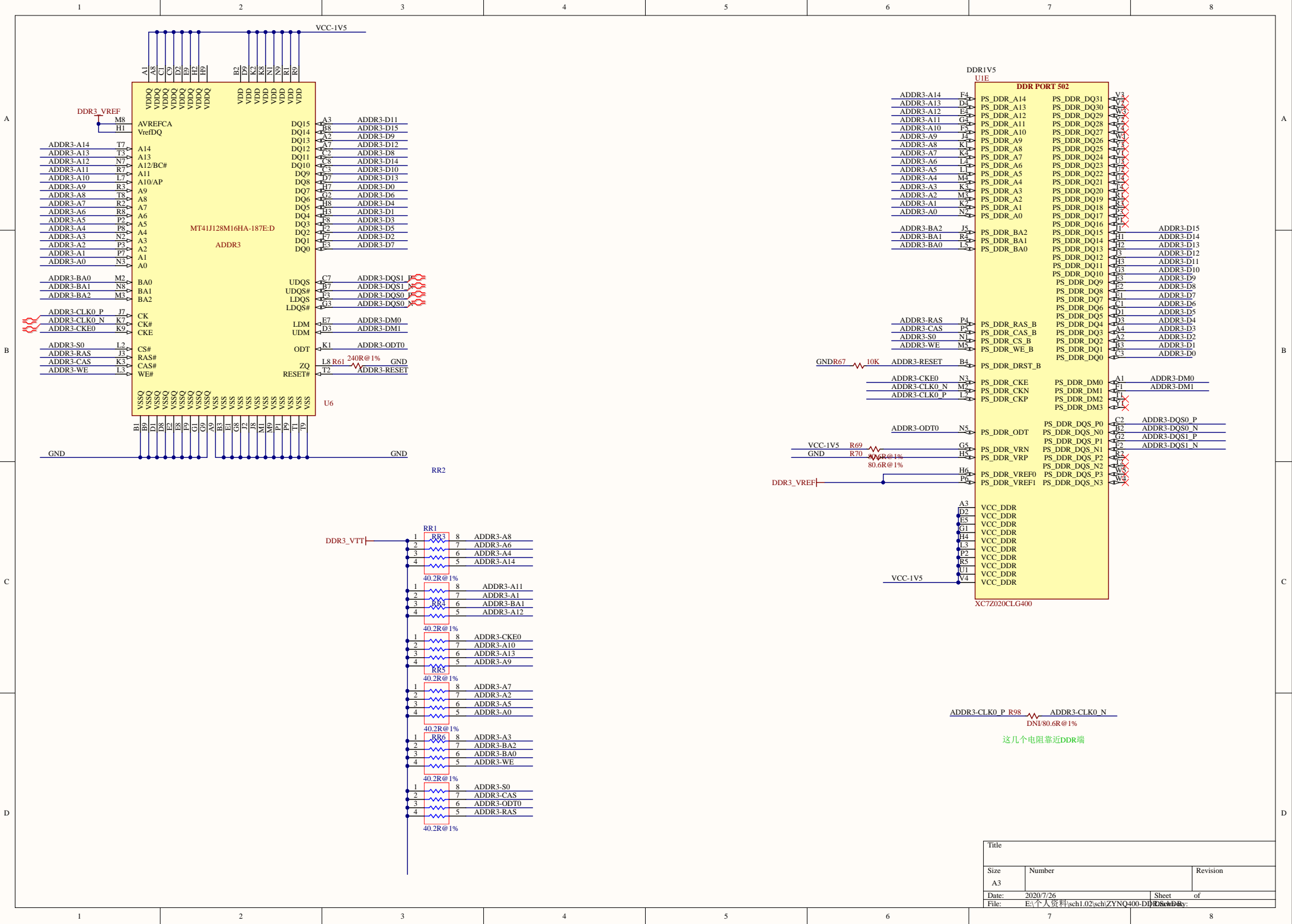
VCC-IN	J1 2	J1 1	VCC-IN
VCC-IN	J1 4	J1 3	VCC-IN
VCC-IN	J1 6	J1 5	VCC-IN
B35 L16 N	J1 8	J1 7	B35 L16 P
B35 L18 N	J1 10	J1 9	B35 L18 N
B35 L14 N	J1 12	J1 11	B35 L14 P
B35 L17 N	J1 14	J1 13	B35 L17 P
B35 L10 N	J1 16	J1 15	B35 L10 P
B35 L12 N	J1 18	J1 17	B35 L12 N
B35 L9 P	J1 20	J1 19	B35 L9 N
B35 L24 N	J1 22	J1 21	B35 L24 N
B35 L8 P	J1 24	J1 23	B35 L8 N
B35 L7 N	J1 26	J1 25	B35 L7 P
B34 L23 N	J1 28	J1 27	B34 L23 N
B34 L14 N	J1 30	J1 29	B34 L14 N
B34 L13 N	J1 32	J1 31	B34 L13 N
B34 L19 N	J1 34	J1 33	B34 L19 P
B34 L12 N	J1 36	J1 35	
B34 L9 N	J1 38	J1 37	B34 L21 N
B34 L21 N	J1 40	J1 39	B34 L9 P
B34 L18 N	J1 42	J1 41	B34 L11 N
B34 L10 N	J1 44	J1 43	B34 L11 P
B34 L5 P	J1 46	J1 45	B34 L3 P
B34 L2 P	J1 48	J1 47	B34 L2 N
B34 L16 N	J1 50	J1 49	B34 L4 P
B34 L16 N	J1 52	J1 51	B34 L4 N
B34 L15 N	J1 54	J1 53	B34 L3 N
B34 L15 N	J1 56	J1 55	B34 L8 N
B34 L22 N	J1 58	J1 57	B34 L8 P
B34 L17 N	J1 60	J1 59	B34 L10 N
B34 L22 N	J1 62	J1 61	B34 L7 P
B34 L17 N	J1 64	J1 63	B34 L18 N
B34 L7 N	J1 66	J1 65	
GND	J1 68	J1 67	GND
GND	J1 70	J1 69	GND
GND	J1 72	J1 71	GND
GND	J1 74	J1 73	GND
VCC-3V3	J1 76	J1 75	VCC-3V3
VCC-3V3	J1 78	J1 77	VCC-3V3
VCC-3V3	J1 80	J1 79	VCC-3V3

ZYNQ\_CORE

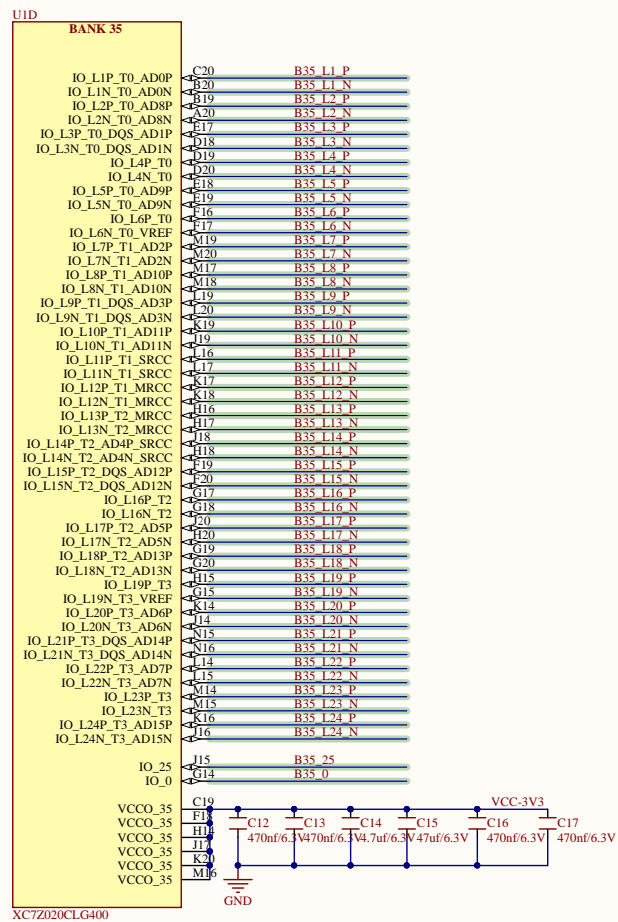
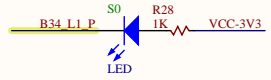
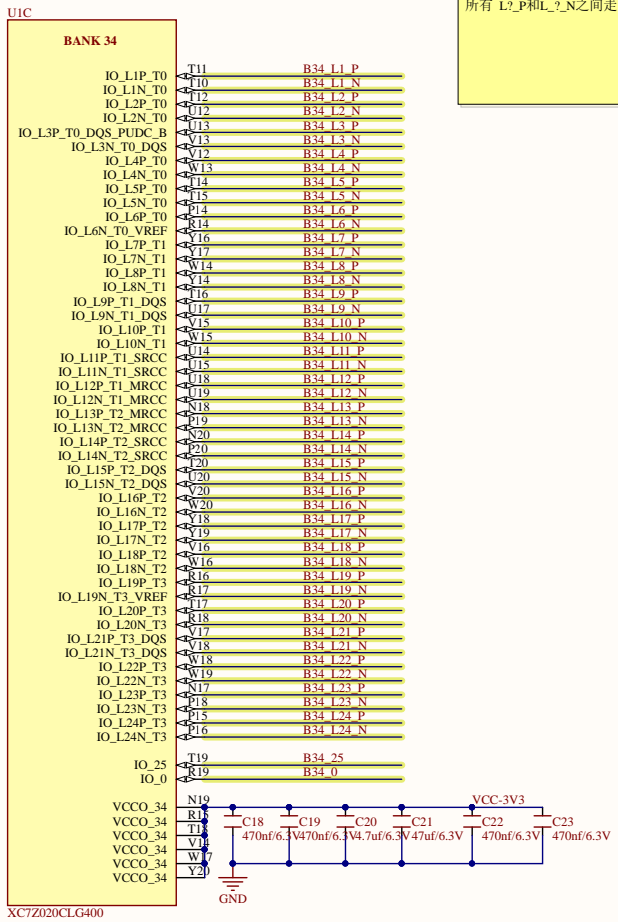
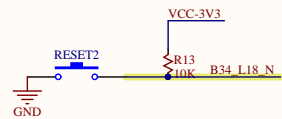
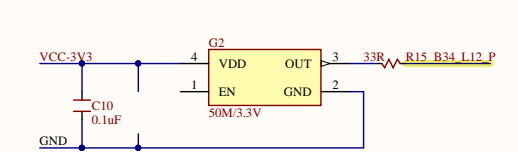


GND	J2 2	J2 1	GND
GND	J2 4	J2 3	GND
GND	J2 6	J2 5	GND
ASPL-SCK/V500	J2 8	J2 7	V501
GND	J2 10	J2 9	GND
ASPL-DQ3/MODE1	J2 12	J2 11	ASPL-DQ1/MODE1
GND	J2 14	J2 13	GND
ASPL-CS	J2 16	J2 15	ASPL-DQ2/MODE2
GND	J2 18	J2 17	GND
V500	J2 20	J2 19	ASPL-DQ0/MODE0
MIO14	J2 22	J2 21	MIO9
AJTAG TMS	J2 24	J2 23	AJTAG TDO
GND	J2 26	J2 25	GND
AJTAG TCK	J2 28	J2 27	AJTAG TDI
MIO51	J2 30	J2 29	MIO13
MIO12	J2 32	J2 31	MIO15
MIO19	J2 34	J2 33	MIO10
MIO37	J2 36	J2 35	MMC-D1
MIO53	J2 38	J2 37	MIO52
MIO49	J2 40	J2 39	MIO23
MIO35	J2 42	J2 41	MMC-D0
MIO38	J2 44	J2 43	MIO29
MMC-D2	J2 46	J2 45	MMC-SCLK
MIO33	J2 48	J2 47	MIO30
MIO28	J2 50	J2 49	MIO46
MIO31	J2 52	J2 51	MMC-CMD
MIO36	J2 54	J2 53	MIO39
MIO48	J2 56	J2 55	MIO34
MIO32	J2 58	J2 57	
MIO26	J2 60	J2 59	MIO47
MIO24	J2 62	J2 61	MMC-D3
MIO22	J2 64	J2 63	MIO20
MIO16	J2 66	J2 65	MIO18
B35 L2 N	J2 68	J2 67	B35 L2 P
B35 L1 N	J2 70	J2 69	B35 L1 P
B35 L3 N	J2 72	J2 71	B35 L3 P
B35 L4 N	J2 74	J2 73	B35 L4 P
B35 L5 N	J2 76	J2 75	B35 L5 P
B35 L6 N	J2 78	J2 77	B35 L6 P
B35 L15 N	J2 80	J2 79	B35 L15 P

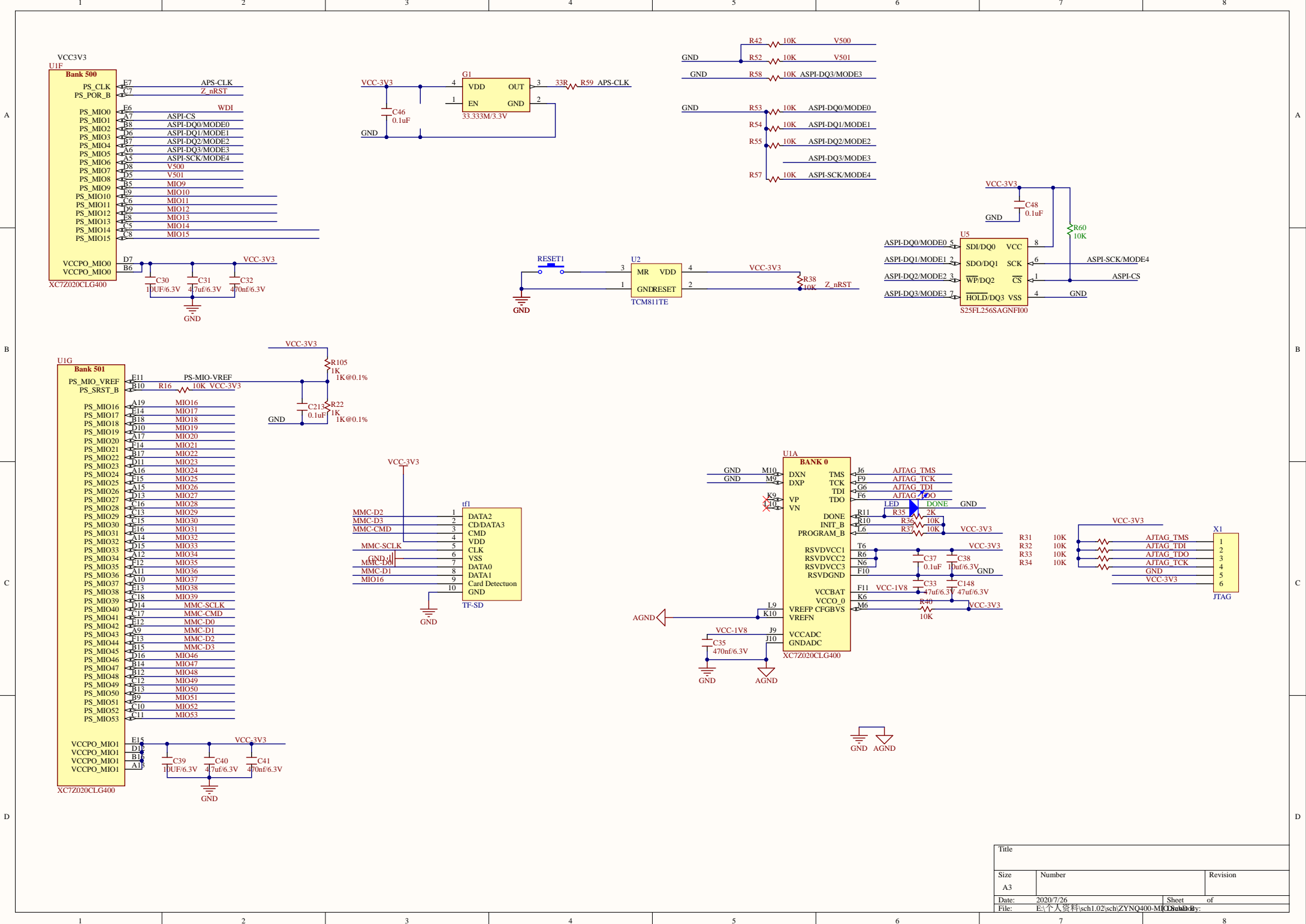
Title		
Size	Number	Revision
A3		
Date:	2020/7/26	Sheet of
File:	E:\个人资料\ZYNQ400-CONDIP.Sch	Down By:



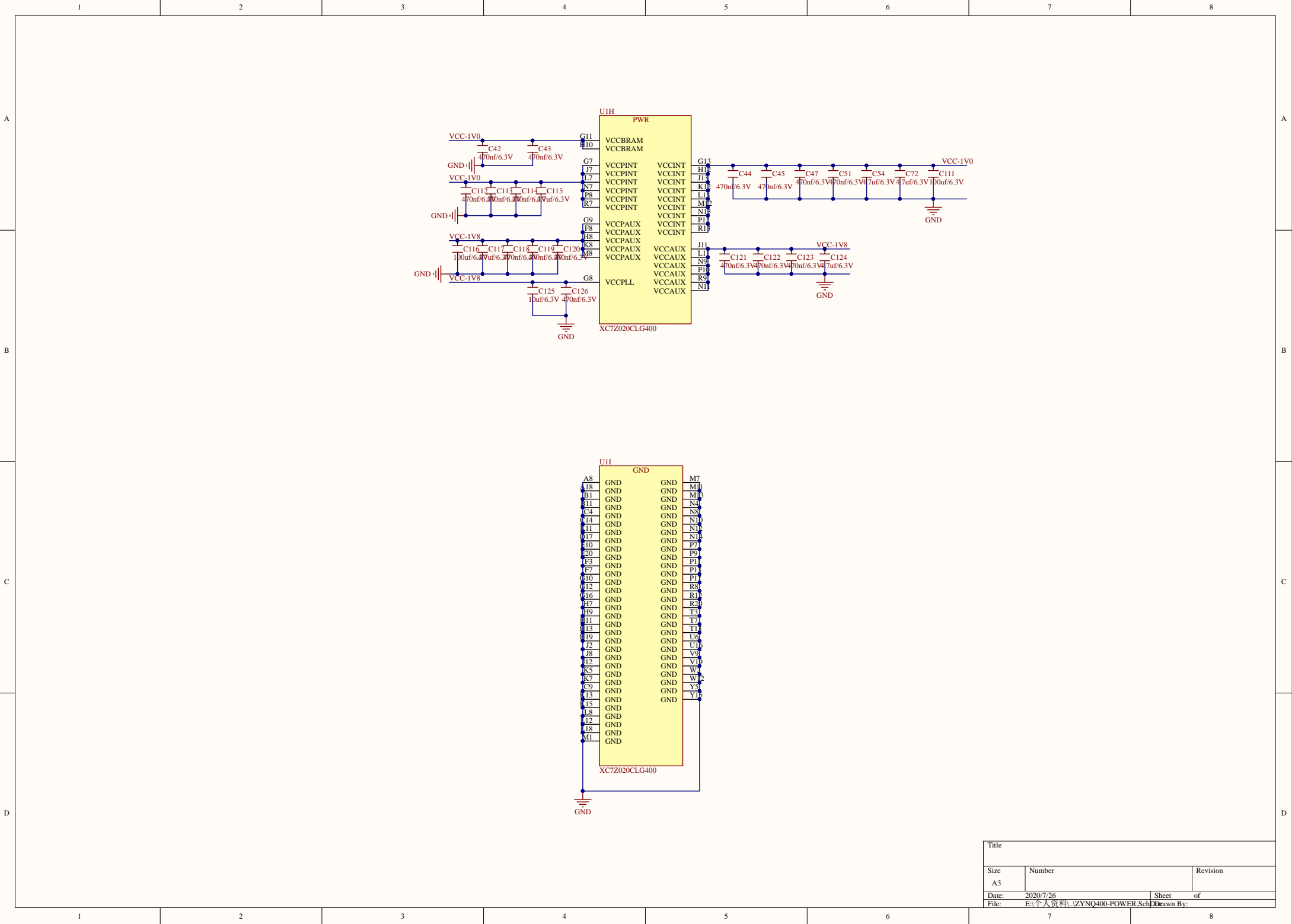
Title		
Size	Number	Revision
A3		
Date:	2020/7/26	Sheet of
File:	E:\个人资料\sch1.02\sch\ZYNQ400-DDR3-DDIO.sch	DR3-DDIO



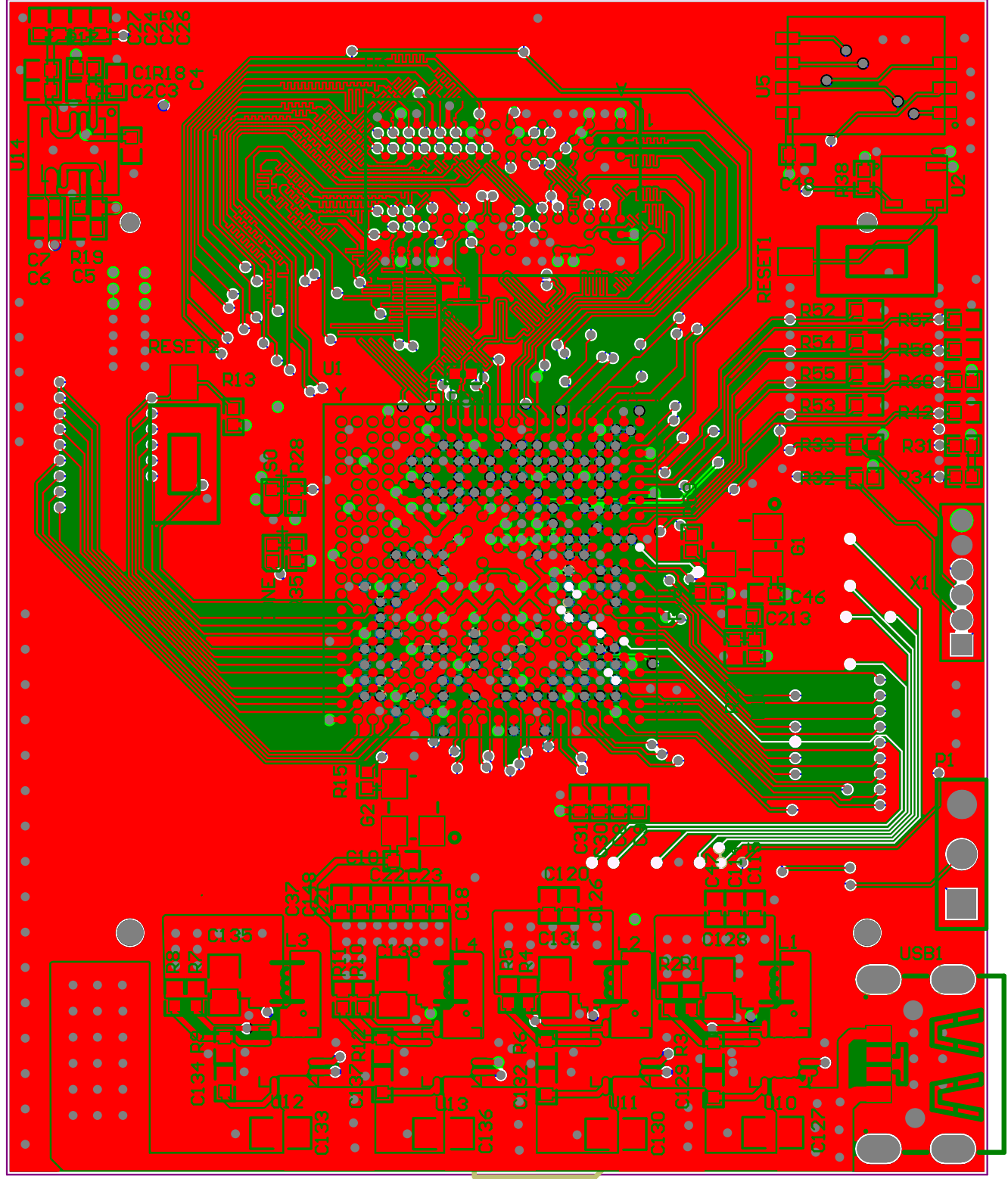
Title		
Size A3	Number	Revision
Date: 2020/7/26	Sheet of	
File: E:\个人资料\sch1.02\sch\ZYNQ400-EMIO	<del>Docu</del>	



Title		
Size	Number	Revision
A3		
Date:	2020/7/26	Sheet of
File:	E:\个人资料\sch1.02\sch\ZYNQ400-M0100.sch	Drawn by:



Title		
Size	Number	Revision
A3		
Date:	2020/7/26	Sheet of
File:	E:\个人资料\ZYNO400-POWER.Sch	Drawn By:



# BOARD STACK REPORT