

Simulator::WritePort< I >

```
classDiagram
    class SimulatorWritePort["Simulator::WritePort< I >"]
    class SimulatorArbitratedWritePort["Simulator::ArbitratedWritePort< I >"]
    class SimulatorDedicatedWritePort["Simulator::DedicatedWritePort< I >"]
    SimulatorArbitratedWritePort --|> SimulatorWritePort
    SimulatorDedicatedWritePort --|> SimulatorWritePort
```

Simulator::ArbitratedWritePort< I >

Simulator::DedicatedWritePort< I >