RESUME

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PROFESSIONAL SUMMARY

- **2+ Years** of Experience in **Post Silicon Functional Validation of SoC and its Sub-System IPs.**
- Worked with **VOTARY SOFTECH SOUTIONS PVT LTD**, in Hyderabad as a Software Engineer from JUNE 2022 -MARCH 2023.
- Worked with **ANVHAYA TECHNICAL SOLUTIONS PVT LTD**, in **Bengaluru** as a Validation Engineer from DEC 2020 -JUNE 2022.

WORK EXPERIENCE HIGHLIGHTS

- Work Experience on **Post Silicon Validation** of various subsystems **IP'S** like **I2C, SPI** and **CAN 2.0**
- Work Experience on **CPU System Software Engineer**.
- Tuning Delta from Design Documentation into Validation **Test Plan** and Executing the test as per test plan.
- Experience on Programming Languages like **C**, **Embedded C**, **Assembly Language**.
- Experience on 32-bit and 64-bit Microcontrollers and microprocessors ARM based SOCs.
- Good understanding of **Hardware Schematics**.
- Effectively communicated with **Design Engineers**, **Micro Architects** and **Team Members** to ensure High Quality Test Plans.
- Development of functional coverage and achieving coverage goals - Identifying and driving to closure of bugs.
- Experience in usage of Debugging tools like **JTAG, GDB debugger**.
- Experience in usage of Bug tracking and reporting tool like **JIRA**.
- Extensively used version controller tools: **GIT**.
- Utilizing various Software and Hardware tools like **Oscilloscope, DMM, Logic Analyzer** and **Protocol Analyzer** to ensure high quality results.
- Experience in ARM based SOC Post-Silicon IP Validation related to debug by Trace32 environment.
- Exposure to **Windows** and **Linux Operating systems.**
- Experience in usage of **IDE**s like **Keil Micro Vision** till v5.O and **Notepad++.**

TECHNICAL SKILLS

- Programming Languages : C, Embedded C
- Operating systems : Windows, Linux
- Tools / IDES : GCC, Micro Vision Keil5, GDB, Notepad++.
- HW Tools : JTAG Debugger, DSO, DMM, CANoe
- Protocols : I2C, SPI, CAN 2.0

Educational Details:

> Bachelor of Engineering (B.E) in Computer Science and Engineering from Sri Krishna Institute of Technology, Bangalore - Visveswaraya Technical University -Belagavi, from 2017 to 2021.

Project Experience:

Project #03: **CPU System Software Engineer (**client Qualcomm**)**
Roles & Responsibilities:

> Involved in modifying CPU centric system software for kernel, bootloader and power management software for pre-silicon platform.
> Exposure and good understanding of ARM CPU Architecture.
> Responsible for software prototypes of new ARM features.
>> **Features validated:**
>>> Configmon
>>> LPM

Project #02: Post Silicon Validation of **CAN 2.0**
Roles & Responsibilities:

> Understanding the CAN 2.0 specifications documents.
> Owning, reviewing, and altering the features that are to be validated.
> Created test plan for validating various features of CAN 2.0 that are to be validated.
> Executed test cases.
>> **Features validated:**
>>> CAN Internal Loop back validation with all supported baud rates.
>>> CAN Interrupts validation.
>>> CAN Tx and Rx data validation with CANoe vector hardware tool.
>>> CAN External loop back validation.

Project #01: Post Silicon Validation of I**2C** and **SPI**
Roles and Responsibilities:

> Understanding the I2C & SPI specifications documents.
> Owning, reviewing, and altering the features that are to be validated.
> Created test plan for validating various features of I2C & SPI that are to be validated.
> Executed test cases.
>> **Features validated of I2C:**
>>> IP in different speed Modes.
>>> START, STOP, Repeated START.
>>> ACK, NACK condition generation.
>>> WRITE and READ operations.
>>> Clock Stretching.
>> **Features validated of SPI:**
>>> Clock Polarity and Clock Phase.
>>> SPI Operating Modes.