QuickUSB Adapter Board Pinout
Rev 1.30
Date 4/7/2003

Bitwise SYSTEMS
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Connector J2

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	GND	N/A	Ground	2	+5V	N/A	Unregulated +5V from the USB bus (300mA total)
3	PA0	I/O	Port A, Bit 0	4	RESET_B	OD	FX2 reset, Active low.
5	PA1	I/O	Port A, Bit 1	6	CLKOUT	Output	48MHz CPU clock
7	PA2	I/O	Port A, Bit 2	8	IFCLK	Output	48MHz GPIO clock
9	PA3	I/O	Port A, Bit 3	10	INT4	Input	8051 INT4 IRQ. Active high, edge sensitive
11	PA4	I/O	Port A, Bit 4	12	RXD_0	Input	Serial Port 0 RS-232 RxD
13	PA5	I/O	Port A, Bit 5	14	TXD_0	Output	Serial Port 0 RS-232 TxD
15	PA6	I/O	Port A, Bit 6	16	TXD_1	Output	Serial Port 1 RS-232 TxD
17	PA7	I/O	Port A, Bit 7	18	RXD_1	Input	Serial Port 1 RS-232 RxD
19	GND	N/A	Ground	20	+5V	N/A	Unregulated +5V from the USB bus (300mA total)
21	PB0	I/O	Port B, Bit 0 / FD0	22	CTL0	Output	GPIF ctl out 0 / CMD_DATA
23	PB1	I/O	Port B, Bit 1 / FD1	24	CTL1	Output	GPIF ctl out 1 / REN
25	PB2	I/O	Port B, Bit 2 / FD2	26	CTL2	Output	GPIF ctl out 2 / WEN
27	PB3	I/O	Port B, Bit 3 / FD3	28	CTL3	Output	GPIF ctl out 3 / nREN
29	PB4	I/O	Port B, Bit 4 / FD4	30	CTL4	Output	GPIF ctl out 4 / nWEN
31	PB5	I/O	Port B, Bit 5 / FD5	32	CTL5	Output	GPIF ctl out 5 / AEN
33	PB6	I/O	Port B, Bit 6 / FD6	34	RXD0	Input	Serial Port 0 TTL RxD (Do not use if U1 is populated)
35	PB7	I/O	Port B, Bit 7 / FD7	36	TXD0	Output	Serial Port 0 TTL RxD (Do not use if U1 is populated)
37	T0	Input	Input for Timer0	38	T1	Input	Input for Timer1
39	GND	N/A	Ground	40	+5V	N/A	Unregulated +5V from the USB bus (300mA total)

Connector J3

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	PC0	I/O	Port C, Bit 0 / GPIFADR0	2	RDY0	Input	GPIF input signal 0
3	PC1	I/O	Port C, Bit 1 / GPIFADR1	4	RDY1	Input	GPIF input signal 1
5	PC2	I/O	Port C, Bit 2 / GPIFADR2	6	RDY2	Input	GPIF input signal 2
7	PC3	I/O	Port C, Bit 3 / GPIFADR3	8	RDY3	Input	GPIF input signal 3
9	PC4	I/O	Port C, Bit 4 / GPIFADR4	10	RDY4	Input	GPIF input signal 4
11	PC5	I/O	Port C, Bit 5 / GPIFADR5	12	RDY5	Input	GPIF input signal 5
13	PC6	I/O	Port C, Bit 6 / GPIFADR6	14	RXD1	Input	Serial Port 1 TTL RxD (Do not use if U1 is populated)
15	PC7	I/O	Port C, Bit 7 / GPIFADR7	16	TXD1	Output	Serial Port 1 TTL RxD (Do not use if U1 is populated)
17	GND	N/A	Ground	18	+5V	N/A	Unregulated +5V from the USB bus (300mA total)
19	PD0	I/O	Port D, Bit 0 / FD8	20	PE0	I/O	Port E, Bit 0 / DATA0 / MOSI
21	PD1	I/O	Port D, Bit 1 / FD9	22	PE1	I/O	Port E, Bit 1 / DCLK / SCK
23	PD2	I/O	Port D, Bit 2 / FD10	24	PE2	I/O	Port E, Bit 2 / nCE
25	PD3	I/O	Port D, Bit 3 / FD11	26	PE3	I/O	Port E, Bit 3 / nCONFIG
27	PD4	I/O	Port D, Bit 4 / FD12	28	PE4	I/O	Port E, Bit 4 / nSTATUS
29	PD5	I/O	Port D, Bit 5 / FD13	30	PE5	I/O	Port E, Bit 5 / CONF_DONE / MISO (see note)
31	PD6	I/O	Port D, Bit 6 / FD14	32	PE6	I/O	Port E, Bit 6 / nSS
33	PD7	I/O	Port D, Bit 7 / FD15	34	PE7	I/O	Port E, Bit 7 / GPIFADR8

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35	SCL	OD	Clock for I2C interface
37	SDA	OD	Data for I2C interface
39	GND	N/A	Ground

36	WAKEUP_B	Input	USB Wakeup. Active low.
38	INT5_B	Input	INT5 Interrupt Request. Active low, edge sensitive
40	+5V	N/A	+5V

Notes:
1) +5V is the USB bus power. Do not exceed 300mA current drain. USB bus supplies 500mA and QuickUSB consumes 200mA.
2) RXD0, TXD0, RXD1 & TXD1 are TTL serial lines from the FX2. These signals are only usable when U1 is not populated (Only usable on the QUSB2T board).
3) SPI functionality is provided on DATA0 and DCLK