QuickUSB Evaluation Board Pinout
Rev   1.32
Date   5/16/2003

Connector	J1	See Target Interface Document

Connector   J2

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	GND	N/A	Ground	2	+5V	N/A	Unregulated +5V from the USB bus (300mA total)
3	PA0	I/O	FX2 Port A, Bit 0 / U1 Pin 7 / nSS2	4	RESET_B	OD	FX2 Reset, Active low.
5	PA1	I/O	FX2 Port A, Bit 1 / U1 Pin 8 / nSS3	6	CLKOUT	Output	FX2 48MHz CPU clock
7	PA2	I/O	FX2 Port A, Bit 2 / U1 Pin 9 / nSS4 / SLOE	8	IFCLK	Output	FX2 48MHz GPIO clock
9	PA3	I/O	FX2 Port A, Bit 3 / U1 Pin 10 / nSS5	10	INT4	Input	FX2 INT4 IRQ. Active high, edge sensitive
11	PA4	I/O	FX2 Port A, Bit 4 / U1 Pin 11 / nSS6 / FIFOADR0	12	RXD_0	Input	FX2 Serial Port 0 RS-232 RxD
13	PA5	I/O	FX2 Port A, Bit 5 / U1 Pin 12 / nSS7 / FIFOADR1	14	TXD_0	Output	FX2 Serial Port 0 RS-232 TxD
15	PA6	I/O	FX2 Port A, Bit 6 / U1 Pin 13 / nSS8 / PKTEND	16	TXD_1	Output	FX2 Serial Port 1 RS-232 TxD
17	PA7	I/O	FX2 Port A, Bit 7 / U1 Pin 14 / nSS9 / FLAGD	18	RXD_1	Input	FX2 Serial Port 1 RS-232 RxD
19	GND	N/A	Ground	20	+5V	N/A	Unregulated +5V from the USB bus (300mA total)
21	PB0	I/O	FX2 Port B, Bit 0 / FD0	22	CTL0	Output	FX2 GPIF CTL 0 / CMD_DATA / FLAGA (PF)
23	PB1	I/O	FX2 Port B, Bit 1 / FD1	24	CTL1	Output	FX2 GPIF CTL 1 / REN / FLAGB (FULL)
25	PB2	I/O	FX2 Port B, Bit 2 / FD2	26	CTL2	Output	FX2 GPIF CTL 2 / WEN / FLAGC (EMPTY)
27	PB3	I/O	FX2 Port B, Bit 3 / FD3	28	CTL3	Output	FX2 GPIF CTL 3 / nREN
29	PB4	I/O	FX2 Port B, Bit 4 / FD4	30	CTL4	Output	FX2 GPIF CTL 4 / nWEN
31	PB5	I/O	FX2 Port B, Bit 5 / FD5	32	CTL5	Output	FX2 GPIF CTL 5 / AEN
33	PB6	I/O	FX2 Port B, Bit 6 / FD6	34	RXD0	Input	FX2 Serial Port 0 TTL RxD (Do not use if U1 is populated)
35	PB7	I/O	FX2 Port B, Bit 7 / FD7	36	TXD0	Output	FX2 Serial Port 0 TTL RxD (Do not use if U1 is populated)
37	T0	Input	FX2 Input for Timer0	38	T1	Input	FX2 Input for Timer1
39	GND	N/A	Ground	40	+5V	N/A	Unregulated +5V from the USB bus (300mA total)

Connector   J3

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	PC0	I/O	FX2 Port C, Bit 0 / GPIFADR0	2	RDY0	Input	FX2 GPIF input signal 0 / SLRD
3	PC1	I/O	FX2 Port C, Bit 1 / GPIFADR1	4	RDY1	Input	FX2 GPIF input signal 1 / SLWR
5	PC2	I/O	FX2 Port C, Bit 2 / GPIFADR2	6	RDY2	Input	FX2 GPIF input signal 2
7	PC3	I/O	FX2 Port C, Bit 3 / GPIFADR3	8	RDY3	Input	FX2 GPIF input signal 3
9	PC4	I/O	FX2 Port C, Bit 4 / GPIFADR4	10	RDY4	Input	FX2 GPIF input signal 4
11	PC5	I/O	FX2 Port C, Bit 5 / GPIFADR5	12	RDY5	Input	FX2 GPIF input signal 5
13	PC6	I/O	FX2 Port C, Bit 6 / GPIFADR6	14	RXD1	Input	FX2 Serial Port 1 TTL RxD (Do not use if U1 is populated)
15	PC7	I/O	FX2 Port C, Bit 7 / GPIFADR7	16	TXD1	Output	FX2 Serial Port 1 TTL RxD (Do not use if U1 is populated)
17	GND	N/A	Ground	18	+5V	N/A	Unregulated +5V from the USB bus (300mA total)
19	PD0	I/O	FX2 Port D, Bit 0 / FD8	20	PE0	I/O	FX2 Port E, Bit 0 / DATA0 / MOSI
21	PD1	I/O	FX2 Port D, Bit 1 / FD9	22	PE1	I/O	FX2 Port E, Bit 1 / DCLK / SCK
23	PD2	I/O	FX2 Port D, Bit 2 / FD10	24	PE2	I/O	FX2 Port E, Bit 2 / nCE
25	PD3	I/O	FX2 Port D, Bit 3 / FD11	26	PE3	I/O	FX2 Port E, Bit 3 / nCONFIG
27	PD4	I/O	FX2 Port D, Bit 4 / FD12	28	PE4	I/O	FX2 Port E, Bit 4 / nSTATUS
29	PD5	I/O	FX2 Port D, Bit 5 / FD13	30	PE5	I/O	FX2 Port E, Bit 5 / CONF_DONE / MISO
31	PD6	I/O	FX2 Port D, Bit 6 / FD14	32	PE6	I/O	FX2 Port E, Bit 6 / nSS0
33	PD7	I/O	FX2 Port D, Bit 7 / FD15	34	PE7	I/O	FX2 Port E, Bit 7 / GPIFADR8 / nSS1
35	SCL	OD	Clock for I2C interface (Termination supplied on-board)	36	WAKEUP_B	Input	FX2 USB Wakeup. Active low.
37	SDA	OD	Data for I2C interface (Termination supplied on-board)	38	INT5_B	Input	FX2 INT5 Interrupt Request. Active low, edge sensitive
39	GND	N/A	Ground	40	+5V	N/A	Unregulated +5V from the USB bus (300mA total)

Connector   J4

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	+3VD	N/A	Regulated +3.3V from evaluation board regulator	2	+3VD	N/A	Regulated +3.3V from evaluation board regulator
3	USERIO7	I/O	U1, Pin 7 / Port A, Bit 0	4	USERIO8	I/O	U1, Pin 8 / Port A, Bit 1
5	USERIO9	I/O	U1, Pin 9 / Port A, Bit 2	6	USERIO10	I/O	U1, Pin 10 / Port A, Bit 3
7	USERIO11	I/O	U1, Pin 11 / Port A, Bit 4	8	USERIO12	I/O	U1, Pin 12 / Port A, Bit 5
9	USERIO13	I/O	U1, Pin 13 / Port A, Bit 6	10	USERIO14	I/O	U1, Pin 14 / Port A, Bit 7
11	USERIO15	I/O	U1, Pin 15 / Port B, Bit 0	12	USERIO16	I/O	U1, Pin 16 / Port B, Bit 1
13	USERIO17	I/O	U1, Pin 17 / Port B, Bit 2	14	USERIO18	I/O	U1, Pin 18 / Port B, Bit 3
15	USERIO19	I/O	U1, Pin 19 / Port B, Bit 4	16	USERIO24	I/O	U1, Pin 24 / Port B, Bit 5
17	USERIO25	I/O	U1, Pin 25 / Port B, Bit 6	18	USERIO26	I/O	U1, Pin 26 / Port B, Bit 7
19	USERIO27	I/O	U1, Pin 27 / Port C, Bit 0	20	USERIO28	I/O	U1, Pin 28 / Port C, Bit 1
21	USERIO29	I/O	U1, Pin 29 / Port C, Bit 2	22	USERIO30	I/O	U1, Pin 30 / Port C, Bit 3
23	USERIO31	I/O	U1, Pin 31 / Port C, Bit 4	24	USERIO36	I/O	U1, Pin 36 / Port C, Bit 5
25	USERIO37	I/O	U1, Pin 37 / Port C, Bit 6	26	USERIO38	I/O	U1, Pin 38 / Port C, Bit 7
27	USERIO39	I/O	U1, Pin 39 / Port D, Bit 0	28	USERIO40	I/O	U1, Pin 40 / Port D, Bit 1
29	USERIO41	I/O	U1, Pin 41 / Port D, Bit 2	30	USERIO44	I/O	U1, Pin 44 / Port D, Bit 3
31	USERIO45	I/O	U1, Pin 45 / Port D, Bit 4	32	USERIO46	I/O	U1, Pin 46 / Port D, Bit 5
33	USERIO47	I/O	U1, Pin 47 / Port D, Bit 6	34	USERIO53	I/O	U1, Pin 53 / Port D, Bit 7
35	USERIO54	I/O	U1, Pin 54 / FX2 SCL	36	USERIO55	I/O	U1, Pin 55 / FX2 SDA
37	USERIO56	I/O	U1, Pin 56 / FX2 T0	38	USERIO57	I/O	U1, Pin 57 / FX2 INT4
39	GND	N/A	Ground	40	GND	N/A	Ground

Connector   J5

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	+3VD	N/A	Regulated +3.3V from evaluation board regulator	2	+3VD	N/A	Regulated +3.3V from evaluation board regulator
3	USERIO58	I/O	U1, Pin 58 / FX2 CTL0	4	USERIO60	I/O	U1, Pin 60 / FX2 CTL1
5	USERIO61	I/O	U1, Pin 61 / FX2 CTL2	6	USERIO62	I/O	U1, Pin 62 / FX2 CTL3
7	USERIO63	I/O	U1, Pin 63 / FX2 CTL4	8	USERIO64	I/O	U1, Pin 64 / FX2 CTL5
9	USERIO65	I/O	U1, Pin 65 / FX2 RDY0	10	USERIO67	I/O	U1, Pin 67 / FX2 RDY0
11	USERIO68	I/O	U1, Pin 68 / FX2 RDY2	12	USERIO69	I/O	U1, Pin 69 / FX2 RDY3
13	USERIO70	I/O	U1, Pin 70 / FX2 RDY4	14	USERIO71	I/O	U1, Pin 71 / FX2 RDY5
15	USERIO73	I/O	U1, Pin 73 / Port E, Bit 6	16	USERIO74	I/O	U1, Pin 74 / Port E, Bit 7
17	USERIO75	I/O	U1, Pin 75 / FX2 USB Wakeup. Active low.	18	INPUT1	Input	U1, Pin 78
19	GLCK1	N/A	Global Clock 1 See Schematic	20	INPUT2	Input	U1, Pin 80
21	USERIO83	I/O	U1, Pin 83 / FX2 T1	22	USERIO85	I/O	U1, Pin 85 / FX2 INT5
23	USERIO86	I/O	U1, Pin 86	24	USERIO87	I/O	U1, Pin 87
25	USERIO88	I/O	U1, Pin 88	26	USERIO89	I/O	U1, Pin 89
27	USERIO90	I/O	U1, Pin 90	28	USERIO92	I/O	U1, Pin 92
29	USERIO93	I/O	U1, Pin 93	30	USERIO94	I/O	U1, Pin 94
31	USERIO95	I/O	U1, Pin 95	32	USERIO96	I/O	U1, Pin 96
33	USERIO97	I/O	U1, Pin 97	34	USERIO99	I/O	U1, Pin 99
35	USERIO100	I/O	U1, Pin 100	36	USERIO101	I/O	U1, Pin 101
37	USERIO102	I/O	U1, Pin 102	38	USERIO103	I/O	U1, Pin 103
39	GND	N/A	Ground	40	GND	N/A	Ground

QuickUSB Evaluation Board Pinout
Rev 1.32
Date 5/16/2003

Connector **J6**

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	+3VD	N/A	Regulated +3.3V from evaluation board regulator	2	+3VD	N/A	Regulated +3.3V from evaluation board regulator
3	USERIO104	I/O	U1, Pin 104	4	USERIO111	I/O	U1, Pin 111
5	USERIO112	I/O	U1, Pin 112	6	USERIO113	I/O	U1, Pin 113
7	USERIO114	I/O	U1, Pin 114	8	USERIO115	I/O	U1, Pin 115
9	USERIO116	I/O	U1, Pin 116	10	USERIO119	I/O	U1, Pin 119
11	USERIO120	I/O	U1, Pin 120	12	USERIO121	I/O	U1, Pin 121
13	USERIO122	I/O	U1, Pin 122	14	USERIO125	I/O	U1, Pin 125
15	USERIO126	I/O	U1, Pin 126	16	USERIO127	I/O	U1, Pin 127
17	USERIO128	I/O	U1, Pin 128	18	USERIO131	I/O	U1, Pin 131
19	USERIO132	I/O	U1, Pin 132	20	USERIO133	I/O	U1, Pin 133
21	USERIO134	I/O	U1, Pin 134	22	USERIO135	I/O	U1, Pin 135
23	USERIO136	I/O	U1, Pin 136	24	USERIO139	I/O	U1, Pin 139
25	USERIO140	I/O	U1, Pin 140	26	USERIO141	I/O	U1, Pin 141
27	USERIO142	I/O	U1, Pin 142	28	USERIO143	I/O	U1, Pin 143
29	USERIO144	I/O	U1, Pin 144	30	USERIO147	I/O	U1, Pin 147
31	USERIO148	I/O	U1, Pin 148	32	USERIO149	I/O	U1, Pin 149
33	USERIO150	I/O	U1, Pin 150	34	USERIO157	I/O	U1, Pin 157
35	USERIO158	I/O	U1, Pin 158	36	USERIO159	I/O	U1, Pin 159
37	USERIO160	I/O	U1, Pin 160	38	USERIO161	I/O	U1, Pin 161
39	GND	N/A	Ground	40	GND	N/A	Ground

Connector **J7**

Pin	Name	Dir	Description	Pin	Name	Dir	Description
1	+3VD	N/A	Regulated +3.3V from evaluation board regulator	2	+3VD	N/A	Regulated +3.3V from evaluation board regulator
3	USERIO162	I/O	U1, Pin 162	4	USERIO163	I/O	U1, Pin 163
5	USERIO164	I/O	U1, Pin 164	6	USERIO166	I/O	U1, Pin 166
7	USERIO167	I/O	U1, Pin 167	8	USERIO168	I/O	U1, Pin 168
9	USERIO169	I/O	U1, Pin 169	10	USERIO170	I/O	U1, Pin 170
11	USERIO172	I/O	U1, Pin 172	12	USERIO173	I/O	U1, Pin 173
13	USERIO174	I/O	U1, Pin 174	14	USERIO175	I/O	U1, Pin 175
15	USERIO176	I/O	U1, Pin 176	16	USERIO177	I/O	U1, Pin 177
17	USERIO179	I/O	U1, Pin 179	18	USERIO180	I/O	U1, Pin 180
19	INPUT3	Input	U1, Pin 182	20	CLK	Input	FX2 IFCLK / U1, Pin 183
21	INPUT4	Input	U1, Pin 184	22	USERIO186	I/O	U1, Pin 186
23	USERIO187	I/O	U1, Pin 187	24	USERIO189	I/O	U1, Pin 189
25	USERIO190	I/O	U1, Pin 190	26	USERIO191	I/O	U1, Pin 191
27	USERIO192	I/O	U1, Pin 192	28	USERIO193	I/O	U1, Pin 193
29	USERIO195	I/O	U1, Pin 195	30	USERIO196	I/O	U1, Pin 196
31	USERIO197	I/O	U1, Pin 197	32	USERIO198	I/O	U1, Pin 198
33	USERIO199	I/O	U1, Pin 199	34	USERIO200	I/O	U1, Pin 200
35	USERIO202	I/O	U1, Pin 202	36	USERIO203	I/O	U1, Pin 203
37	USERIO204	Input	U1, Pin 204	38	USERIO205	I/O	U1, Pin 205
39	GND	N/A	Ground	40	GND	N/A	Ground

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Connector   **P1**

Pin	Name	Dir	Description
1	NC	N/A	Not Connected
2	TXD_0	Output	FX2 RXD_0 Pin 51 (NULL Modem incorporated into board)
3	RXD_0	Input	FX2 TXD_0 Pin 50 (NULL Modem incorporated into board)
4	NC	N/A	Not Connected
5	GND	N/A	Ground
6	NC	N/A	Not Connected
7	NC	N/A	Not Connected
8	NC	N/A	Not Connected
9	NC	N/A	Not Connected

Connector   **P2**

Pin	Name	Dir	Description
1	NC	N/A	Not Connected
2	TXD_1	Output	FX2 RXD_1 Pin 53 (NULL Modem incorporated into board)
3	RXD_1	Input	FX2 TXD_1 Pin 52 (NULL Modem incorporated into board)
4	NC	N/A	Not Connected
5	GND	N/A	Ground
6	NC	N/A	Not Connected
7	NC	N/A	Not Connected
8	NC	N/A	Not Connected
9	NC	N/A	Not Connected

Notes:

1) +5V is the USB bus power. Do not exceed 300mA current drain. USB bus supplies 500mA and QuickUSB consumes 200mA.

2) RXD0, TXD0, RXD1 & TXD1 are TTL serial lines from the FX2. These signals are only usable when U1 is not populated (Only usable on the QUSB2T board).

3) SPI functionality is provided on DATA0 and DCLK