QuickUSB QUSB2 Target Interface Connector
Rev   B
Date   February 5, 2010

Pin	Name	Alternate Function	Dir	Description	Pin	Name	Alternate Function	Dir	Description
1	GND		N/A	Ground	2	+5V		N/A	Unregulated +5V from the USB bus (425mA total)
3	PA0	nSS2 / nINT0	I/O	Port A, Bit 0	4	RESET_B		Open Drain	FX2 reset, Active low. Resets CPU, but not the USB.
5	PA1	nSS3 / nINT1	I/O	Port A, Bit 1	6	CLKOUT		Output	48MHz CPU clock
7	PA2	nSS4 / SLOE	I/O	Port A, Bit 2	8	IFCLK		I/O	48MHz GPIO clock output or external clock input
9	PA3	nSS5	I/O	Port A, Bit 3	10	INT4		Input	8051 INT4 IRQ. Active high, edge sensitive
11	PA4	nSS6 / FIFOADR0	I/O	Port A, Bit 4	12	RXD_0		Input	Serial Port 0 EIA/TIA 564 RxD
13	PA5	nSS7 / FIFOADR1	I/O	Port A, Bit 5	14	TXD_0		Output	Serial Port 0 EIA/TIA 564 TxD
15	PA6	nSS8 / PKTEND	I/O	Port A, Bit 6	16	TXD_1		Output	Serial Port 1 EIA/TIA 564 TxD
17	PA7	nSS9 / FLAGD / nSLCS	I/O	Port A, Bit 7	18	RXD_1		Input	Serial Port 1 EIA/TIA 564 RxD
19	GND		N/A	Ground	20	+5V		N/A	Unregulated +5V from the USB bus (425mA total)
21	PB0	FD0	I/O	Port B, Bit 0	22	CTL0	CMD_DATA / FLAGA	Output	GPIF control output 0
23	PB1	FD1	I/O	Port B, Bit 1	24	CTL1	REN / FLAGB	Output	GPIF control output 1
25	PB2	FD2	I/O	Port B, Bit 2	26	CTL2	WEN / FLAGC	Output	GPIF control output 2
27	PB3	FD3	I/O	Port B, Bit 3	28	CTL3		Output	GPIF control output 3
29	PB4	FD4	I/O	Port B, Bit 4	30	CTL4		Output	GPIF control output 4
31	PB5	FD5	I/O	Port B, Bit 5	32	CTL5		Output	GPIF control output 5
33	PB6	FD6	I/O	Port B, Bit 6	34	RXD0		Input	Serial Port 0 TTL RxD (Do not use if U1 is populated)
35	PB7	FD7	I/O	Port B, Bit 7	36	TXD0		Output	Serial Port 0 TTL RxD (Do not use if U1 is populated)
37	GND		N/A	Ground	38	+5V		N/A	Unregulated +5V from the USB bus (250mA total)
39	PC0	GPIFADR0	I/O	Port C, Bit 0	40	RDY0	EF / ACK / SLRD	Input	GPIF input signal 0
41	PC1	GPIFADR1	I/O	Port C, Bit 1	42	RDY1	FF / SLWR	Input	GPIF input signal 1
43	PC2	GPIFADR2	I/O	Port C, Bit 2	44	RDY2		Input	GPIF input signal 2
45	PC3	GPIFADR3	I/O	Port C, Bit 3	46	RDY3		Input	GPIF input signal 3
47	PC4	GPIFADR4	I/O	Port C, Bit 4	48	RDY4		Input	GPIF input signal 4
49	PC5	GPIFADR5	I/O	Port C, Bit 5	50	RDY5		Input	GPIF input signal 5
51	PC6	GPIFADR6	I/O	Port C, Bit 6	52	RXD1		Input	Serial Port 1 TTL RxD (Do not use if U1 is populated)
53	PC7	GPIFADR7	I/O	Port C, Bit 7	54	TXD1		Output	Serial Port 1 TTL RxD (Do not use if U1 is populated)
55	GND		N/A	Ground	56	+5V		N/A	Unregulated +5V from the USB bus (425mA total)
57	PD0	FD8	I/O	Port D, Bit 0	58	PE0	DATA0 / MOSI	I/O	Port E, Bit 0
59	PD1	FD9	I/O	Port D, Bit 1	60	PE1	DCLK / SCK	I/O	Port E, Bit 1
61	PD2	FD10	I/O	Port D, Bit 2	62	PE2	nCE	I/O	Port E, Bit 2
63	PD3	FD11	I/O	Port D, Bit 3	64	PE3	nCONFIG	I/O	Port E, Bit 3
65	PD4	FD12	I/O	Port D, Bit 4	66	PE4	nSTATUS	I/O	Port E, Bit 4
67	PD5	FD13	I/O	Port D, Bit 5	68	PE5	CONF_DONE / MISO	I/O	Port E, Bit 5
69	PD6	FD14	I/O	Port D, Bit 6	70	PE6	nSS0	I/O	Port E, Bit 6
71	PD7	FD15	I/O	Port D, Bit 7	72	PE7	GPIFADR8 / nSS1	I/O	Port E, Bit 7
73	SCL		OD	Clock for I2C interface	74	WAKEUP_B		Input	USB Wakeup. Active low. (Do not connect)
75	SDA		OD	Data for I2C interface	76	SW_PG		Output	High indicates valid +5V.
77	T0		Input	Input for Timer0	78	T1		Input	Input for Timer1
79	GND		N/A	Ground	80	+5V		N/A	+5V

Notes:

1) With pin 1 in the upper left, numbering starts with pin 1 and proceeds left to right, top to bottom.

2) The target interface connector is a Hirose FX8-80P-SV. The mating connector is FX8-80S-SV (3mm mating height) or FX8-80S-SV1 (4mm mating height)

3) +5V is the USB bus power. Do not exceed 425mA current drain. USB bus supplies 500mA and QuickUSB consumes approx 75mA.

4) RXD0, TXD0, RXD1 & TXD1 are TTL serial lines from the FX2. Only usable when U1 is not populated or its outputs are disabled.

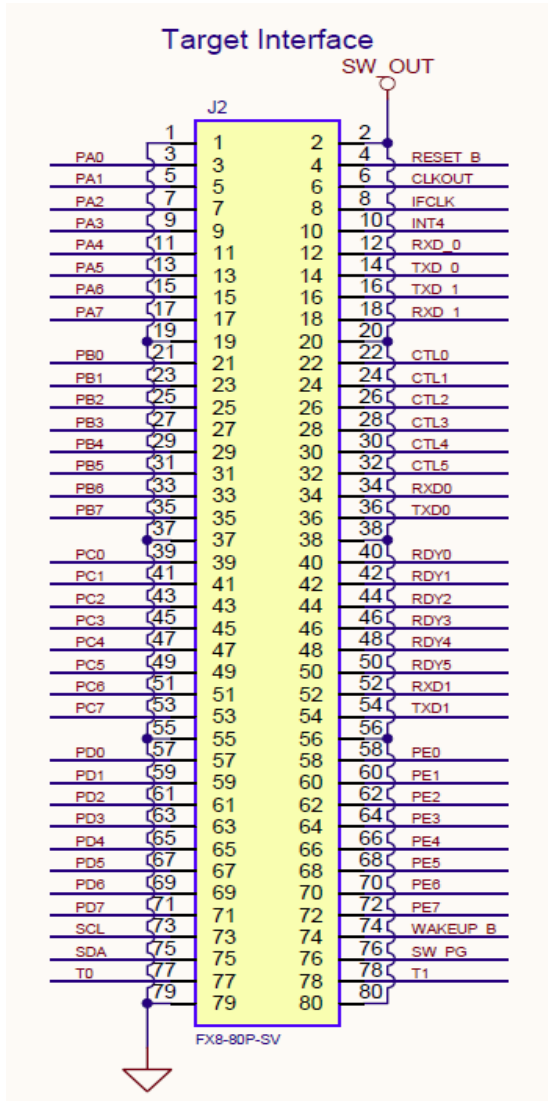5) SPI functionality is provided on MOSI, MISO, SCLK, and nSS0-9

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Schematic Symbol

Photograph

Target Interface

SW_OUT

J2 — FX8-80P-SV

Left signal	Pin	Pin	Right signal
	1	2	SW_OUT
PA0	3	4	RESET_B
PA1	5	6	CLKOUT
PA2	7	8	IFCLK
PA3	9	10	INT4
PA4	11	12	RXD_0
PA5	13	14	TXD_0
PA6	15	16	TXD_1
PA7	17	18	RXD_1
	19	20	
PB0	21	22	CTL0
PB1	23	24	CTL1
PB2	25	26	CTL2
PB3	27	28	CTL3
PB4	29	30	CTL4
PB5	31	32	CTL5
PB6	33	34	RXD0
PB7	35	36	TXD0
	37	38	
PC0	39	40	RDY0
PC1	41	42	RDY1
PC2	43	44	RDY2
PC3	45	46	RDY3
PC4	47	48	RDY4
PC5	49	50	RDY5
PC6	51	52	RXD1
PC7	53	54	TXD1
	55	56	
PD0	57	58	PE0
PD1	59	60	PE1
PD2	61	62	PE2
PD3	63	64	PE3
PD4	65	66	PE4
PD5	67	68	PE5
PD6	69	70	PE6
PD7	71	72	PE7
SCL	73	74	WAKEUP_B
SDA	75	76	SW_PG
T0	77	78	T1
	79	80	

FX8-80P-SV

Hirose FX8-80P-SV
Connector