



复旦微电子

# ***FM11NT0X1D***

***NFC Forum Type2 Tag compliant IC with  
Dual Interface***

**Datasheet**

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**Dec.2014**



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# Contents

<b>CONTENTS.....</b>	<b>3</b>
<b>1 PRODUCT OVERVIEW.....</b>	<b>4</b>
1.1 INTRODUCTION.....	4
1.2 FEATURES .....	4
1.2.1 Key features .....	4
1.2.2 RF Interface .....	4
1.2.3 Contact Interface .....	5
1.2.4 EEPROM .....	5
1.2.5 NFC Forum Tag 2 Type compliance.....	5
1.2.6 Security .....	5
1.2.7 Cascaded UID.....	6
1.2.8 Anticollision.....	6
1.3 BLOCK DIAGRAM.....	6
1.4 PINNING INFORMATION .....	7
1.4.1 DFN10 Pinning Assignment (Top View) for FM11NT0X1DS .....	7
1.4.2 Pin description for FM11NT0X1DS.....	7
1.4.3 DFN10 Pinning Assignment (Top View) for FM11NT0X1DI.....	8
1.4.4 Pin description for FM11NT0X1DI .....	8
<b>2 ORDERING INFORMATION.....</b>	<b>9</b>
<b>3 PACKAGE OUTLINE.....</b>	<b>10</b>
3.1 DFN10.....	10
<b>REVISION HISTORY.....</b>	<b>11</b>
<b>SALES AND SERVICE.....</b>	<b>12</b>

# 1 Product Overview

## 1.1 Introduction

FM11NT0X1D is a NFC Forum Type 2 Tag chip offering both contactless and contact interfaces. In addition to the passive NFC Forum compliant contactless interface, the IC features an I2C(FM11NT0X1DI) or SPI(FM11NT0X1DS) contact interface, which can communicate with a microcontroller.

FM11NT0X1D features a configurable Field Detection Pin, which provides a trigger to an external device depending on the activities at the RF interface.

FM11NT0X1D can also supply power to external (low power) devices (e.g., a microcontroller) via the embedded energy harvesting circuitry.

FM11NT0X1D can be used in applications such as electronic shelf labels, Home automation, Consumer electronics, Healthcare, etc.

Please consult Fudan Micro Electronics Company for more documents.

## 1.2 Features

### 1.2.1 Key features

- Ø RF interface NFC forum Type 2 Tag compliant.
- Ø I2C or SPI interface, zero power consumption on standby mode.
- Ø Configurable field detection pin based on open drain implementation that can be triggered upon the following events:
  - I A RF field presence
  - I The first Start-of-Frame
  - I The selection of the tag only
- Ø Energy harvesting from the field to power external devices by the VOUT pin. The voltage of VOUT can be configured to 1.5V, 1.8V, 2.5V or 3.3V. Current limitation ability during energy harvesting can be configured.

### 1.2.2 RF Interface

- Ø ISO/IEC 14443A
- Ø Contactless transmission of data and supply energy (no battery needed)
- Ø Operating distance: up to 100 mm (depending on various parameters as e.g. field strength and antenna geometry)
- Ø Operating frequency: 13.56 MHz
- Ø Fast data transfer: 106 Kbit/s
- Ø High data integrity: 16-bit CRC, parity, bit coding, bit counting

- Ø True anticollision
- Ø 7 byte serial number (cascade level 2 according to ISO/IEC 14443-3)
- Ø UID ASCII mirror for automatic serialization of NDEF messages
- Ø Automatic NFC counter triggered at read command
- Ø NFC counter ASCII mirror for automatic adding the NFC counter value to the NDEF message
- Ø ECC based originality signature
- Ø Fast read command
- Ø 50 pF input capacitance

### 1.2.3 Contact Interface

- Ø Zero standby power consumption for contact interface
- Ø Operating voltage for contact interface: 1.8~5.5V
- Ø Contact interface: SPI or I2C
- Ø Max data rate for I2C: 1Mbps
- Ø Max data rate for SPI: 5Mbps

### 1.2.4 EEPROM

- Ø 180, 540 or 924 bytes organized in 45, 135 or 231 pages with 4 bytes per page
- Ø 144, 504 or 888 bytes freely available user Read/Write area (36, 126 or 222 pages)
- Ø 4 bytes initialized capability container with one time programmable access bits
- Ø Field programmable read-only locking function per page for the first 16 pages
- Ø Field programmable read-only locking function above the first 16 pages per double page for FM11NT021D or per 16 pages for FM11NT041D and FM11NT081D
- Ø Configurable password protection with optional limit of unsuccessful attempts
- Ø Anti-tearing support for capability container (CC) and lock bits
- Ø ECC supported originality check
- Ø Data retention of 50 years
- Ø Write endurance 200000 cycles

### 1.2.5 NFC Forum Tag 2 Type compliance

FM11NT0X1D IC provides full compliance to the NFC Forum Tag 2 Type technical specification and enables NDEF data structure configurations.

### 1.2.6 Security

- Ø Manufacturer programmed 7-byte UID for each device
- Ø Pre-programmed Capability container with one time programmable bits
- Ø Field programmable read-only locking function
- Ø ECC based originality signature
- Ø 32-bit password protection to prevent unauthorized memory operations. The protected memory's size can be configured.

### 1.2.7 Cascaded UID

The anticollision function is based on an IC individual serial number called Unique Identifier. The UID of the FM11NT0X1D is 7 bytes long and supports cascade level 2 according to ISO/IEC 14443-3.

### 1.2.8 Anticollision

An intelligent anticollision function according to ISO/IEC 14443 allows operating more than one card in the field simultaneously. The anticollision algorithm selects each card individually and ensures that the execution of a transaction with a selected card is performed correctly without data corruption resulting from other cards in the field.

## 1.3 Block diagram

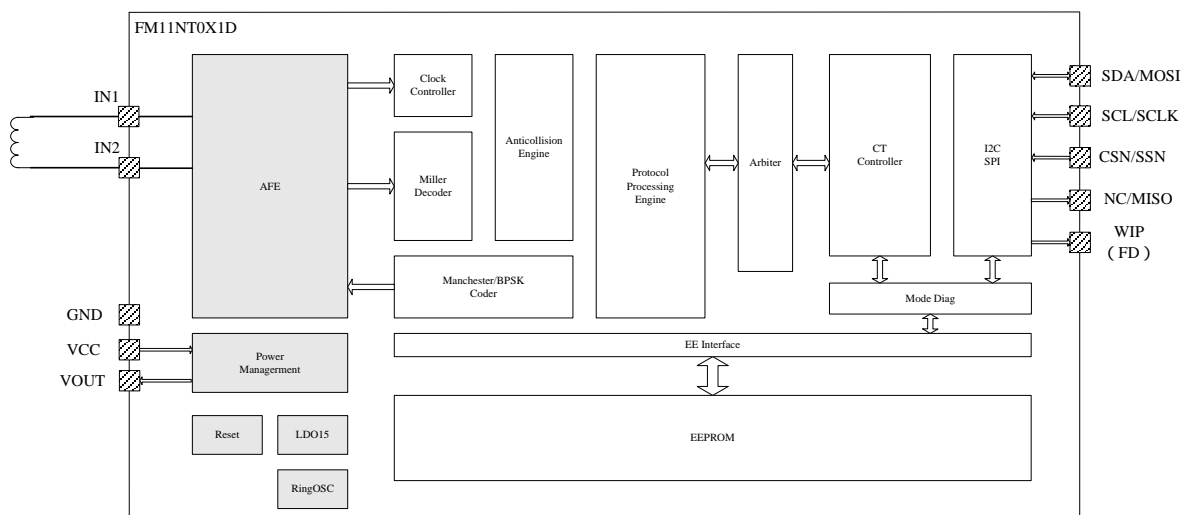


Figure 1-1 FM11NT0X1D Block diagram

## 1.4 Pinning information

### 1.4.1 DFN10 Pinning Assignment ( Top View ) for FM11NT0X1DS

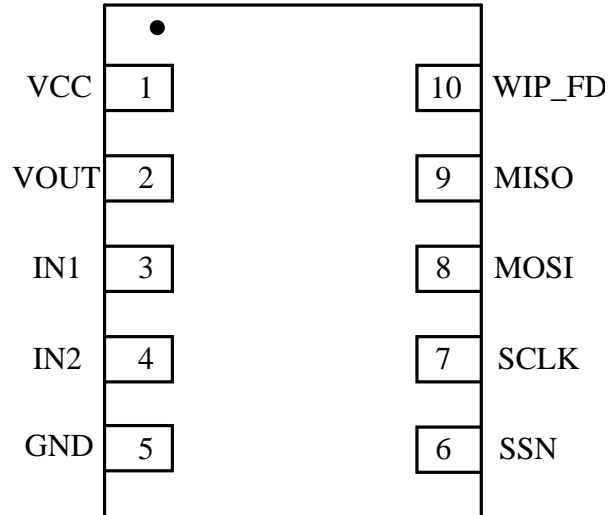


Figure 1-2 FM11NT0X1DS DFN10 Pinning assignment

### 1.4.2 Pin description for FM11NT0X1DS

Pin No.	Pin Name	Type	Description
1	VCC	PWR	contact interface power supply
2	VOUT	ANA	regulated voltage output for energy harvesting
3	IN1	ANA	RF antenna pin
4	IN2	ANA	RF antenna pin
5	GND	GND	chip ground
6	SSN	DI	SPI chip-selection, low-effective
7	SCLK	DI	SPI clock input
8	MOSI	DIO	SPI slave data input
9	MISO	DO	SPI slave data ouput
10	WIP_FD	OD	The flag of EEPROM's write/erase by the contactless interface, or the flag of field detection. Low-effective.

Table 1-1 FM11NT0X1DS DFN10 PIN description

### 1.4.3 DFN10 Pinning Assignment ( Top View ) for FM11NT0X1DI

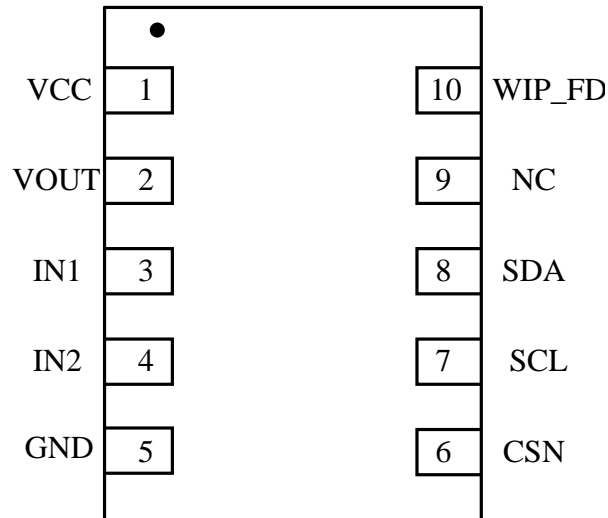


Figure 1-3 FM11NT0X1DI DFN10 Pinning assignment

### 1.4.4 Pin description for FM11NT0X1DI

Pin No.	Pin Name	Type	Description
1	VCC	PWR	contact interface power supply
2	VOUT	ANA	regulated voltage output for energy harvesting
3	IN1	ANA	RF antenna pin
4	IN2	ANA	RF antenna pin
5	GND	GND	chip ground
6	CSN	DI	I2C chip-selection, low-effective
7	SCL	DI	I2C clock input
8	SDA	DI/OD	I2C data, pull up resistor is needed
9	NC	DO	high-Z after power up
10	WIP_FD	OD	The flag of EEPROM's write/erase by the contactless interface, or the flag of field detection. Low-effective.

Table 1-2 FM11NT0X1DI DFN10 PIN description





## 2 Ordering information

Type Number	Package Name	Packing
FM11NT0X1DS-DN-T-G	DFN10	Tape and Reel

	FM	11NT	0X	1	D	-XXX	-C	-H
Company Name	FM=Shanghai Fudan Microelectronics Group Company Limited							
Product Family Name		11NT=NFC TAG Product						
Memory			02: about 2k bit EEPROM 04: about 4k bit EEPROM 08: about 8k bit EEPROM					
Generation number				1: The second generation				
Product type					D: Dual interface			
Package						DN: DFN10		
Packing							T: Tape and Reel U: Tube	
HSF code								G: ROHS Compliant, Halogen-free, Antimony-free

## 3 Package outline

### 3.1 DFN10

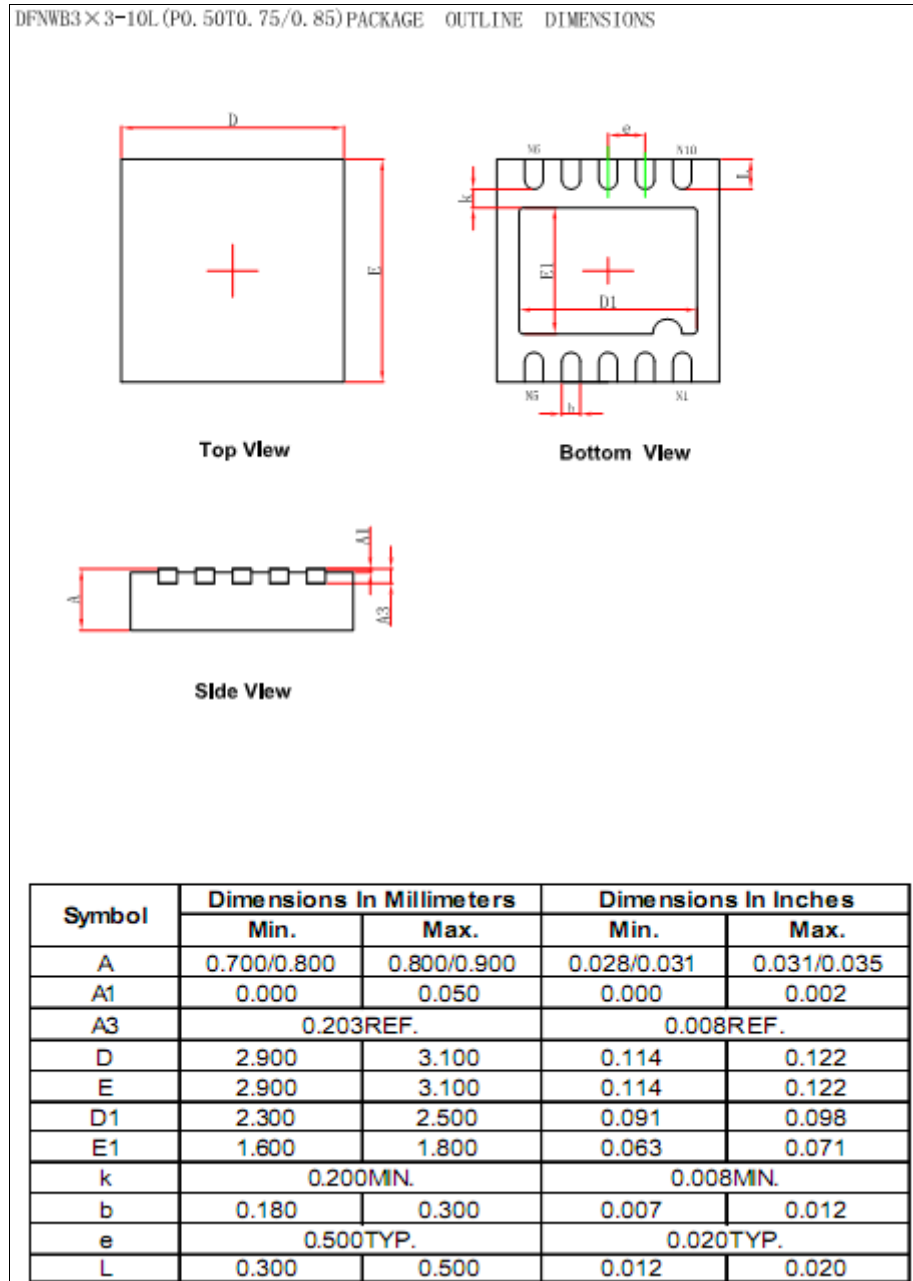


Figure 3-1 DFN10 package size



## Revision history

Version	Publication date	Pages	Paragraph or Illustration	Revise Description
1.0	Dec.2014	12		Initial release



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