

A

B

C

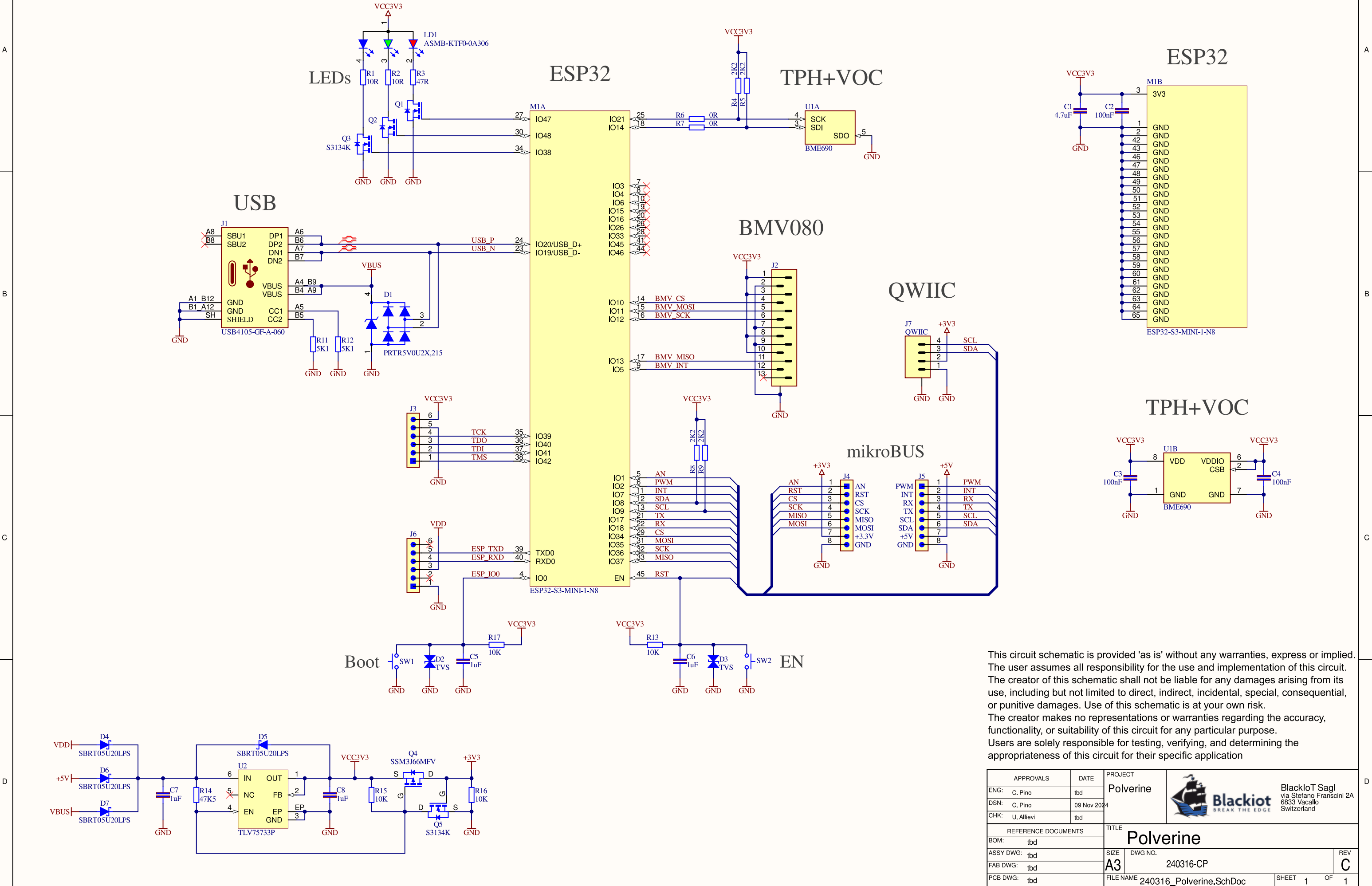
D

A


B

C

D



This circuit schematic is provided 'as is' without any warranties, express or implied. The user assumes all responsibility for the use and implementation of this circuit. The creator of this schematic shall not be liable for any damages arising from its use, including but not limited to direct, indirect, incidental, special, consequential, or punitive damages. Use of this schematic is at your own risk. The creator makes no representations or warranties regarding the accuracy, functionality, or suitability of this circuit for any particular purpose. Users are solely responsible for testing, verifying, and determining the appropriateness of this circuit for their specific application

APPROVALS	DATE	PROJECT	 BlackIoT Sagl via Stefano Francini 2A 6833 Vacallo Switzerland
ENG: C. Pino	tbd	Polverine	
DSN: C. Pino	09 Nov 2024		
CHK: U. Allievi	tbd		
REFERENCE DOCUMENTS		TITLE	Polverine
BOM: tbd			
ASSY DWG: tbd			
FAB DWG: tbd			
PCB DWG: tbd			
SIZE A3	DWG NO. 240316-CP	REV C	
FILE NAME 240316_Polverine.SchDoc		SHEET 1 OF 1	