

**EE 309**  
**Microprocessors**

**IITB - RISC**

- (A) *Multi-cycle Implementation*  
(B) *Pipelined Implementation*

**Team :**

Aakash Kapoor	160070060
Goru Meher Ritesh Kumar	160070048
Hemant Sharma	160070039
Neeraj Chanamolu	160070047

## **Undertaking**

We hereby affirm and assure that whatever code files, data path and control store have been submitted have all been developed by us individually and we have not taken help from any of our peers or our seniors as part of this project.

## **Documentation Details**

The code files have been duly shown to the TAs and as per their request, have been submitted in the necessary format at the appropriate places. We have also attached images of the data paths that we have developed for both parts of the assignment separately in the folder provided.