I-bus 0×4000_0000	ESP32 Memory Map	D-bus
0x4000_8000	ROM0 (32 kB)	
	ROM0 (352 kB)	
0x4005_FFFF 0x4001 0000		0x3FF9 0000
	ROM1	
0x4001_FFFF	(64 kB)	0x3FF9_FFFF
0x4007 0000	_	
(16k, 32k lCache) (POOL 0,1) 0x4008_0000	(64kB)	
0x4009 FFFF	SRAM0 (128 kB)	
0x400A_0000		
	SRAM1 (128 kB)	0x3FFF_FFFF (I-bus and D-bus address space has reversed
0x400B_7FFF		order) 0x3FFE 8000
0x400B_FFFF	(ROM-code reserved)	0x3FFE_0000
		0x3FFA_E000
	(reserved for shared	0x3FFB 0000
	buffers such as IPC,) SRAM2 (200 kB)	
		0x3FFD_FFFF
0x5000_0000		0x5000_0000
	RTCSLOW	
0x5000_1FFF	(8 kB)	0x5000_1FFF
0x600F_E000		0x3FF8_0000
(PRO_CPU only) 0x600F_FFFF	RTCFAST (8 kB)	(PRO_CPU only) 0x3FF8 1FFF
		_
		0x3F40_0000
	D-Cache	(Read Only) (Ext. Flash)
	(4 MB)	0x3F7F_FFFF 0x3F80_0000
	D-Cache (4 MB)	(Ext. SPIRAM)
0x400C_2000		0x3FBF_FFFF
(Ext. Flash) (64kB I-Cache blocks) (4B,16B align. RD)	I-Cache (11512 kB)	

	0x3FF0_0000	DPORT addr: (speculative reads may not be safe)	AHB addr: (Safe FIFO access)	
SP32	0x3FF0_0000 0x3FF0_1000 0x3FF0_2000 0x3FF0_3000 0x3FF1_0000 0x3FF1_2000 0x3FF1_3FC 0x3FF4_0000 0x3FF4_2000	DPORT AES RSA SHA FLASH_MMU_TABLE_PRO FLASH_MMU_TABLE_APP DPORT_END UART(UART_FIFO) SPI1 SPI0	0x6000_0000	UART_FIFO
	0x3FF4_4000 0x3FF4_4F00 0x3FF4_5000 0x3FF4_6000 0x3FF4_7000 0x3FF4_8000 0x3FF4_8400 0x3FF4_8600 0x3FF4_8C00 0x3FF4_9000 0x3FF4_B000 0x3FF4_F0000	GPIO GPIO_SD FE2 FE FRC_TIMER RTCCNTL RTCIO SENS RTC_I2C IO_MUX HINF (SDIO_SLAVE0) UHCI1 (UDMA1) I2S	0x6000_4000	GPIO_REGS**
Peripherals	0x3FF4_F000 0x3FF4_F004 0x3FF5_0000 0x3FF5_1000 0x3FF5_3000 0x3FF5_4000 0x3FF5_5000 0x3FF5_5000 0x3FF5_7000 0x3FF5_8000 0x3FF5_8000 0x3FF5_B000 0x3FF5_B000 0x3FF5_D000 0x3FF5_D000 0x3FF5_E000 0x3FF5_F000 0x3FF6_1000 0x3FF6_1000 0x3FF6_1000 0x3FF6_1000 0x3FF6_2000 0x3FF6_3000 0x3FF6_5000 0x3FF6_5000 0x3FF6_6000 0x3FF6_6000 0x3FF6_6000 0x3FF6_6000 0x3FF6_6000 0x3FF6_6000 0x3FF6_7000	I2S I2S0_FIFO_RD UART1(UART1_FIFO) BT I2C_EXT UHCI0 SLCHOST (SDIO_SLAVE1) RMT PCNT SLC(SDIO_SLAVE2) LEDC EFUSE SPI_ENCRYPT NRX BB PWM0 TIMERGROUP0 TIMERGROUP1 RTCMEM0 RTCMEM1 RTCMEM2 SPI2 SPI3 SYSCON(APB_CTRL) I2C1_EXT	0x6000_F004 0x6001_0000	I2S_FIFO_RD UART1_FIFO
	0x3FF6_8000 0x3FF6_9000 0x3FF6_B000 0x3FF6_C000 0x3FF6_D000 0x3FF6_D004 0x3FF6_E000 0x3FF7_5000	SDMMC EMAC TWAI PWM1 I2S1 I2S1_FIFO_RD UART2 RNG	0x6002_D004 0x6002_E000	I2S1_FIFO_RD USART2_FIFO

	Blk#	Bits	Purpose
ESP32 e-Fuses	0 1 2	256/192/128 256/192/128 256/192/128	Depends on coding scheme Depends on coding scheme Depends on coding scheme