Fel	b 12, 18 11:06	VC	_x86	_Exercise.asm	Page 1/2
1	; Listing generated by M			=	
2	TITLE c:\K-Sta	ate\cis45	0\Pro	grams\MemorvTest\VC :	x86_Exercise\VC_x86_Exe
3	rcise.c	ICC (CIBIO	0 (110)	grams (Memory rese (VC	AUG_EXCICISC (VC_XUU_EXC
4 5	.686₽ . <b>XMM</b>				
6	include listing.	inc			
7	.model flat				
8 9	INCLUDELIB LIBCMT				
10	INCLUDELIB OLDNAMES				
11 12	PUBLIC _x				
13	_DATA <b>SEGMENT</b>				
14 15	_x <b>DD</b> 02H _DATA <b>ENDS</b>				
16	PUBLIC _f				
17 18	PUBLIC _main _BSS SEGMENT				
19	_y <b>DD</b> 01H <b>DUP</b>	(?)			
20 21	_BSS ENDS	. /0d+n			
21	; Function compile flags ; File c:\k-state\cis450		s\mem	orytest\vc_x86_exerc	ise\vc_x86_exercise.c
23 24	_TEXT <b>SEGMENT</b> _m1\$ = -8				size = 4
25	_s1\$ = -4				size = 4 size = 2
26	_argc\$ = 8				size = 4
27 28	_argv\$ = 12 _envp\$ = 16				size = 4 size = 4
29	_main <b>PROC</b>		-1	,	
30 31	00000 55 00001 8b ec	push mov	ebp ebp,	esp	
32	00003 83 ec 08	sub	esp,	8	
33 34	00006 8b 45 08 00009 89 45 f8	mov mov		DWORD PTR _argc\$[ebj D PTR _ml\$[ebp], eax	
37	tes (int)				
35 36	0000c b9 22 00 00 00 00011 66 89 4d fc	mov mov	ecx,	34 PTR _s1\$[ebp], cx	; 00000022H ; MM: s1 is 2 by
30	tes (signed or unsigned:		wn)		
37	00015 Of bf 55 fc ed => short s1;	movsx	edx,	WORD PTR _s1\$[ebp]	; MM: s1 is sign
38	00019 52	push	edx		
39	0001a 8b 45 f8 0001d 50	mov		DWORD PTR _m1\$[ebp]	
40 41	0001a 50 0001e e8 00 00 00 00	push call	eax _f		
42	00023 83 c4 08	add	esp,		
43 44	00026 33 c0 00028 8b e5	mov	eax, esp,		
45	0002a 5d	pop	ebp		
46 47	0002b c3 _main <b>ENDP</b>	ret	0		
48	_TEXT ENDS	/ <b></b> .			
49 50	<pre>; Function compile flags ; File c:\k-state\cis450</pre>		s\mem	orvtest\vc x86 exerc	ise\vc x86 exercise.c
51	_TEXT SEGMENT	, J_ 4m	,		
52 53	_i1\$ = -12 s2\$ = -8				size = 4 size = 2
54	_s1\$ = -4			;	size = 2
55 56	_p1\$ = 8 _p2\$ = 12				size = 4 size = 4
57	_f PROC			,	
58	00000 55	push	ebp	ogn	
59 60	00001 8b ec 00003 83 ec 0c	mov sub	ebp, esp,		; 0000000cH
61	00006 8b 45 08	mov		<pre>DWORD PTR _p1\$[ebp]</pre>	; MM: p1 is 4 by
62	tes (int) 00009 03 45 0c	add	eax,	DWORD PTR _p2\$[ebp]	; MM: p2 is 4 by
63	tes (int) 0000c 89 45 f4	mov	DWOR	D PTR _i1\$[ebp], eax	; MM: i1 is 4 by
64	tes (int) 0000f 8b 4d f4	mov	ecx,	DWORD PTR _i1\$[ebp]	
65	00012 03 4d 08	add	ecx,	DWORD PTR _p1\$[ebp]	. MM. ~1 -1 ~ 0 1
66	00015 66 89 4d fc	mov	WORD	PTR _s1\$[ebp], cx	; MM: s1 is 2 by

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	tes (signed/unsingne	ed not known)		
67	00019 8b 55 f4	mov	edx, <b>DWORD PTR</b> _i1\$[ebp]	
68	0001c 03 55 0c	add	edx, <b>DWORD PTR</b> _p2\$[ebp]	
69	0001f 66 89 55 f8	mov	WORD PTR _s2\$[ebp], dx	; MM: s2 is 2 by
70	tes (signed/unsigned 00023 al 00 00 00		eax, <b>DWORD PTR</b> _x	
71	00028 83 e8 01	sub	eax, 1	
72	0002b a3 00 00 00	00 mov	DWORD PTR _x, eax	
73	00030 83 3d 00 00			
74	00 00	cmp	DWORD PTR _x, 0	
75 76	00037 7e 17 00039 0f b7 4d f8	jle movzx	SHORT \$LN2@f	; MM: s2 is uns
76	gned short	IIIOVZX	ecx, WORD PTR _s2\$[ebp]	; MM: SZ IS UNS
77	0003d 51	push	ecx	
78	0003e Of bf 55 fc	movsx	edx, WORD PTR _s1\$[ebp]	; MM: sl is sign
	ed short			
79	00042 52	push	edx	
80	00043 e8 00 00 00		_f	
81	00048 83 c4 08 0004b a3 00 00 00	add	esp, 8	
82 83	\$LN2@f:	00 mov	DWORD PTR _y, eax	
84	00050 Of bf 45 fc	movsx	eax, WORD PTR _s1\$[ebp]	
85	00054 03 45 f4	add	eax, DWORD PTR _i1\$[ebp]	
86	00057 Of b7 4d f8	movzx	ecx, WORD PTR _s2\$[ebp]	
87	0005b 03 05 00 00			
88	00	add	eax, <b>DWORD PTR</b> _y	
89	00061 03 c1 00063 8b e5	add	eax, ecx	
90 91	00065 5d	mov pop	esp, ebp ebp	
92	00066 c3	ret	0	
93	_f ENDP			
94	_TEXT ENDS			
95	END			