This repository | Search   Pull requests   Issues   Marketplace   Gist

📖 dmlc / **tvm**

⊙ Watch ▾ 54   ★ Star 213   ⑂ Fork 25

‹› Code   ⊙ Issues **3**   �ⵂ Pull requests **2**   ⊞ Projects **0**   ▤ Wiki   Insights ▾

End to end Tensor IR/DSL stack for deploying deep learning workloads to hardwares

compiler   tensor   deep-learning   dsl   gpu   opencl   metal   performance   javascript

ⓘ **373** commits	ⵂ **1** branch	🏷 **1** release	👥 **17** contributors	⚖ Apache-2.0

Branch: master ▾   New pull request   Create new file   Upload files   Find file   Clone or download ▾

👤 **Huyuwei** committed with **tqchen** update depthwise convolution api (#344)   Latest commit b5c6b99 Aug 17, 2017

📁 HalideIR @ f3eb854	[DOC] Release note (#340)	Aug 18, 2017
📁 apps	[FIX] Pass the attributes of master node (#304)	Aug 9, 2017
📁 cmake	use auto source_group (#146)	May 19, 2017
📁 dlpack @ 36e5738	[MODULE/RUNTIME] Remove Precompile, simplify module (#174)	Jun 6, 2017
📁 dmlc-core @ 7afa45b	[DOC] Fix doxygen comments (#247)	Jul 15, 2017
📁 docs	update depthwise convolution api (#344)	Aug 18, 2017
📁 include/tvm	[PASS] RewriteUnsafeSelect lowers unsafe select to condition expr (#335)	Aug 16, 2017
📁 jvm	[tvm4j] disable proxy test for now (#307)	Aug 11, 2017
📁 make	[Contrib] CuDNN v7 Support (#311)	Aug 16, 2017
📁 python	[PASS] RewriteUnsafeSelect lowers unsafe select to condition expr (#335)	Aug 16, 2017
📁 src	[PASS] RewriteUnsafeSelect lowers unsafe select to condition expr (#335)	Aug 16, 2017
📁 tests	[PASS] RewriteUnsafeSelect lowers unsafe select to condition expr (#335)	Aug 16, 2017
📁 topi	[WIP] [TOPI] Depth wise Conv for NHWC (#325)	Aug 16, 2017
📁 tutorials/python	Add tutorial for convolution in CUDA (#343)	Aug 18, 2017
📁 verilog	[CONTRIB/BLAS] Add CBLAS Example to contrib (#120)	May 6, 2017
📁 web	[SCHEDULE][RUNIME] Introduce pragma for additional extension hint, th...	Aug 6, 2017
📄 .gitignore	gitignore tags (#277)	Jul 27, 2017
📄 .gitmodules	[SUBMODULE] switch to https (#341)	Aug 18, 2017
📄 .travis.yml	Remove linux from travis (#156)	May 23, 2017
📄 CMakeLists.txt	Fix CUDA library search (#339)	Aug 18, 2017
📄 CONTRIBUTORS.md	[TUTORIAL] Optimize gemm on CPU add! (#270)	Jul 26, 2017
📄 Jenkinsfile	[BUILD] Enable cudnn in gpu build (#333)	Aug 16, 2017
📄 LICENSE	[DOC/LICENSE] Make doc and license consistent, opensource repo when w...	May 10, 2017
📄 Makefile	[Contrib] CuDNN v7 Support (#311)	Aug 16, 2017
📄 NEWS.md	[DOC] Release note (#340)	Aug 18, 2017
📄 README.md	[DOC] Add link to release blog (#342)	Aug 18, 2017

📖 **README.md**

TVM: Tensor IR Stack for Deep Learning Systems

`license` `Apache 2.0` `build` `passing`

[Installation](#) | [Documentation](#) | [Tutorials](#) | [Operator Inventory](#) | [FAQ](#) | [Contributors](#) | [Release Notes](#)

TVM is a Tensor intermediate representation(IR) stack for deep learning systems. It is designed to close the gap between the productivity-focused deep learning frameworks, and the performance- and efficiency-focused hardware backends. TVM works with deep learning frameworks to provide end to end compilation to different backends. Checkout our [announcement](#) for more details.

License

© Contributors, 2017. Licensed under an [Apache-2.0](#) license.

Contribute to TVM

TVM adopts apache committer model, we aim to create an open source project that is maintained and owned by the community.

- [Contributor Guide](#)
- Please add your name to [CONTRIBUTORS.md](#)
- Please also update [NEWS.md](#) on changes and improvements in API and codes.