

SK6013 Low Noise, High PSRR, High Speed, CMOS LDO

GENERAL DESCRIPTION

The SK6013 is a high accuracy, low noise, high speed, low dropout CMOS Linear regulator with high ripple rejection and fast discharge function. The device offers a new level of cost effective 63 devices.

SK6013 can provide product selections of output value in the range of 1.0V~3.6V by every 0.1V step.

The current limiter's fold-back circuit also operates as a short circuit protection and an output current limiter at the output pin.

The SK6013 regulators are zavailable in standard SOT23-5Land DFN1x1-4L packages. Standard products are Pb-free and Halogen-free.

FEATURES

Input voltage: 2.5V~6.5VOutput range: 1.0V~3.6V

(customized by every 0.1V step)

Maximum output current: 400mA@

 V_{IN} - V_{OUT} =0.5V

PSRR: 75dB @1KHz

Dropout voltage: 220mV @ I_{OUT}=200mA

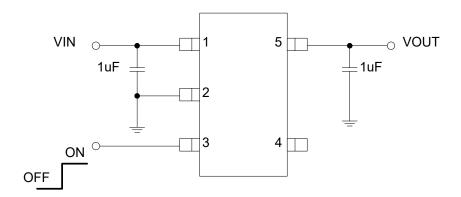
Quiescent current: 35μA Typ.
 Shut-down current: <1μA
 Recommend capacitor: 1μF

Ultra-low output noise: 20μV_{RMS}

APPLICATIONS

- Digital cameras
- Cellphones
- Bluetooth and wireless handsets
- Other portable electronic devices

TYPICAL APPLICATION CIRCUIT

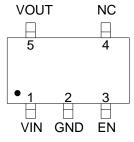




PIN ASSIGNMENT



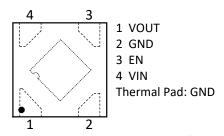
SOT23-5L



SOT23-5L (Top View)



DFN1x1-4L



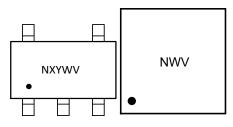
DFN1x1-4L (Top View)

PART NUMBER RULES

SK60131 2 3

Code	Description
1	V _{OUT} discharge
	" ": w/o fast discharge
	A/B: with fast discharge
2	Package:
	S5: SOT23-5L
	D4: DFN1x1-4L
3	Voltage version:
	XX: 1.0V~3.6V by 0.1V step
	Example:
	28: 2.8V

MARKING DESCRIPTION:



SOT23-5L DFN1x1-4L

"N": Product code
"X":Packagefactory

"Y":Wafer foundry vendor.

"W": The week of manufacturing. "A" stands for week1, "Z" stands for week 26, "a" stands for week 52.

"V": Output voltage code.



ORDERING INFORMATION

PART NO	PACAKGE	V _{OUT} DISCHARGE	TEMPERATURE	TAPE & REEL
SK6013S5-XX Note	SOT23-5L	No	-40 ~ +85℃	3000/REEL
SK6013BS5-XX Note	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013AD4-XX ^{Note}	DFN1x1-4L	Yes	-40 ~ +85℃	10000/REEL

Note:XX indicates $1.0V^{\sim}3.6V$ by 0.1V step. For example, 28 means product outputs 2.8V

PACKAGE INFORMATION

PART NO	PACAKGE	V _{OUT} DISCHARGE	TEMPERATURE	TAPE & REEL
SK6013S5-18	SOT23-5L	No	-40 ~ +85℃	3000/REEL
SK6013S5-28	SOT23-5L	No	-40 ~ +85℃	3000/REEL
SK6013S5-30	SOT23-5L	No	-40 ~ +85℃	3000/REEL
SK6013S5-33	SOT23-5L	No	-40 ~ +85℃	3000/REEL
SK6013BS5-12	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013BS5-15	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013BS5-18	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013BS5-25	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013BS5-28	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013BS5-30	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013BS5-33	SOT23-5L	Yes	-40 ~ +85℃	3000/REEL
SK6013AD4-12	DFN1x1-4L	Yes	-40 ~ +85℃	10000/REEL
SK6013AD4-18	DFN1x1-4L	Yes	-40 ~ +85℃	10000/REEL
SK6013AD4-25	DFN1x1-4L	Yes	-40 ~ +85℃	10000/REEL
SK6013AD4-28	DFN1x1-4L	Yes	-40 ~ +85℃	10000/REEL
SK6013AD4-30	DFN1x1-4L	Yes	-40~+85℃	10000/REEL
SK6013AD4-33	DFN1x1-4L	Yes	-40~+85℃	10000/REEL



PIN DESCRIPTION

PIN NO	SYMBOL	1/0	DESCRIPTION
SOT23-5L	STIVIBOL	I/O	DESCRIPTTION
1	VIN	Power	Input
2	GND	Ground Ground	
3	EN	I	Enable (active high, do not float)
4	NC	/	Not connected
5	VOUT	0	Output

PIN NO	SYMBOL	1/0	DESCRIPTTION		
DFN1x1-4L		1,0	DESCRIPTION		
1	VOUT	0	Output		
2	GND	Ground Ground			
3	EN	I	Enable (active high, do not float)		
4	VIN	Power	Input		

ABSOLUTE MAXIMUM RATINGS(Note)

SYMBOL	ITEMS		VALUE	UNIT	
V _{IN}	Input Voltage		-0.3~8	V	
I _{OUT}	Output Current	Output Current		mA	
	Power Dissipation SOT23-5L DFN1x1-4L	SOT23-5L	0.3	147	
P _{DMAX}		DFN1x1-4L	0.6	W	
T _J	Junction Temperature		-40~125	$^{\circ}\!\mathbb{C}$	
T _A	Ambient Temperature		-40~85	$^{\circ}$ C	
T _{STG}	Storage Temperature		-55 to 150	$^{\circ}$ C	
T _{SOLDER}	Package Lead Soldering 1	Temperature	260℃, 10s		

Note: Exceed these limits to damage to the device. Exposure to absolute maximum rating conditions may affect device reliability.

RECOMMANDED OPERATING RANGE

SYMBOL	ITEMS	VALUE	UNIT
V_{IN}	Supply Voltage	2.5to 6.5	٧
I _{OUT}	Output Current	<300	mA
T _{OPT}	Operating Temperature	-40 to +85	$^{\circ}$

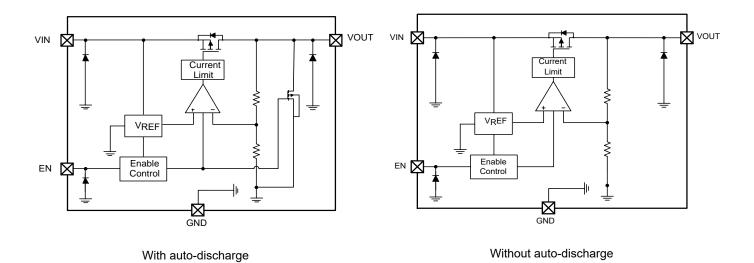


ELECTRICAL CHARACTERISTICS

The following specifications apply for $V_{OUT}\!=\!2.8VT_A\!=\!25^{\circ}\!\text{C}$, unless specified otherwise.

SYMBOL	ITEMS	CONDITIONS	MIN	TYP	MAX	UNIT
V _{IN}	Input Voltage				6.5	V
V	Outrot Barre	V _{OUT} <2V V _{IN} =2.7V, I _{OUT} =1mA	-3	V _{OUT}	3	0/
V _{OUT}	Output Range	V _{OUT} ≥2V, I _{OUT} =1mA	-2	V _{OUT}	2	- %
ΙQ	Quiescent Current	V _{OUT} =2.8V, I _{OUT} =0		35		μΑ
I _{LIMIT}	Current Limit	V _{IN} =V _{EN} =4.5V		500		mA
V	Duana, t Valtaga	V _{OUT} =2.8V, I _{OUT} =200mA		220	250	>/
V_{DROP}	Dropout Voltage	V _{OUT} =2.8V, I _{OUT} =300mA		320	350	mV
$\triangle V_{LINE}$	Line Regulation	V _{IN} =2.7~5.5V,I _{OUT} =1mA		0.01	0.15	%/V
$\triangle V_{LOAD}$	Load Regulation	V _{OUT} =2.8V, I _{OUT} =1~300mA		40	70	mV
I _{SHORT}	Short Current	$V_{EN}=V_{IN}$, V_{OUT} Short to GND with 1Ω		80		mA
I _{SHDN}	Shut-down Current	V _{EN} =0V			1	μΑ
		V _{IN} =5V _{DC} +0.5V _{P-P}		75		
PSRR	Power Supply Rejection	F=1KHz,I _{OUT} =10mA		75		- dB
PSKK	Rate	$V_{IN}=5V_{DC}+0.5V_{P-P}$				ив
		F=1MHz, I _{OUT} =10mA		55		
V_{ENH}	EN logic high voltage	V _{IN} =5.5V, I _{OUT} =1mA	1.2		V _{IN}	V
V _{ENL}	EN logic low voltage	V _{IN} =5.5V, V _{OUT} =0V			0.4	V
I _{EN}	EN Input Current	V _{EN} = 0 to 5.5V			1.0	μΑ
e _{NO}	Output Noise Voltage	10Hz to 100KHz, C _{OUT} =1μF		20		μV_{RMS}

SIMPLIFIED BLOCK DIAGRAM

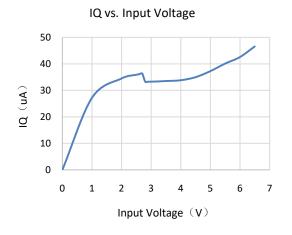


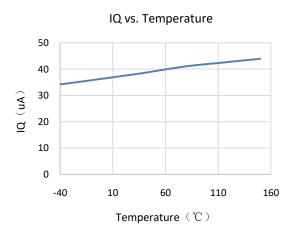
上海洺太电子科技有限公司

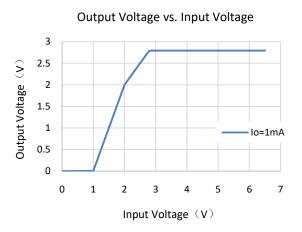


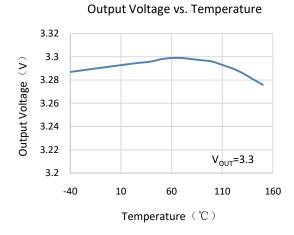
TYPICAL PERFORMANCE CHARACTERISTICS

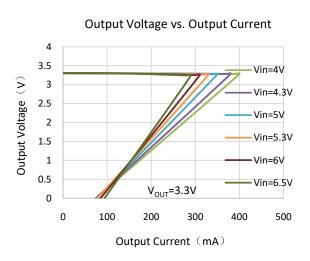
 $C_{IN}\!=\!1uF,\,C_{OUT}\!=\!1uF,\,V_{IN}\!=\!4.5V,\,V_{OUT}\!=\!2.8VT_{A}\!=\!25^{\circ}\!\text{C},$ unless specified otherwise.

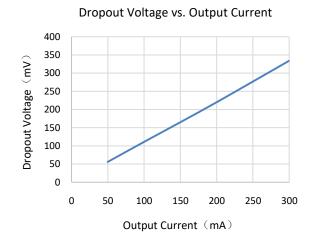




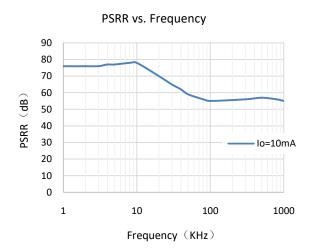


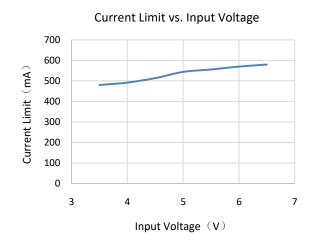




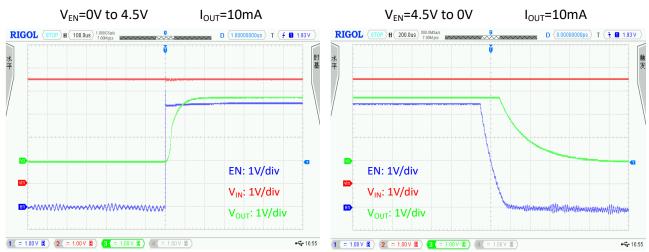




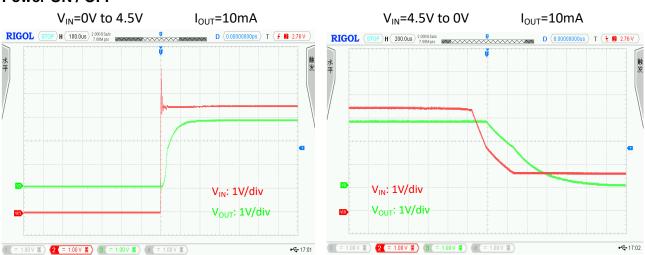




EN ON / OFF

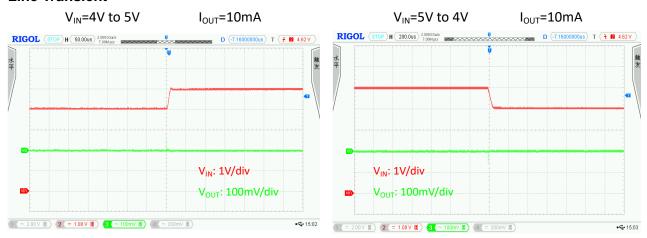


Power ON / OFF

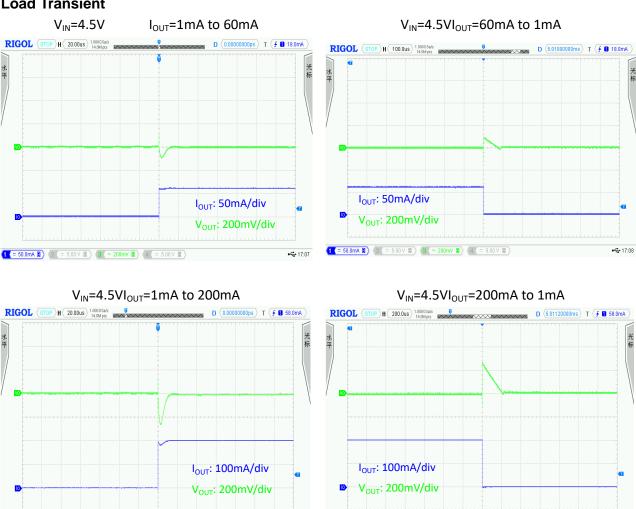




Line Transient



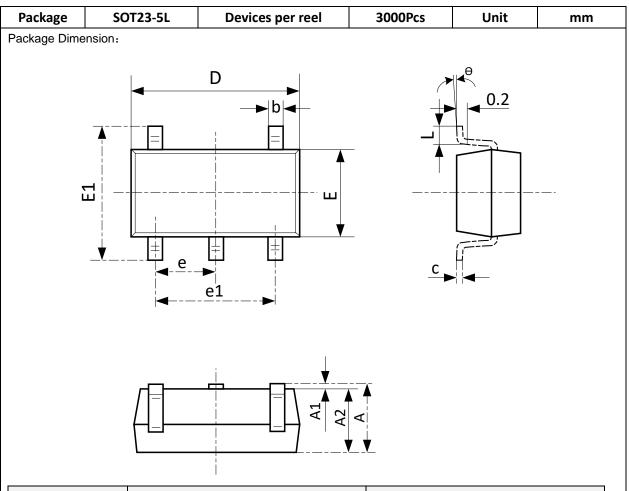
Load Transient



1 = 100mA 8 2 = 5.00 V 8 3 ~ 200mV 8 4 = 5.00 V 8 3



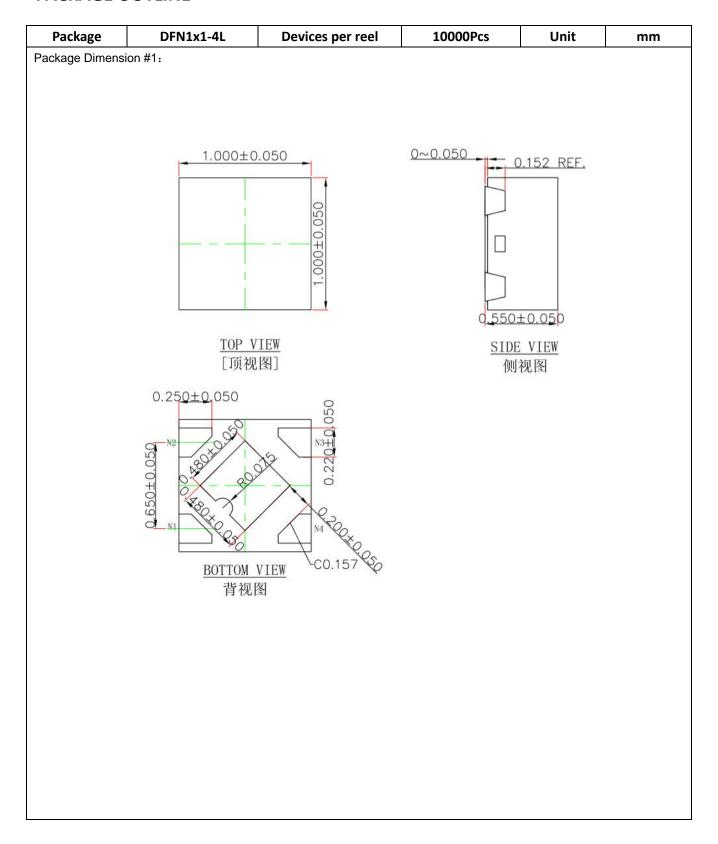
PACKAGE OUTLINE



Sumbal	Dimensions I	Dimensions In Millimeters		s In Inches
Symbol	Min	Max	Min	Max
А	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
С	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
Е	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
е	0.950	(BSC)	0.037	(BSC)
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0℃	8℃	0℃	8℃



PACKAGE OUTLINE





PACKAGE OUTLINE

Package	DFN1x1-4L	Devices per reel	10000	Pcs	Unit	m
ckage Dimensi	on #2:					
			λ I *	e 1 /		
	D	-	42		Op.	
		1			\rightarrow	
PIN 1 D						
BY MARK	KING	4		\rightarrow		
			-	$\prec \land$		
	W			\mathcal{X}		
			b			ENTIFICATIO
	TOP VIEW		рпттг	JM VIEV	CHAFMER	0.12MM
	IUI VILW				<u>M</u> ENSIONS(MM)	
			PKG.	>	1:EXTREME TH	
		٦	REF. A	MIN. 0.40	N□M. -	MAX 0,50
	T		A1	0,00	0.125REF	0.05
		<u> </u>	A3 D	0,95	1.00	1.05
	Z SIDE VIEV	A3+	E	0.95 0.15	1.00 0.20	1,05 0,25
	SIDE VIEW	<i>(</i>	L	0.15	0.25	0,35
RFM	IARK:		D2 E2	0,38 0,38	0.48 0.48	0.58 0.58
	LEAD FINISH: NI	PDAU	е		0.65 BSC	



Tape

