DC ANALYSIS OF CMOS INVERTER

SCHEMATIC OF CMOS INVERTER SIMULATED IN LTSPICE:

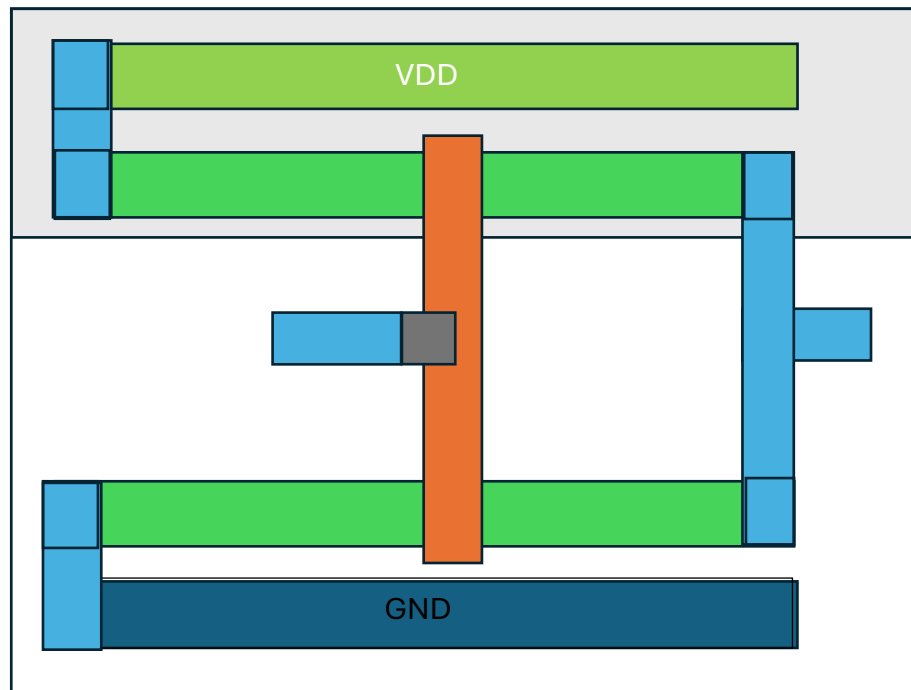**M2**
PMOS

V2

1.8

M1
NMOS

V1

.dc V1 0 1.8 0.1
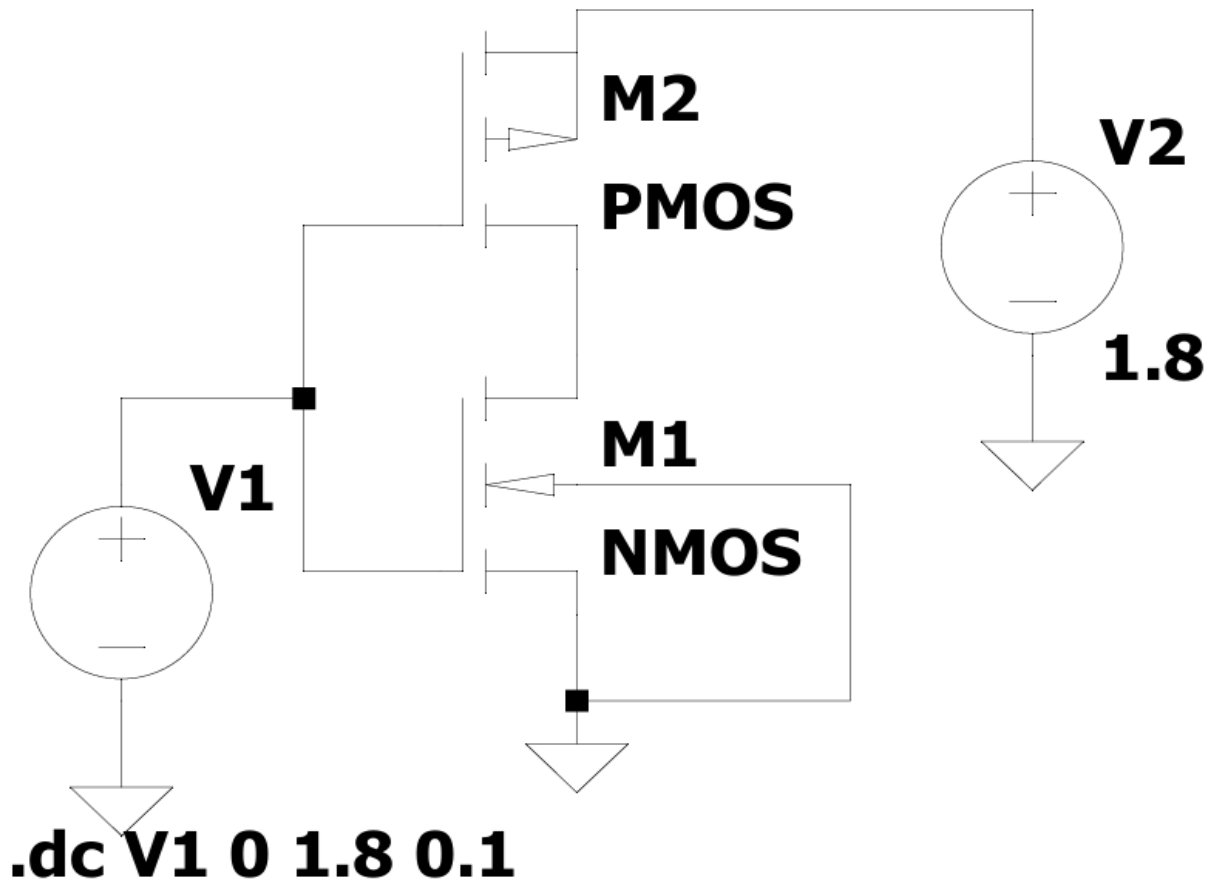
OUTPUT OF CMOS INVERTER IN LTSPICE:

--- C:\Users\User\OneDrive\Documents\LTspice\Draft2.raw ---
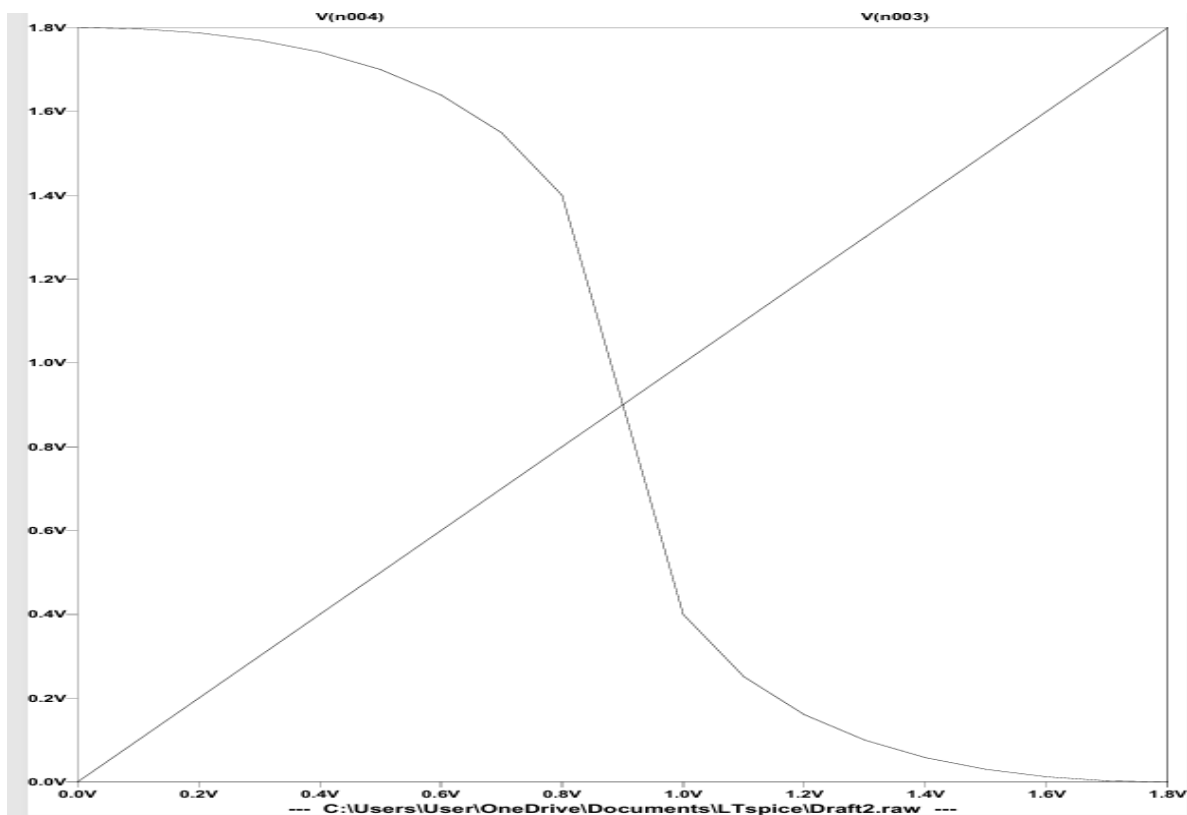
STICK DIAGRAM:

LAYOUT OF CMOS INVERTER TAKEN USING MAGIC VLSI OPENSOURCE TOOL: