Prototype Design

F2019 – Edit this document into a deliverable.

|  |  |  |  |
| --- | --- | --- | --- |
| Lab Section: | 5 | Group: | 16 |

# Necessary Changes and Notes

**Answer these questions by editing and adding to Table 1 and Table 2 below.**

**Table 1:** What changes had to be made to get your Feasibility Model working as expected?

Table 1: Necessary Design Changes

|  |  |  |
| --- | --- | --- |
| # | Change | Reason/Notes |
| 1 | No longer planning to use UART to send commands. Will only be using it in testing context. | Doesn’t offer any additional value over other main components of the project. |
| 2 | We were originally planning to use 2 separate boards, in order to have access to enough GPIO pins. Will only be using one board. | We now realize that we have enough pins on a single board. |
| 3 | No longer planning to use interrupts for button presses (mainly, limit switches). | We use sufficiently small steps, that tight polling the button statuses should be responsive enough for our needs. Moreover, this simplifies our code. |
| 4 | LEDs will not be connected to their own GPIO pin. Instead, they will be wired in-line with the limit switches. | Reduces usage of GPIO pins, allowing us to consolidate the design to a single board. Also, these LEDs should only be on when the button is pressed, therefore there is no need to control them independently from the limit switch push buttons. |
| 5 | Changed the input resistance to the limit LEDs from 300Ω to 200Ω. | Changed this value so that we use less resistors (because at 300Ω, we’d need to use three 100Ω resistors), whereas we only use two 100 Ω resistors now. This resistance still allows us to limit the input current within the nominal range acceptable to drive the LED. |

**Table 2:** Lessons Learned – Is there anything you want to remember so that you don’t make the same mistake again? Or, not waste time on something you already figured out?

Table 2: Important Notes

|  |  |
| --- | --- |
| # | Note |
| 1 | As per the suggestion of the TA conducting our Feasibility Model demo, we investigated using hall effect sensors as our limit switches. We found that these were functionally equivalent to the push buttons (but with inverted output), however they also required their output to be amplified to 3.3v, in order to properly read their statuses. This adds unnecessary complexity to our design. Additionally, upon investigating similar products that exist already, we found that they commonly use physical limit switches, similar to our design. |
| 2 | We should always remember to initialize the required pins before attempting to use them. It is best to initialize them with all the other initializers (at the top of the file), since we disable interrupts in this section. |
| 3 | We sample the keypad buttons multiple times, in order to reduce possible noise on these inputs. |
| 4 | We purposefully omit debouncing the limit switch push buttons because, in the event of noise, the steppers will only skip one movement due to this noise and continue normally afterwards. |

# Signal Specifications

**Answer these questions by editing and adding to Table 3 below.**

**Table 3:** For all the important signals in your Prototype:

* Name the signal
* State which signal property is important (voltage, frequency, rise time, etc.)
* State whether you need to include a Test Point (TP) on the PCB so you can probe the signal
* State which software mode will let you test the signal as indicated
  + You may need to create a special test mode in your code to exercise the signal to its limits
* State the Minimum (Min), Nominal, and Maximum (Max) acceptable values for that signal property, as appropriate
* Include signals for attached components, modules, sensors, etc. Do not include power rails.

Table 3: Hardware Signal Test Plan

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| Signal (TP\*) | Property | Required Software Mode | Min | Nominal | Max |
| X\_MIN\_TEST (X) | Voltage | Limit Switch test mode | 0 V | 3.3 V | 5 V |
| X\_MAX\_TEST (X) | Voltage | Limit Switch test mode | 0 V | 3.3 V | 5 V |
| Y\_MIN\_TEST (X) | Voltage | Limit Switch test mode | 0 V | 3.3 V | 5 V |
| Y\_MAX\_TEST (X) | Voltage | Limit Switch test mode | 0 V | 3.3 V | 5 V |
| Keypad COL 1 | Voltage | Keypad Column 1 test mode |  | 3.3 V |  |
| Keypad COL 2 | Voltage | Keypad Column 2 test mode |  | 3.3 V |  |
| Keypad COL 3 | Voltage | Keypad Column 3 test mode |  | 3.3 V |  |
| ROW\_1\_TEST (X) | Voltage | Keypad Column [1|2|3] test mode |  | 3.3 V |  |
| ROW\_2\_TEST (X) | Voltage | Keypad Column [1|2|3] test mode |  | 3.3 V |  |
| ROW\_3\_TEST (X) | Voltage | Keypad Column [1|2|3] test mode |  | 3.3 V |  |
| ROW\_4\_TEST (X) | Voltage | Keypad Column [1|2|3] test mode |  | 3.3 V |  |
| STEPPER\_1\_OUT\_TEST (X) | Voltage | Stepper test mode | 0 V |  | 5V |
| Stepper 1 IN 2 | Voltage | Stepper test mode | 0 V |  | 5V |
| Stepper 1 IN 3 | Voltage | Stepper test mode | 0 V |  | 5V |
| Stepper 1 IN 4 | Voltage | Stepper test mode | 0 V |  | 5V |
| STEPPER\_2\_OUT\_TEST (X) | Voltage | Stepper test mode | 0 V |  | 5V |
| Stepper 2 IN 2 | Voltage | Stepper test mode | 0 V |  | 5V |
| Stepper 2 IN 3 | Voltage | Stepper test mode | 0 V |  | 5V |
| Stepper 2 IN 4 | Voltage | Stepper test mode | 0 V |  | 5V |

\*Indicates Test Point Required

Notes: Signals requiring a test point are labelled in the table above using the test point’s signal name and have a (X) next to them. Signals not requiring a test point are labelled using their regular signal name (from table below). We will only be testing Stepper out 1, as all 4 stepper driver signals are functionally equivalent (they alternate).

# Signal Mapping

**Answer these questions by editing and adding to Table 4 below.**

**Table 4:** How will your Prototype design electrically connect to the LaunchPad?

MSP430FR4133 IC pin <--> BoosterPack pin on J1/J2 of the LaunchPad <--> Your Prototype

Table 4: Hardware Signal Connectivity

|  |  |  |  |
| --- | --- | --- | --- |
| Signal | MSP430FR4133 Pin | LaunchPad J1/J2 Pin | Prototype Connection |
| Digital Out | P8.1 | J1 pin 2 | Stepper 1 IN 1 |
| Digital Out | P1.1 | J1 pin 3 | Stepper 1 IN 2 |
| Digital Out | P1.0 | J1 pin 4 | Stepper 1 IN 3 |
| Digital Out | P2.7 | J1 pin 5 | Stepper 1 IN 4 |
| Digital Out | P8.0 | J1 pin 6 | Stepper 2 IN 1 |
| Digital Out | P5.1 | J1 pin 7 | Stepper 2 IN 2 |
| Digital Out | P2.5 | J1 pin 8 | Stepper 2 IN 3 |
| Digital Out | P8.2 | J1 pin 9 | Stepper 2 IN 4 |
| Digital Out | P8.3 | J1 pin 10 | Keypad COL 2 |
|  |  |  |  |
| Digital In | P1.5 | J2 pin 1 | Keypad ROW 2 |
| Digital In | P1.4 | J2 pin 2 | Keypad ROW 3 |
| Digital Out | P1.3 | J2 pin 3 | Keypad COL 3 |
| Digital In | P5.3 | J2 pin 4 | Keypad ROW 4 |
| Digital In | P5.2 | J2 pin 5 | Y Axis MIN |
| Digital In | P5.0 | J2 pin 7 | Y Axis MAX |
| Digital In | P1.6 | J2 pin 8 | X Axis MIN |
| Digital In | P1.7 | J2 pin 9 | X Axis MAX |
|  |  |  |  |
| Digital Out | P2.6 | J3 pin 1 | Keypad COL 1 |
| Digital In | P4.0 | J3 pin 2 | Keypad ROW 1 |