Embedded Linux Code

1.0

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## **Chapter 1**

# Main Page

### 1.1 Introduction

The software program "focserver" implements a web server and a websocket server in the Field-Oriented Control (FOC) system, developed with Xilinx SDSoC tools.

### 1.2 Synopsis

### Command line:

See the Table 1.1 for the detailed description of command line options.

**Table 1.1 Command line options** 

Option	Description
-c filename	Capture ADC data and write it to a file, don't start the server
-D	Start the server as a daemon
-d bitfield	Set Libwebsocket debug log bitfield. Example values: 0 log nothing, 255: log everything
-f filepath	Use the given configuration file
-h	Show this text
-р	Print values of all registers, don't start the server
-s[speed]	Start the motor. The speed (in RPM) is optional
-V	Print version information and exit
-t	Test flag
-w reg=val	Write the value to the register, don't start the server
-W directory	Document root directory for the web server

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Executing "focserver" will start it in server mode. Unless the option -*c*, -*v* or -*w* was supplied on the command line, the program "focserver" will perform as follows:

- 1. Read the configuration file; see section Configuration file for the format and location.
- Open the hardware devices; see section Requirements for the Linux operating system for the devices required.
- 3. Start the internal web server. Default document root directory is "/usr/share/focserver".
- 4. Blink the heartbeat LED LD3 on the Arty Z7 board once per second; this features is not available in the SDSoC FOC project.

### 1.3 Configuration file

The configuration file is in JSON format and contains just one JSON object with FOC parameters and initialization values for the parameter registers. See the Table 1.2 for the list of supported fields. The complete list of parameter register names can be found in the the *Network API*, table "Parameter registers".

The default path for the configuration file is "/etc/focserver.conf".

Table 1.2 Fields in the configuration file

Field	Description	Default value
ppr	Pulses per revolution	1000
adc2A	Conversion factor from ADC samples to amperes 0.00039	
pwm2V	Conversion factor from PWM duty cycle to volts 0.0003662	
init	Parameter register values to be written during initialization See the Table 1.3	
speed	Parameter register values to be written before starting the motor in a speed control loop	
torque	Parameter register values to be written before starting the motor in a current control loop	

Table 1.3 The default initialization values

Step #	Name of SDSoC FOC register	Value
1	FluxSp	0
2	FluxKp	-4096
3	FluxKi	0
4	RPMSp	3000
5	RPMKp	0
6	RPMKi	-10
7	TorqueSp	1000
8	TorqueKp	5000
9	TorqueKi	0
10	Shift	719
11	Vd	-7424
12	Vq	-16128
13	Fa	18120
14	Fb	14647
15	Control2	10

The configuration file as used in the SDSoC FOC design:

```
"init" : {
         "FluxSp" : 0,
         "FluxKp" : -4096,
         "FluxKi" : 0,
         "RPMSp" : 3000,
"RPMKp" : -200,
         "RPMKi" : -5,
"Shift" : 719,
         "Vd" : -7424,
         "Vq" : -16128,
         "Fa" : 18120,
         "Fb" : 14647,
         "Mode" : 0,
         "FixedDelay" : 20
    "speed" : {
         "TorqueSp" : 0,
         "TorqueKp" : 5000,
         "TorqueKi" : 0
    "torque" : {
         "TorqueKp" : -20000,
         "TorqueKi" : -5000
    "ppr" : 1000,
    "adc2A" : 0.00039,
"pwm2V" : 0.0003662
}
```

### 1.4 Requirements for the Linux operating system

The program "focserver" expects the following hardware to be available on the Linux system:

- 1. The capture device IP core as the UIO device "AXI-Data-Capture".
- 2. The FOC IP core, either through the UIO device named "foc" (HLS FOC project) or the name in the device tree must have the prefix "xlnx,foc-" (SDSoC FOC project).

For the reference, following are the device tree overrides as used in SDSoC FOC design:

```
&AXI_StreamCapture_0 {
    compatible = "trenz.biz,smartio-1.0";
    trenz.biz,name = "AXI-Data-Capture";
    trenz.biz,buffer-size = <0x400000>;
    trenz.biz,sample-rate = <78125>;
    xlnx,cdata-width = <16>;
    xlnx,channels = <4>;
};
```

In order to fulfill the above conditions automatically, it is recommended to use the Petalinux project provided in the IIoT-EDDP repository as a basis as follows:

- 1. In the Vivado SDx IDE: Create and build an SDSoC application based on the SDSoC platform and utilizing the FOC IP core.
- 2. Locate the Hardware Definition File (\*.hdf) and copy it to the root of your copy of the Petalinux project.

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3. On the Linux command line and in the Petalinux project directory, run the following command: petalinux-config –get-hw-description

This will update the Petalinux project and the device tree with the latest hardware information.

- 4. Build your petalinux project.
- 5. Copy the files "images/linux/image.ub" and "images/linux/u-boot.elf" to the SDSoC platform, overriding existing files.
- 6. Rebuild your SDSoC application for the changes to be effective.

Important: Using the Hardware Definition File (\*.hdf) from the platform Vivado project will not work, because it doesn't have the necessary information for the device tree.

### 1.5 Startup script "focinit"

A startup script named "focserver" is provided for use in the Petalinux project for TEC0053.

At the Linux startup, this script executes as follows:

- 1. Mount the SD card temporarily in order to execute the script named "init.sh" on it if found.
- 2. Start the FOC server if not started by the the script "init.sh" beforehand.
- 3. Wait 10 seconds before setting the IP address to the default of 192.168.42.123
- 4. Start the FOC server if the file "init.sh" was not found on the SD card.

### 1.6 Building from the source

By including "focserver" in a Petalinux project it will be automatically rebuilt from the source as needed.

To regenerate the Doxygen documentation, run the script "run doxygen.bat".

### 1.7 Tools

The tools required are listed in the Table 1.4. For the documentation Doxygen is used; the documentation is generated from the doxygen-formatted comments in the the source code files.

Table 1.4 Tools

Tool	Version	Notes
Xilinx SDK	2017.1	Development environment for developing bare-metal and Linux software
PetaLinux	2017.1	Xilinx tool for building embedded Linux systems
Doxygen	1.8.11	Documentation extraction
MiKTeX	2.9	PDF generation

## Chapter 2

## **Class Index**

### 2.1 Class List

Here are the classes, structs, unions and interfaces with brief descriptions:

FocServer::BinaryHeader	
Layout of the binary header	9
DeviceTreeDevice	
Fetch information from the Linux Device Tree	9
FocConfiguration	
Configuration of the FOC server	14
FocDevice	
Access to the FOC IP core	16
FocServer	
FOC server implementing the Network API and a web server, which permits control and monitor	
of the FOC system from the Web UI	30
FocConfiguration::ParameterValue	
Value of a parameter in the configuration file	32
FocDevice::RegisterAccess	
Description of access to a register in a register bank	33
WebsocketBuffer	
Write buffer, consisting of a queue of the messages to be written and a write buffer for the	
libwebsockets	34

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# **Chapter 3**

# File Index

### 3.1 File List

Here is a list of all documented files with brief descriptions:

main.cpp
Implementation of the main function of the focserver
src/DeviceTreeDevice.cpp
src/DeviceTreeDevice.h
Implementation of the class DeviceTreeDevice
src/FocConfiguration.cpp
Implementation of the class FocConfiguration
src/FocConfiguration.h
Interface of the class FocConfiguration
src/FocDevice.cpp
Implementation of the class FocDevice
src/FocDevice.h
Interface of the class FocDevice
src/FocServer.cpp
Implementation of the class FocServer
src/FocServer.h
Interface of the class FocServer
src/focserver_main.cpp
Implementation of the function focserver_main
src/focserver_main.h
Declaration of the function focserver_main
src/Version.h
Version information
src/WebsocketBuffer.cpp
Implementation of the class WebsocketBuffer
src/WebsocketBuffer.h
Interface of the class WebsocketBuffer

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## **Chapter 4**

## **Class Documentation**

### 4.1 FocServer::BinaryHeader Struct Reference

Layout of the binary header.

```
#include <FocServer.h>
```

### **Public Attributes**

• uint16\_t nchannels

Bytes 0..1: Number of channels.

• uint16\_t nsamples

Bytes 2..3: Number of samples.

• uint32\_t sample\_rate

Bytes 4..7: Sample rate.

• uint8\_t name [BINARY\_HEADER\_SIZE - 2u - 2u - 4u]

Name of the data.

### 4.1.1 Detailed Description

Layout of the binary header.

Definition at line 84 of file FocServer.h.

The documentation for this struct was generated from the following file:

• src/FocServer.h

### 4.2 DeviceTreeDevice Class Reference

Fetch information from the Linux Device Tree.

```
#include <DeviceTreeDevice.h>
```

### **Public Member Functions**

DeviceTreeDevice (const std::string &pDeviceDirectoryPath, const std::string &pName, const std::string &p
 — Compatible, const uintptr\_t pAddress, const unsigned int pLength)

Create new object for fetching data for the given device.

- int readUInt32Array (uint32\_t \*value, const unsigned int nValues, const char \*propertyName) const Read one or more UInt32-s.
- int readUInt32 (uint32\_t &value, const char \*propertyName) const

Read property as unsigned 32-bit integer.

### **Static Public Member Functions**

static std::shared\_ptr< DeviceTreeDevice > findByProperty (const char \*propertyName, const char \*propertyValue)

Find the device by the given property value.

· static void demo ()

Small demo program of the capabilities, specific to ARTY-Z7 FOC project.

### **Public Attributes**

const std::string deviceDirectoryPath

Path to the device directory in the device tree.

· const std::string name

Name.

const std::string compatible

Compatible string.

· const uintptr t address

HW-address.

· const unsigned int length

Length of the memory area that can be mapped.

### **Static Public Attributes**

• static const char \*const PROPERTY\_COMPATIBLE = "compatible"

Name of the compatible property: "compatible".

• static const char \*const PROPERTY\_TRENZ\_BIZ\_NAME = "trenz.biz,name"

Name of the name property: "trenz.biz,name".

### 4.2.1 Detailed Description

Fetch information from the Linux Device Tree.

Definition at line 43 of file DeviceTreeDevice.h.

### 4.2.2 Constructor & Destructor Documentation

### 4.2.2.1 DeviceTreeDevice()

Create new object for fetching data for the given device.

Normally, this should not be called directly.

#### **Parameters**

pDeviceDirectoryPath	Absolute path to the device in the device tree.
pName	Name of the device.
pCompatible	Value of the device tree property "compatible".
pAddress	First value in the device tree property "reg".
pLength	Second value in the device tree property "reg".

Definition at line 104 of file DeviceTreeDevice.cpp.

### 4.2.3 Member Function Documentation

### 4.2.3.1 findByProperty()

Find the device by the given property value.

This doesn't throw exceptions, just returns empty shared\_ptr on errors.

#### **Parameters**

propertyName	Name of the property to search for.
propertyValue	Value of the property to search for.

### Returns

Smart pointer to the device; in the case none found, the pointer will be empty.

Definition at line 115 of file DeviceTreeDevice.cpp.

```
116 {
117
         const unsigned int value_length = strlen(propertyValue);
118
119
         // Scan the device tree directory for files.
120
         DIR*
                 dir = opendir(DEVICE_TREE_DIR);
         if (dir == nullptr) {
121
122
              return std::shared_ptr<DeviceTreeDevice>();
123
124
125
         std::string device_dir;
126
         std::string p_value;
127
         std::string p_name;
128
         uint32_t
                      p_reg[2];
129
130
         struct stat st;
131
         for (struct dirent* ent=readdir(dir); ent!=nullptr; ent=readdir(dir)) {
    ssprintf(device_dir, "%s/%s", DEVICE_TREE_DIR, ent->d_name);
132
133
134
135
              // Must be directory.
136
              if (stat(device_dir.c_str(), &st) != 0 || (st.st_mode & S_IFDIR) == 0) {
137
138
       // Compatible string must match.
   if (read_all_text(p_value, device_dir, propertyName) <= 0 || strncmp(p_value.c_str(),
propertyValue, std::min<unsigned int>(value_length, p_value.size())) != 0) {
139
140
141
142
              // Parameters must be readable.
143
              if (read_all_text(p_name, device_dir, "name") <= 0</pre>
144
                     || read_uint32_array(p_reg, sizeof(p_reg), device_dir, "reg") < 2) {</pre>
145
146
                   continue;
147
148
              closedir(dir);
             std::string p_compatible;
if (strcmp(propertyName, PROPERTY_COMPATIBLE) == 0) {
149
150
151
                  p_compatible = propertyValue;
152
153
              else {
154
                  read_all_text(p_compatible, device_dir, PROPERTY_COMPATIBLE);
155
              return std::make_shared<DeviceTreeDevice>(device_dir, p_name, p_compatible, p_reg[0], p_reg[1]);
156
157
         closedir(dir);
158
159
160
         // Nothing found :(
161
         return std::shared_ptr<DeviceTreeDevice>();
162 }
```

### 4.2.3.2 readUInt32()

Read property as unsigned 32-bit integer.

### **Parameters**

value	Buffer to store the value read.
propertyName	Name of the property to read the value from.

### Returns

1 on success, 0 when the property doesn't contain enough data, -1 on failure.

Definition at line 171 of file DeviceTreeDevice.cpp.

```
172 {
173          return read_uint32_array(&value, 1, deviceDirectoryPath, propertyName);
174 }
```

### 4.2.3.3 readUInt32Array()

Read one or more UInt32-s.

### **Parameters**

value	Buffer to store values read.
nValues	Number of values to be read.
propertyName	Name of the property to read values from.

### Returns

Number of values read, or -1 on failure.

Definition at line 165 of file DeviceTreeDevice.cpp.

```
166 {
167          return read_uint32_array(value, nValues, deviceDirectoryPath, propertyName);
168 }
```

The documentation for this class was generated from the following files:

- src/DeviceTreeDevice.h
- src/DeviceTreeDevice.cpp

### 4.3 FocConfiguration Class Reference

### Configuration of the FOC server.

```
#include <FocConfiguration.h>
```

### Classes

struct ParameterValue

Value of a parameter in the configuration file.

### **Public Member Functions**

• FocConfiguration ()

Create new configuration with default values.

• FocConfiguration (const std::string &jsonString)

Construct configuration from a JSON string.

void dump ()

Dump configuration to standard output.

### **Static Public Member Functions**

static std::shared\_ptr< FocConfiguration > fromFile (const std::string &filepath)
 Load configuration from a file.

### **Public Attributes**

unsigned int ppr

Pulses per revolution. 0 when undetermined.

double adc2A

Conversion factor from ADC units to mA.

double pwm2V

Conversion factor from PWM factors to voltages.

std::vector< ParameterValue > init

Initialization sequence.

• std::vector< ParameterValue > speed

Sequence for changing to the speed mode.

std::vector< ParameterValue > torque

Sequence for changing to the torque mode.

### **Static Public Attributes**

static constexpr int INDEX\_NOT\_KNOWN\_YET = -1

The index corresponding to the name is not known yet.

• static constexpr int INDEX\_INVALID\_NAME = -2

The name of the ParameterValue was invalid and no index can be determined.

static constexpr const char \* FILENAME = "/etc/focserver.conf"

Default name for the configuration file.

• static constexpr double DEFAULT\_ADC2A = 0.00039

Default value for adc2A.

• static constexpr double DEFAULT\_PWM2V = 0.0003662

Default value for pwm2V.

### 4.3.1 Detailed Description

Configuration of the FOC server.

Definition at line 34 of file FocConfiguration.h.

### 4.3.2 Constructor & Destructor Documentation

### 4.3.2.1 FocConfiguration()

Construct configuration from a JSON string.

Throws an exception when the JSON string is faulty.

### **Parameters**

```
jsonString | String in the JSON format.
```

Definition at line 96 of file FocConfiguration.cpp.

```
97 {
        if (jsonString.size() == 0u) {
98
            throw std::runtime_error("Empty configuration not permitted");
100
101
         Json::Reader reader;
102
        Json::Value
103
         if (!reader.parse(&jsonString[0], &jsonString[0] + jsonString.size(), root)) {
104
             throw std::runtime_error("Invalid JSON");
105
107
        // Load the values from JSONCPP.
108
         ppr = root.get(NAME_PPR, PPR).asInt();
adc2A = root.get(NAME_ADC2A, DEFAULT_ADC2A).asDouble();
pwm2V = root.get(NAME_PWM2V, DEFAULT_PWM2V).asDouble();
109
110
111
112
         load_params(init, root, NAME_INIT);
113
         load_params(speed, root, NAME_SPEED);
114
         load_params(torque, root, NAME_TORQUE);
115 }
```

### 4.3.3 Member Function Documentation

### 4.3.3.1 fromFile()

Load configuration from a file.

Throws exceptions when the file is faulty or non-existent.

### **Parameters**

filepath	Path to the file to be read.
----------	------------------------------

### Returns

Smart pointer to the configuration.

Definition at line 118 of file FocConfiguration.cpp.

```
119 {
120     std::string s = File::readAllText(filepath);
121     return std::make_shared<FocConfiguration>(s);
122 }
```

The documentation for this class was generated from the following files:

- src/FocConfiguration.h
- src/FocConfiguration.cpp

### 4.4 FocDevice Class Reference

Access to the FOC IP core.

```
#include <FocDevice.h>
```

### Classes

• struct RegisterAccess

Description of access to a register in a register bank.

### **Public Types**

 enum PSEUDO\_PARAMETER: unsigned int { MODE = PSEUDO\_PARAMETER\_OFFSET, FIXED\_PERIOD, SPREAD\_SPECTRUM }

Pseudo register indices.

• enum RegisterType : uint32\_t { RegisterType::INT32, RegisterType::UINT32 }

Type of a register.

### **Public Member Functions**

FocDevice (std::shared\_ptr< FocConfiguration > pConfig)

Create new FOC device object.

· FocDevice ()

Create new FOC device object with the default configuration.

· uintptr\_t getBaseAddress () const

Get the base address.

void writeParameter (const unsigned int parameterIndex, const uint32\_t parameterValue)

Write parameter register.

• uint32\_t readParameter (const unsigned int parameterIndex)

Read parameter register.

void readParameterString (std::string &buffer, const unsigned int parameterIndex)

String representation of the parameter register in the following format: NAME CONVERTED\_VALUE REGISTER\_← VALUE.

• uint32\_t readStatus (const unsigned int statusIndex)

Read status register.

void readStatusString (std::string &buffer, const unsigned int statusIndex)

String representation of the status register in the following format: NAME VALUE VALUE.

• void defaultInit ()

Perform default initialization.

void startMotor (const unsigned int mode, smart::hw::AxiDataCapture \*capture)

Start the motor in the given mode.

void stopMotor ()

Stop the motor.

void writeCaptureSource (const unsigned int sourceIndex)

Set new capture source.

· unsigned int readCaptureSource () const

Read the capture source index.

• unsigned int readLeds ()

Read LED-s state.

• void resetError ()

Reset the error flag of the speed monitor.

void writeLeds (const uint32\_t leds)

Write led state.

void writeErrorLimit (const unsigned int error\_limit)

Write error limit.

unsigned int readErrorLimit ()

Read the test error limit.

• void writeDecimate (const unsigned int decimationFactor)

Write decimation factor (number of samples to skip for every sample captured).

• unsigned int readDecimate () const

Read decimation factor.

void writeSpreadSpectrum (const bool enableSpreadSpectrum)

Write the spread spectrum flag.

bool readSpreadSpectrum ()

Read the spread spectrum flag.

### **Public Attributes**

• const char \* designName

Name of the HW design the software is running on.

• std::shared\_ptr< FocConfiguration > config

Configuration. This will be created anew if not existing.

· unsigned int parameterCount

Number of parameter registers.

const RegisterAccess \* parameterRegisters

List of the known parameter registers. End marker: nullptr as name.

· unsigned int statusCount

Number of status registers.

• const RegisterAccess \* statusRegisters

List of the known status registers. End marker: nullptr as name.

### **Static Public Attributes**

static const char \*const NAME SDSOC = "SDSoC"

Name of the SDSoC design, constant string "SDSoc".

• static const char \*const NAME\_HLS = "HLS"

Name of the HLS design, constant string "HLS".

• static const char \*const NAME\_UNKNOWN = "Unknown"

Name of an unknown design.

• static constexpr unsigned int PSEUDO\_PARAMETER\_OFFSET = 16u

Offset to the pseudo registers.

### 4.4.1 Detailed Description

Access to the FOC IP core.

Example code:

```
FocDevice dev;
dev.writeParameter(RPM_SP_REG, 1000);
dev.startMotor(CONTROL_SPEED);
```

Definition at line 49 of file FocDevice.h.

### 4.4.2 Member Enumeration Documentation

### 4.4.2.1 PSEUDO\_PARAMETER

```
enum FocDevice::PSEUDO_PARAMETER : unsigned int
```

Pseudo register indices.

This is common for both parameters and status registers.

### Enumerator

MODE	Operating mode of the FOC.
FIXED_PERIOD	Fixed speed increment.
SPREAD_SPECTRUM	Spread spectrum register.

Definition at line 65 of file FocDevice.h.

```
: unsigned int {
    /// Operating mode of the FOC.
    MODE = PSEUDO_PARAMETER_OFFSET,
    /// Fixed speed increment.
    FIXED_PERIOD,
    /// Spread spectrum register.
    SPREAD_SPECTRUM,
};
```

### 4.4.2.2 RegisterType

```
enum FocDevice::RegisterType : uint32_t [strong]
```

Type of a register.

### Enumerator

INT32	Signed 32-bit integer.
UINT32	Unsigned 32-bit integer.

Definition at line 76 of file FocDevice.h.

### 4.4.3 Constructor & Destructor Documentation

### 4.4.3.1 FocDevice()

Create new FOC device object.

Setup the default configuration.

Definition at line 121 of file FocDevice.cpp.

```
122 : designName(NAME_UNKNOWN),
      config(pConfig),
123
124
      parameterCount(0),
125
      parameterRegisters(parameter_registers),
      statusCount(0),
statusRegisters(status_registers),
126
127
128
      _parameter_registers_offset(0x10),
      _status_registers_offset(0x20)
129
130 {
131
        // Are we running on SDSoC or HLS?
         _sdsoc_info = DeviceTreeDevice::findByProperty(
132
      DeviceTreeDevice::PROPERTY_COMPATIBLE, FOC_COMPATIBLE_DEVICE_PREFIX);
133
        if (sdsoc info) {
134
            designName = NAME_SDSOC;
135
            _sdsoc_device = std::unique_ptr<smart::MappedFile>(new smart::MappedFile(FILENAME_DEV_MEM,
      _sdsoc_info->address, MappedFile::pageSize()));
            _registers = _sdsoc_device.get();
_hw_address = _sdsoc_info->address;
136
137
138
139
        else {
            designName = NAME_HLS;
140
141
            _hls_device = std::unique_ptr<smart::UioDevice>(new smart::UioDevice(UIO_FOC_DEVICE_NAME));
            _registers = _hls_device->getRequiredMap(0);
_hw_address = _hls_device->maps[0].addr;
142
143
144
145
146
        unsigned int i;
147
148
        for (i=0; parameterRegisters[i].name!=nullptr; ++i) {
149
150
        parameterCount = i;
151
152
        for (i=0; statusRegisters[i].name!=nullptr; ++i) {
153
154
        statusCount = i;
155
156
        /// Setup the default configuration.
157
        if (!config) {
158
            config = std::make_shared<FocConfiguration>();
            add_parameter_value(config->init, parameterRegisters, CONTROL_REG, 0);
159
      // Motor OFF
160
            add_parameter_value(config->init, parameterRegisters,
      PSEUDO PARAMETER::FIXED PERIOD. 50):
                                                      // Reasonably slow rotation.
161
            add_parameter_value(config->init, parameterRegisters, FLUX_SP_REG, 0);
      // Flux Sp = 0
162
            add_parameter_value(config->init, parameterRegisters, FLUX_KP_REG, 0
      xFFFFF000); // Flux Kp = -4096
163
            add_parameter_value(config->init, parameterRegisters, FLUX_KI_REG, 0);
      // Flux Ki = 0
164
            add_parameter_value(config->init, parameterRegisters, TORQUE_SP_REG, 0);
      // Torque Sp (used only in debug modes)
            add_parameter_value(config->init, parameterRegisters, TORQUE_KP_REG, 5000);
165
      // Torque Kp = 1.0
166
            add_parameter_value(config->init, parameterRegisters, TORQUE_KI_REG, 0);
      // Torque Ki = 0
167
            add parameter value (config->init, parameterRegisters, RPM SP REG, 3000);
      // Speed Sp = 3000 RPM
168
            add_parameter_value(config->init, parameterRegisters, RPM_KP_REG, -200);
      // Speed Kp = 2.88
169
            add_parameter_value(config->init, parameterRegisters, RPM_KI_REG, -5);
      // Speed Ki
170
            add_parameter_value(config->init, parameterRegisters, ANGLE_SH_REG, 719);
      // Angle between encoder index and Phase A
            add_parameter_value(config->init, parameterRegisters, VD_REG, 0xFFFFE300);
171
      // Vd (used only in debug modes)
172
            add_parameter_value(config->init, parameterRegisters, VQ_REG, 0xFFFFc100);
      // Vq (used only in debug modes)
173
            add_parameter_value(config->init, parameterRegisters, FA_REG, 18120);
      // Filter coefficient A = 0.553
174
            add_parameter_value(config->init, parameterRegisters, FB_REG, 14647);
      // Filter coefficient A = 0.447
175
176
             // The last registers already have suitable default values.
            add_parameter_value(config->init, parameterRegisters, CONTROL2_REG,
177
      CONTROL2 BV RESET ERROR);
178
            add_parameter_value(config->init, parameterRegisters, CONTROL2_REG, 100u <<
       CONTROL2_BIT_ERROR_LIMIT);
179
180
            add_parameter_value(config->speed, parameterRegisters, TORQUE_SP_REG, 0);
            add_parameter_value(config->speed, parameterRegisters, TORQUE_KP_REG, 5000)
181
182
            add_parameter_value(config->speed, parameterRegisters, TORQUE_KI_REG, 0);
183
184
            add_parameter_value(config->torque, parameterRegisters, TORQUE_KP_REG, -200
      00);
185
            add_parameter_value(config->torque, parameterRegisters, TORQUE_KI_REG, -500
      0);
```

```
186 }
187 }
```

### 4.4.4 Member Function Documentation

### 4.4.4.1 defaultInit()

```
void FocDevice::defaultInit ( )
```

Perform default initialization.

This does not start the motor.

Definition at line 314 of file FocDevice.cpp.

```
315 {
316     write_parameter(CONTROL_REG, 0);
317     if (config) {
318          writeParameterValues(config->init);
319     }
320 }
```

### 4.4.4.2 readCaptureSource()

```
unsigned int FocDevice::readCaptureSource ( ) const
```

Read the capture source index.

Returns

Capture source index.

Definition at line 422 of file FocDevice.cpp.

### 4.4.4.3 readDecimate()

```
unsigned int FocDevice::readDecimate ( ) const
```

Read decimation factor.

Returns

The current decimation factor.

Definition at line 477 of file FocDevice.cpp.

### 4.4.4.4 readErrorLimit()

```
unsigned int FocDevice::readErrorLimit ( )
```

Read the test error limit.

Returns

Error limit for the speed monitor.

Definition at line 463 of file FocDevice.cpp.

### 4.4.4.5 readLeds()

```
unsigned int FocDevice::readLeds ( )
```

Read LED-s state.

Returns

Bitfield of the leds  $\mathtt{LD0}\ ...\ \mathtt{LD3}$  on the ARTY Z7 platform.

Definition at line 429 of file FocDevice.cpp.

### 4.4.4.6 readParameter()

Read parameter register.

#### **Parameters**

parameterIndex Index of the parameter register to be read from	parameterIndex
--	----------------

### Returns

Value of the parameter register.

Definition at line 240 of file FocDevice.cpp.

```
241 {
242
       CHECK_PARAMETER_INDEX(argumentIndex);
       243
244
       const unsigned int
245
       const uint32_t r = _registers->read32(_parameter_registers_offset + index);
if (argumentIndex == RPM_SP_REG && !_sdsoc_info) {
246
247
248
           return negative_of_uint32(r);
249
250
       else {
           return (r >> ra->shift) & ra->mask;
251
252
```

### 4.4.4.7 readParameterString()

String representation of the parameter register in the following format: NAME CONVERTED\_VALUE REGISTER ← \_\_VALUE.

### **Parameters**

buffer	Buffer to store the the string to.
parameterIndex	Index of the parameter register to be formatted.

Definition at line 256 of file FocDevice.cpp.

```
257 {
258
       CHECK_PARAMETER_INDEX(argumentIndex);
259
       const RegisterAccess* ra = &parameterRegisters[argumentIndex];
260
        const uint32_t
                               u_reg_0 = read_parameter(ra->index);
261
       uint32_t
                                u_reg = (u_reg_0 >> ra-> shift) & ra-> mask;
262
263
       if (argumentIndex == RPM_SP_REG && !_sdsoc_info) {
264
           u_reg = negative_of_uint32(u_reg);
265
266
267
       switch (ra->registerType) {
268
       case RegisterType::UINT32:
          ssprintf(buffer, "%s %u 0x%X", ra->name, (unsigned int)u_reg, u_reg);
269
270
271
       case RegisterType::INT32:
272
        ssprintf(buffer, "%s %d 0x%X", ra->name, (int)u_reg, u_reg);
273
274
       default:
275
           buffer = "Error: Internal #1";
276
```

### 4.4.4.8 readSpreadSpectrum()

```
bool FocDevice::readSpreadSpectrum ( )
```

Read the spread spectrum flag.

Returns

true if the spread spectrum is enabled, false otherwise.

Definition at line 498 of file FocDevice.cpp.

```
499 {
500     if (designName == NAME_HLS) {
501          const uint32_t m = read_parameter(CONTROL2_REG);
502          return (m & CONTROL2_BV_SPREAD_SPECTRUM) != 0u;
503     }
504     else {
505          return false;
506     }
507 }
```

### 4.4.4.9 readStatus()

Read status register.

**Parameters** 

statusIndex	Index of the status register to be read.
-------------	--

### Returns

Value of the status register.

Definition at line 288 of file FocDevice.cpp.

### 4.4.4.10 readStatusString()

String representation of the status register in the following format: NAME VALUE VALUE.

### **Parameters**

buffer	Buffer to store the string to.
statusIndex	Index of the status register to be formatted.

Definition at line 295 of file FocDevice.cpp.

```
296 {
                                        CHECK_STATUS_INDEX(statusIndex);
const RegisterAccess*     ra = &statusRegisters[statusIndex];
const registerAccess*     ra = registerS->read32( status registerS->read32
297
298
299
                                                                                                                                                                                          u_reg = _registers->read32(_status_registers_offset + statusIndex);
                                        const uint32 t
300
301
                                       switch (ra->registerType) {
302
                                        case RegisterType::UINT32:
303
                                                             ssprintf(buffer, "%s %u 0x%X", ra->name, u_reg, u_reg);
304
305
                                       case RegisterType::INT32:
                                                          ssprintf(buffer, "%s %d 0x%X", ra->name, static_cast<int32_t>(u_reg), u_reg);
306
                                                             break;
308
                                        default:
309
                                                           buffer = "Error: Internal #2";
                                        }
310
311 }
```

### 4.4.4.11 startMotor()

Start the motor in the given mode.

### **Parameters**

mode	Mode to start the motor in. See the control register in the user manual for the FOC SDSoC project for the applicable values.
capture	Data capture device used for capturing the

Definition at line 325 of file FocDevice.cpp.

```
326 {
327     const uint32_t old_mode = readParameter(PSEUDO_PARAMETER::MODE);
328     if (old_mode == newMode) {
329         // Already started, do nothing.
330         return;
331     }
332     if (newMode != MODE_STOPPED) {
333         // Stopping the motor resets various internal variables in the FOC.
```

```
334
            writeParameter(PSEUDO_PARAMETER::MODE, MODE_STOPPED);
335
            if (config) {
336
                if (newMode == MODE_SPEED
                    || newMode == MODE_SPEED_WITHOUT_TORQUE) {
337
338
                    writeParameterValues(config->speed);
339
                else if (newMode == MODE_TORQUE_WITHOUT_SPEED) {
340
341
                     writeParameterValues(config->torque);
342
343
            if (old mode == MODE STOPPED) {
344
                const unsigned int old_fixed_period = readParameter(
345
      PSEUDO_PARAMETER::FIXED_PERIOD);
                const unsigned int fixed_period = std::max<unsigned int>(old_fixed_period + 1u, 200u);
346
347
                const float
                                     clocks_per_rev = static_cast<float>(fixed_period) * static_cast<float>(CPR
      * CPR);
                const unsigned int <code>ms_to_sleep</code> = static_cast<unsigned int>(2.0 * (1000.0 / FOC_CLOCK_HZ) *
348
      clocks_per_rev);
349
350
                write_parameter(ANGLE_SH_REG, Ou);
351
                msleep(100);
352
                // The forced rotation mode ensures that the encoder index is reset at least once.
                writeParameter(PSEUDO_PARAMETER::MODE, MODE_MANUAL_TORQUE_FLUX_FIXED_SPEED);
353
354
                writeParameter (PSEUDO PARAMETER::FIXED PERIOD, fixed period);
355
                const unsigned int
const unsigned int
const unsigned int
old_decimate = readDecimate();
356
357
358
                while (capture->isCaptureInProgress()) {
359
                    msleep(10);
360
361
362
                writeDecimate(0);
363
                writeCaptureSource(DATASOURCE_V_A_B_C);
364
365
                const uint64_t
                                              t_end = time_us() + ms_to_sleep * 1000ull;
                                             buffer16 = reinterpret_cast<volatile int16_t*>(capture->buffer->
366
                volatile int16_t*
      data());
367
                // Count of Va samples.
368
                unsigned int
                                             count_va = 0u;
369
                // Last Va sample.
370
                int16_t
                                             last_va = 0;
371
                // Zero crossings.
372
                std::vector<unsigned int>
                                             zero cross:
373
                                             NSAMPLES = 32u;
                constexpr unsigned int
374
                do {
375
                     const unsigned int current_angle = _registers->read32(_status_registers_offset + ANGLE_REG
      );
376
                    capture->startCapture(NSAMPLES * sizeof(uint64_t));
377
                    do {
378
                        msleep(1);
379
                    } while (capture->isCaptureInProgress());
380
                    const int16_t
381
                                         this_va = *buffer16;
382
                    if (count_va>0 && last_va <= 0 && this_va>0) {
383
                        zero_cross.push_back(current_angle);
384
385
386
                     ++count_va;
                    last_va = this_va;
387
388
                } while (time_us() < t_end);</pre>
389
390
391
                writeCaptureSource(old_capture_source);
392
                writeDecimate(old_decimate);
393
                writeParameter(PSEUDO_PARAMETER::FIXED_PERIOD, old_fixed_period);
394
395
                if (zero_cross.size()>0u) {
396
                    // The offset of 150 works for the EDDP Kit.
397
                    constexpr unsigned int offset = (3*CPR)/(10*PPR) - 15;
                                             new_shift = (zero_cross.back() + offset) % CPR;
398
                     const unsigned int
399
                     write_parameter(ANGLE_SH_REG, new_shift);
400
401
                else {
                    // TODO: How to report the failure?
402
403
404
405
406
        writeParameter(PSEUDO_PARAMETER::MODE, newMode);
                                                                  // Run motor in speed loop
407 }
```

### 4.4.4.12 writeCaptureSource()

Set new capture source.

### **Parameters**

sourcember   New Capture Source muex.	sourceIndex	New capture source index.
---------------------------------------	-------------	---------------------------

### Definition at line 416 of file FocDevice.cpp.

```
417 {
418 __registers->write32Masked(_parameter_registers_offset + CONTROL2_REG, 0x7, sourceIndex);
419 }
```

### 4.4.4.13 writeDecimate()

Write decimation factor (number of samples to skip for every sample captured).

### **Parameters**

decimationFactor	New decimation factor.
------------------	------------------------

### Definition at line 470 of file FocDevice.cpp.

### 4.4.4.14 writeErrorLimit()

Write error limit.

### **Parameters**

error_limit	New error limit for the speed monitor.
-------------	--

Definition at line 456 of file FocDevice.cpp.

### 4.4.4.15 writeLeds()

Write led state.

At the moment only 1 led is supported.

#### **Parameters**

```
leds 0 to turn the led LD0 on the ARTY Z7 platform off, 1 to turn it on.
```

Definition at line 444 of file FocDevice.cpp.

### 4.4.4.16 writeParameter()

Write parameter register.

### **Parameters**

parameterIndex	Index of the parameter register to be written to.
parameterValue	Value of the parameter to be written.

Definition at line 217 of file FocDevice.cpp.

```
218 {
```

```
219
       CHECK_PARAMETER_INDEX(argumentIndex);
       220
221
222
       if (argumentIndex == RPM_SP_REG && !_sdsoc_info) {
223
224
          write_parameter(index, negative_of_uint32(argumentValue));
225
226
       else {
                             shift = ra->shift;
227
          const uint32_t
228
           const uint32_t
                                 mask = ra->mask;
           if (shift==0 && mask==UINT32_MAX) {
229
              // just reading registers can be expensive, too.
write_parameter(index, argumentValue);
230
231
232
233
           else {
234
              _registers->write32Masked(_parameter_registers_offset + index, mask << shift, argumentValue <<
     shift);
235
236
237 }
```

### 4.4.4.17 writeSpreadSpectrum()

Write the spread spectrum flag.

**Parameters** 

enableSpreadSpectrum True if spread spectrum is to be enabled, false otherwise.

Definition at line 484 of file FocDevice.cpp.

```
485 {
486
        if (designName == NAME_HLS) {
487
           const uint32_t m = read_parameter(CONTROL2_REG);
488
           if (enableSpreadSpectrum) {
               write_parameter(CONTROL2_REG, m | CONTROL2_BV_SPREAD_SPECTRUM);
489
490
491
           else {
492
               write_parameter(CONTROL2_REG, m & ~CONTROL2_BV_SPREAD_SPECTRUM);
493
       }
494
495 }
```

### 4.4.5 Member Data Documentation

### 4.4.5.1 designName

```
const char* FocDevice::designName
```

Name of the HW design the software is running on.

This is detected automatically. One of NAME\_SDSOC or NAME\_HLS.

Definition at line 99 of file FocDevice.h.

### 4.4.5.2 PSEUDO\_PARAMETER\_OFFSET

```
constexpr unsigned int FocDevice::PSEUDO_PARAMETER_OFFSET = 16u [static]
```

Offset to the pseudo registers.

Important: this should match ARGS\_SIZE in foc.h

Definition at line 61 of file FocDevice.h.

The documentation for this class was generated from the following files:

- src/FocDevice.h
- src/FocDevice.cpp

### 4.5 FocServer Class Reference

FOC server implementing the *Network API* and a web server, which permits control and monitor of the FOC system from the Web UI.

```
#include <FocServer.h>
```

### **Classes**

· struct BinaryHeader

Layout of the binary header.

### **Public Member Functions**

FocServer (std::shared\_ptr< FocConfiguration > config)

Create new FOC server object.

∼FocServer ()

Destruct server object.

• void run ()

Run the server until either stopped by a signal or by closing the underlying event loop.

void setTestMode (const bool pTestMode)

Set or reset the test mode flag.

void setWwwDirectory (const std::string &newWebDirectory)

Set the new document root directory for the web server.

const std::string & getWwwDirectory () const

Get the docuemnt root directory of the web server.

FocDevice \* device ()

Access to the underlying FOC device.

smart::hw::AxiDataCapture \* deviceCapture ()

Access to the underlying data capture device.

#### **Static Public Attributes**

• static constexpr unsigned int BINARY\_HEADER\_SIZE = 32u

Size of the header, in bytes.

#### 4.5.1 Detailed Description

FOC server implementing the *Network API* and a web server, which permits control and monitor of the FOC system from the Web UI.

#### Example code:

```
FocServer server;
server.run();
```

Definition at line 51 of file FocServer.h.

#### 4.5.2 Member Function Documentation

#### 4.5.2.1 device()

```
FocDevice * FocServer::device ( )
```

Access to the underlying FOC device.

Returns

Definition at line 215 of file FocServer.cpp.

#### 4.5.2.2 setTestMode()

Set or reset the test mode flag.

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#### **Parameters**

pTestMode	New test mode flag.
-----------	---------------------

Definition at line 196 of file FocServer.cpp.

```
197 {
198    __test_mode = pTestMode;
199    __configuration_reply.clear();
200 }
```

#### 4.5.2.3 setWwwDirectory()

Set the new document root directory for the web server.

#### **Parameters**

wWebDirectory New document root directory to serve files from.	١.
--	----

Definition at line 203 of file FocServer.cpp.

The documentation for this class was generated from the following files:

- src/FocServer.h
- src/FocServer.cpp

## 4.6 FocConfiguration::ParameterValue Struct Reference

Value of a parameter in the configuration file.

```
#include <FocConfiguration.h>
```

#### **Public Attributes**

• std::string name

Name of the register.

int index

Index of the parameter, < 0 when unknown.

• uint32\_t value

Value of the parameter.

#### 4.6.1 Detailed Description

Value of a parameter in the configuration file.

Definition at line 37 of file FocConfiguration.h.

#### 4.6.2 Member Data Documentation

#### 4.6.2.1 index

int FocConfiguration::ParameterValue::index

Index of the parameter, <0 when unknown.

See also INDEX\_NOT\_KNOWN\_YET and INDEX\_INVALID\_NAME.

Definition at line 42 of file FocConfiguration.h.

The documentation for this struct was generated from the following file:

· src/FocConfiguration.h

## 4.7 FocDevice::RegisterAccess Struct Reference

Description of access to a register in a register bank.

```
#include <FocDevice.h>
```

## **Public Attributes**

• const char \* name

Name of the register.

· const unsigned int index

Register index in the register bank (parameter or status).

• const RegisterType registerType

Type of the register.

· const int shift

Bit shift, if any.

const uint32\_t mask

Mask of the value in the original position.

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#### 4.7.1 Detailed Description

Description of access to a register in a register bank.

Definition at line 84 of file FocDevice.h.

The documentation for this struct was generated from the following file:

src/FocDevice.h

#### 4.8 WebsocketBuffer Class Reference

Write buffer, consisting of a queue of the messages to be written and a write buffer for the libwebsockets.

```
#include <WebsocketBuffer.h>
```

#### **Public Member Functions**

• WebsocketBuffer (struct lws \*wsi)

Create new write buffer.

• void writeMessage (const std::string &msg)

Write a message to the write queue.

• void writeBinary (const void \*message1, unsigned int size1,...)

Write a binary message to the write queue.

• int onWriteable ()

Call this from the libwebsockets callback.

#### 4.8.1 Detailed Description

Write buffer, consisting of a queue of the messages to be written and a write buffer for the libwebsockets.

This simplifies handling of pre- and postpadding as required by libwebosckets.

Important: A WebsocketBuffer is safe to use from one thread at a time only.

Usage: Call writeMessage() any number of times. In the libwebsockets callback, call onWriteable() as needed.

Definition at line 41 of file WebsocketBuffer.h.

#### 4.8.2 Constructor & Destructor Documentation

#### 4.8.2.1 WebsocketBuffer()

Create new write buffer.

#### **Parameters**

wsi Pointer to the libwebsockets object.

Definition at line 57 of file WebsocketBuffer.cpp.

#### 4.8.3 Member Function Documentation

#### 4.8.3.1 onWriteable()

```
int WebsocketBuffer::onWriteable ( )
```

Call this from the libwebsockets callback.

This will flush the write queue to the extent possible and schedule new callback if there was some data remaining in the queue.

Definition at line 158 of file WebsocketBuffer.cpp.

```
159 {
160
                          // NB! Fragments:
161
                         // The write_mode should be set as below:
162
                          // int write_mode;
                         // Write_mode = LWS_WRITE_BINARY; // single frame, no fragmentation
// write_mode = LWS_WRITE_BINARY | LWS_WRITE_NO_FIN; // first fragment
// write_mode = LWS_WRITE_CONTINUATION | LWS_WRITE_NO_FIN; // all middle fragments
// write_mode = LWS_WRITE_CONTINUATION; // last fragment
164
165
166
167
168
                           // More details can be found in the fragmentation section of the WebSocket RFC:
169
                       https://tools.ietf.org/html/rfc6455#section-5.4
170
                           // Source:
171
                      \verb|http://stackoverflow.com/questions/33916549/libwebsocket-send-big-messages-with-limited-payload in the control of the cont
                          bool stop_sending = false;
unsigned char* write_buffer = reinterpret_cast<unsigned char*>(&_write_buffer[
172
                       bool
173
                    PRE_PADDING / sizeof(_write_buffer[0])]);
174
175
                          while (!stop_sending && !_write_queue.empty()) {
176
                                                                                                                                   msg = _write_queue.front();
                                      WriteRecord&
                                                                                                                                   msg_size = msg.buffer.size();
177
                                       const unsigned int
178
179
180
                                                     unsigned int
                                                                                                                                                 todo;
181
                                                                                                                                                 write_protocol;
182
                                                     if (msg_size <= FRAGMENT_SIZE) {</pre>
183
184
                                                                  todo = msg_size;
185
                                                                  write_protocol = msg.type;
 186
187
                                                     else {
                                                                // Fragmented write.
if (msg.bytesWritten == 0u) {
188
189
190
                                                                               // First fragment.
191
                                                                               todo = FRAGMENT_SIZE;
```

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```
write_protocol = msg.type | LWS_WRITE_NO_FIN;
192
193
194
                      else {
                          const unsigned int real_todo = msg_size - msg.bytesWritten;
if (real_todo > FRAGMENT_SIZE) {
    // Middle fragments.
195
196
197
198
                               todo = FRAGMENT_SIZE;
199
                               write_protocol = LWS_WRITE_CONTINUATION | LWS_WRITE_NO_FIN;
200
2.01
                           else {
                               todo = real_todo;
202
                               write_protocol = LWS_WRITE_CONTINUATION;
203
204
205
                      }
206
                 }
207
                 // sorry, have to memcpy. Memcpy is cheap, guys :) memcpy(write_buffer, &msg.buffer[0] + msg.bytesWritten, todo);
208
209
                  const auto r = lws_write(_wsi, write_buffer, todo, (lws_write_protocol)write_protocol);
210
211
                  if (static_cast<unsigned int>(r) == todo) {
212
                      if (todo > _max_write_size)
213
214
                           _max_write_size = todo;
215
216
                      _was_write_error = false;
217
                      msg.bytesWritten += todo;
218
                 else {
   if (r > 0) {
219
220
                          msg.bytesWritten += r;
221
                           _was_write_error = false;
222
223
                           break;
224
225
                      else {
226
                           if (!_was_write_error) {
                               lwsl_err("Write error: %d.\n", r);
227
228
                          _was_write_error = true;
230
231
                      stop_sending = true;
232
                      break;
233
                  if (lws_partial_buffered(_wsi)) {
234
235
                      stop_sending = true;
236
                      break;
237
238
                  if (lws_send_pipe_choked(_wsi)) {
239
                      stop_sending = true;
240
                      break:
241
242
             } while (!stop_sending && msg.bytesWritten!=msg_size);
243
244
             if (msg.bytesWritten == msg_size) {
245
                 _write_queue.pop_front();
246
247
        }
248
249
         if (!_write_queue.empty()) {
250
             lws_callback_on_writable(_wsi);
251
         return _was_write_error ? -1 : 0;
252
253 }
```

#### 4.8.3.2 writeBinary()

Write a binary message to the write queue.

It will schedule a callback when the queue was not empty before the call.

#### **Parameters**

message1	First message to be written.
size1	Size of the first message to be written.

Definition at line 95 of file WebsocketBuffer.cpp.

```
96 {
97
        unsigned int
                         queue_count = 0;
98
        unsigned int
                         queue_bytes = 0;
99
                         total size = size1:
        unsigned int
100
        const void*
                          message2;
101
         unsigned int
                         size2;
102
         unsigned int
                          so_far = size1;
103
         va_list
104
105
         // Check the queue.
         if (!_checkQueue(queue_count, queue_bytes)) {
106
107
             return;
108
109
         const bool was_empty = queue_count==0u;
110
         // Count the total number of bytes.
111
         va_start(ap, size1);
112
113
         for (;;) {
             message2 = va_arg(ap, const void*);
114
115
             if (message2 == nullptr) {
116
                 break;
117
             size2 = va_arg(ap, unsigned int);
total_size += size2;
118
119
120
121
         va_end(ap);
122
         // Create new write record.
123
        _write_queue.emplace_back();
WriteRecord& packet = _write_queue.back();
packet.type = LWS_WRITE_BINARY;
124
125
126
127
         packet.buffer.resize(total_size);
128
        // Copy stuff over.
memcpy(&packet.buffer[0], message1, size1);
129
130
131
         va_start(ap, size1);
132
         for (;;) {
133
             message2 = va_arg(ap, const void*);
134
             if (message2 == nullptr) {
135
                 break;
136
137
             size2 = va_arg(ap, unsigned int);
             memcpy(&packet.buffer[so_far], message2, size2);
138
139
             so_far += size2;
140
141
         va_end(ap);
142
         packet.bytesWritten = 0;
143
144
145
146
         const unsigned int   qsize = _write_queue.size();
147
         if (qsize > _max_queue_size) {
148
             _max_queue_size = qsize;
149
150
151
         // To start writing again, mark us as writable.
152
         if (was_empty)
153
             lws_callback_on_writable(_wsi);
154
155 }
```

#### 4.8.3.3 writeMessage()

Write a message to the write queue.

It will schedule a callback when the queue was not empty before the call.

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#### **Parameters**

msg Message to be written.

Definition at line 68 of file WebsocketBuffer.cpp.

```
69 {
         70
         if (!_checkQueue(queue_count, queue_bytes)) {
72
73
74
75
         const bool was_empty = queue_count==0u;
77
         _write_queue.emplace_back();
         WriteRecord& packet = _write_queue.back();
packet.type = LWS_WRITE_TEXT;
packet.buffer.resize(msg.size());
78
79
80
        memcpy(&packet.buffer[0], msg.c_str(), msg.size());
packet.bytesWritten = 0;
81
82
         const unsigned int qsize = _write_queue.size();
if (qsize > _max_queue_size) {
    _max_queue_size = qsize;
}
84
85
86
87
88
89
        if (was_empty) {
90
              lws_callback_on_writable(_wsi);
91
92 }
```

The documentation for this class was generated from the following files:

- src/WebsocketBuffer.h
- src/WebsocketBuffer.cpp

## **Chapter 5**

# **File Documentation**

## 5.1 main.cpp File Reference

Implementation of the main function of the focserver.

```
#include "src/focserver_main.h"
```

#### **Functions**

• int main (int argc, char \*argv[])

Entry point to the program focserver.

## 5.1.1 Detailed Description

Implementation of the main function of the focserver.

Webserver control program for the Field-Oriented Control demo.

Version

1.0

Date

2017

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#### 5.1.2 Function Documentation

#### 5.1.2.1 main()

```
int main (
                int argc,
                 char * argv[] )
```

Entry point to the program focserver.

This just calls Main function focserver\_main. See section Introduction for the description.

Definition at line 15 of file main.cpp.

```
16 {
17
18     const int         r = focserver_main(argc, argv);
19     return r;
20 }
```

## 5.2 src/DeviceTreeDevice.h File Reference

Implementation of the class DeviceTreeDevice.

```
#include <memory>
#include <string>
#include <map>
#include <stdint.h>
```

## Classes

• class DeviceTreeDevice

Fetch information from the Linux Device Tree.

#### 5.2.1 Detailed Description

Implementation of the class <a href="DeviceTreeDevice">DeviceTreeDevice</a>.

Interface of the class DeviceTreeDevice.

Version

1.0

Date

2017

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## 5.3 src/FocConfiguration.cpp File Reference

Implementation of the class FocConfiguration.

```
#include <stdexcept>
#include <string>
#include <vector>
#include <stdio.h>
#include <json/reader.h>
#include <json/value.h>
#include <smart/File.h>
#include <smart/string.h>
#include "foc.h"
#include "FocConfiguration.h"
```

#### 5.3.1 Detailed Description

Implementation of the class FocConfiguration.

Version

1.0

Date

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## 5.4 src/FocConfiguration.h File Reference

Interface of the class FocConfiguration.

```
#include <memory>
#include <string>
#include <vector>
#include <stdint.h>
```

#### Classes

• class FocConfiguration

Configuration of the FOC server.

• struct FocConfiguration::ParameterValue

Value of a parameter in the configuration file.

#### 5.4.1 Detailed Description

Interface of the class FocConfiguration.

Version

1.0

Date

2017

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## 5.5 src/FocDevice.cpp File Reference

Implementation of the class FocDevice.

```
#include <stdexcept>
#include <string.h>
#include <smart/string.h>
#include <smart/time.h>
#include "FocDevice.h"
#include "foc.h"
```

#### **Macros**

- #define write\_parameter(index, value) \_registers->write32(\_parameter\_registers\_offset + (index), (value))

  Write a parameter register.
- #define read\_parameter(index) \_registers->read32(\_parameter\_registers\_offset + (index))
   Read a parameter register.
- #define CHECK\_PARAMETER\_INDEX(parameter\_index)

Check the parameter register index and throw an exception when it is not in the permitted range.

• #define CHECK\_STATUS\_INDEX(status\_index)

Check the status register index and throw an exception when it is not in the permitted range.

• #define **GET\_INT16**(data64, int16index) ((int16\_t)((data64 >> (int16index\*16)) & 0xFFFF))

#### 5.5.1 Detailed Description

Implementation of the class FocDevice.

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Date

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#### 5.5.2 Macro Definition Documentation

#### 5.5.2.1 CHECK\_PARAMETER\_INDEX

#### Value:

Check the parameter register index and throw an exception when it is not in the permitted range.

Definition at line 203 of file FocDevice.cpp.

#### 5.5.2.2 CHECK\_STATUS\_INDEX

#### Value:

```
do {
    if ((status_index) >= statusCount) {
        throw std::runtime_error(ssprintf("FocDevice: status index %u outside range 0 ... %u", (
        status_index), statusCount-lu));
    }
    while (0)
```

Check the status register index and throw an exception when it is not in the permitted range.

Definition at line 280 of file FocDevice.cpp.

#### 5.5.2.3 read\_parameter

Read a parameter register.

Ensure index is in the correct range before calling this function.

#### **Parameters**

	index	Index of the parameter register to be read.	1
--	-------	---	---

Definition at line 61 of file FocDevice.cpp.

#### 5.5.2.4 write\_parameter

Write a parameter register.

Ensure index is in the correct range before calling this function.

#### **Parameters**

index	Index of the parameter register.
value	Value to be written to the parameter register.

Definition at line 57 of file FocDevice.cpp.

## 5.6 src/FocDevice.h File Reference

Interface of the class FocDevice.

```
#include <limits>
#include <memory>
#include <stdint.h>
#include <smart/hw/AxiDataCapture.h>
#include <smart/MappedFile.h>
#include <smart/UioDevice.h>
#include "DeviceTreeDevice.h"
#include "FocConfiguration.h"
```

#### Classes

class FocDevice

Access to the FOC IP core.

• struct FocDevice::RegisterAccess

Description of access to a register in a register bank.

#### 5.6.1 Detailed Description

```
Interface of the class FocDevice.
```

Version

1.0

Date

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## 5.7 src/FocServer.cpp File Reference

Implementation of the class FocServer.

```
#include <limits>
#include <stdexcept>
#include <string>
#include <stdio.h>
#include <string.h>
#include <errno.h>
#include <sys/stat.h>
#include <sys/types.h>
#include <inttypes.h>
#include <libwebsockets.h>
#include <smart/string.h>
#include <smart/time.h>
#include <json/writer.h>
#include <json/value.h>
#include "FocServer.h"
#include "DeviceTreeDevice.h"
#include "Version.h"
```

#### **Macros**

• #define DEFAULT\_WWW\_DIRECTORY "/usr/share/focserver"

Default document root directory for the web server.

#define NAME\_LEDS\_STATUS\_REG "LEDs"

Name of the led status register.

#define NAME\_SPREAD\_SPECTRUM\_REG "SpreadSpectrum"

name of the fictive spread spectrum register.

#define COMMAND\_CAPTURE "Capture"

Name of the capture command.

#define COMMAND\_RESET\_ERROR "ResetError"

Name of the reset error command.

• #define COMMAND ERROR LIMIT "ErrorLimit"

Name of the error limit parameter register.

#define COMMAND CONFIGURATION "Configuration"

Command to query/set configuration.

#### **Enumerations**

enum server\_protocols { PROTOCOL\_HTTP = 0, PROTOCOL\_FOC, PROTOCOL\_COUNT }
 List of the protocols supported.

#### 5.7.1 Detailed Description

Implementation of the class FocServer.

Version

1.0

Date

2017

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## 5.8 src/FocServer.h File Reference

Interface of the class FocServer.

```
#include <list>
#include <memory>
#include <string>
#include <stdint.h>
#include <libwebsockets.h>
#include <uv.h>
#include <smart/hw/AxiDataCapture.h>
#include "FocConfiguration.h"
#include "FocDevice.h"
#include "WebsocketBuffer.h"
```

#### Classes

class FocServer

FOC server implementing the Network API and a web server, which permits control and monitor of the FOC system from the Web UI.

• struct FocServer::BinaryHeader

Layout of the binary header.

#### 5.8.1 Detailed Description

Interface of the class FocServer.

Version

1.0

Date

2017

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## 5.9 src/focserver\_main.cpp File Reference

Implementation of the function focserver main.

```
#include "focserver_main.h"
#include <memory>
#include <stdexcept>
#include <string>
#include <getopt.h>
#include <inttypes.h>
#include <stdlib.h>
#include <string.h>
#include <syslog.h>
#include <sys/time.h>
#include <unistd.h>
#include <uv.h>
#include <libwebsockets.h>
#include <smart/File.h>
#include <smart/string.h>
#include <smart/time.h>
#include <smart/WavFormat.h>
#include <smart/hw/AxiDataCapture.h>
#include "FocConfiguration.h"
#include "DeviceTreeDevice.h"
#include "FocDevice.h"
#include "FocServer.h"
#include "Version.h"
#include "foc.h"
```

#### **Functions**

• int focserver\_main (int argc, char \*argv[])

Main function of the focserver, which implements the Network API and a web server.

#### 5.9.1 Detailed Description

Implementation of the function focserver\_main.

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#### 5.9.2 Function Documentation

#### 5.9.2.1 focserver main()

```
int focserver_main (
                int argc,
                char * argv[] )
```

Main function of the focserver, which implements the Network API and a web server.

See section Introduction for the description. Result of the write or capture operation.

Definition at line 234 of file focserver\_main.cpp.

```
235 {
236
        int debug_level = 7;
237
        int n = 0;
238
        int syslog_options = LOG_PID | LOG_PERROR;
239
        bool daemonize = false;
240
        bool start_motor = false;
241
        std::unique_ptr<int>
                                                  start_motor_speed;
        std::unique_ptr<FocDevice>
242
                                                  write_device;
243
        std::unique_ptr<hw::AxiDataCapture>
                                                  capture_device;
                                                  configuration;
244
        std::shared_ptr<FocConfiguration>
245
        /// Result of the write or capture operation.
                                                  op_result = 0;
test_mode = false;
246
247
        bool
248
        const char*
                                                  www_directory = nullptr;
249
250
        try {
            while (n >= 0) {
251
252
                n = getopt_long(argc, argv, "c:d:f:Dhps::tvw:W:", options, NULL);
253
                if (n < 0)
254
                     continue;
                switch (n) {
case 'c':
255
256
                    op_result = capture(capture_device, optarg);
258
                     if (op_result != 0) {
259
                         return op_result;
260
                break;
case 'D':
2.61
262
263
                    daemonize = true;
264
                     syslog_options &= ~LOG_PERROR;
```

```
265
                    break;
266
                case 'd':
267
                     debug_level = atoi(optarg);
                break;
case 'f':
268
2.69
                    configuration = FocConfiguration::fromFile(optarg);
270
271
                     if (configuration) {
272
                         configuration->dump();
273
274
                     else {
                         lwsl_notice("Error: configuration file %s not found\n", optarg);
275
276
                         return 1:
278
279
                case 'h':
280
                   print_usage();
281
                     exit(1);
                case 'p':
282
283
                   print_registers();
284
                     return 0;
285
                case 's':
286
                    if (optarg != nullptr) {
                        int x = 0;
287
                         if (int of(optarg, x)) {
288
289
                             start_motor_speed = std::unique_ptr<int>(new int(x));
290
291
292
                     start_motor = true;
                break; case 't':
293
294
295
                    test mode = true;
296
                    break;
297
                case 'v':
298
                    printf("Version: %s\n", Version::FOCSERVER_DATE);
                return 0; case 'w':
299
300
301
                    op_result = write_register(write_device, optarg);
                     if (op_result != 0) {
302
303
                        return op_result;
304
305
                    break;
                case 'W':
306
307
                    www_directory = optarg;
308
                     break;
309
                }
310
            }
311
312
            if (!start_motor && !daemonize && (write_device || capture_device)) {
313
                return op_result;
314
            }
315
316
317
            * normally lock path would be /var/lock/lwsts or similar, to
318
             \star simplify getting started without having to take care about
319
            * permissions or running as root, set to /tmp/.lwsts-lockc
320
321
            if (daemonize && lws_daemonize("/tmp/.lwsts-lock")) {
322
                fprintf(stderr, "Failed to daemonize\n");
323
                return 1;
324
325
            /* we will only try to log things according to our debug_level \star/ setlogmask(LOG_UPTO (LOG_DEBUG));
326
327
328
            openlog("lwsts", syslog_options, LOG_DAEMON);
329
330
            /\star tell the library what debug level to emit and to send it to syslog \star/
331
            lws_set_log_level(debug_level, lwsl_emit_syslog);
332
333
            lwsl_notice("FOC webserver.\n");
334
335
            if (!configuration && File::exists(FocConfiguration::FILENAME)) {
336
                configuration = FocConfiguration::fromFile(
      FocConfiguration::FILENAME);
337
338
            if (!configuration) {
                lwsl_notice("Configuration file %s not found\n",
339
      FocConfiguration::FILENAME);
340
            FocServer
341
                                         server(configuration);
342
            FocDevice*
                                        dev = server.device();
            smart::hw::AxiDataCapture* devcap = server.deviceCapture();
343
344
345
            server.setTestMode(test_mode);
346
            if (www_directory != nullptr) {
347
                server.setWwwDirectory(www_directory);
348
349
            lwsl notice("focserver version: %s\n", Version::FOCSERVER DATE);
```

```
 lwsl\_notice("FOC design: $s\n", dev->designName); \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev-> \\ \\ lwsl\_notice("FOC IP core base a
                             getBaseAddress());
                                                       lwsl_notice("WWW server directory: %s\n", server.getWwwDirectory().c_str());
lwsl_notice("Test mode: %s\n", test_mode ? "true" : "false");
352
353
354
                                                         if (start_motor) {
356
                                                                                if (start_motor_speed) {
357
                                                                                                   dev->writeParameter(RPM_SP_REG, (uint32_t)*start_motor_speed);
358
                                                                              lwsl_notice("Starting the motor at speed %d RPM.\n", (int32_t)dev->
359
                             readParameter(RPM_SP_REG));
360
                                                                              dev->startMotor(MODE_SPEED, devcap);
361
362
                                                           server.run();
363
                                     lwsl_notice("Exited cleanly\n");
catch (const std::exception& ex) {
  printf("Error: %s\n", ex.what());
364
365
366
                                                           return 2;
368
369
                                       return 0;
370 }
```

## 5.10 src/focserver\_main.h File Reference

Declaration of the function focserver\_main.

#### **Functions**

• int focserver\_main (int argc, char \*argv[])

Main function of the focserver, which implements the Network API and a web server.

#### 5.10.1 Detailed Description

Declaration of the function focserver\_main.

Version

1.0

Date

2017

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#### 5.10.2 Function Documentation

#### 5.10.2.1 focserver\_main()

```
int focserver_main (
                int argc,
                char * argv[] )
```

Main function of the focserver, which implements the Network API and a web server.

See section Introduction for the description. Result of the write or capture operation.

Definition at line 234 of file focserver main.cpp.

```
235 {
236
        int debug_level = 7;
237
        int n = 0;
238
        int syslog_options = LOG_PID | LOG_PERROR;
        bool daemonize = false;
        bool start_motor = false;
241
        std::unique_ptr<int>
                                                 start_motor_speed;
242
        std::unique_ptr<FocDevice>
                                                 write_device;
        std::unique_ptr<hw::AxiDataCapture>
243
                                                 capture_device;
244
        std::shared ptr<FocConfiguration>
                                                 configuration;
245
        /// Result of the write or capture operation.
246
                                                 op_result = 0;
247
        bool
                                                 test_mode = false;
248
        const char*
                                                 www_directory = nullptr;
249
250
        try {
251
            while (n >= 0) {
252
                n = getopt_long(argc, argv, "c:d:f:Dhps::tvw:W:", options, NULL);
253
                if (n < 0)
254
                    continue;
                switch (n) {
case 'c':
255
256
257
                   op_result = capture(capture_device, optarg);
258
                    if (op_result != 0) {
259
                        return op_result;
260
261
                   break;
                case 'D':
262
263
                   daemonize = true;
264
                    syslog_options &= ~LOG_PERROR;
                break; case 'd':
265
266
2.67
                   debug_level = atoi(optarg);
268
                    break;
                case 'f':
269
270
                   configuration = FocConfiguration::fromFile(optarg);
271
                    if (configuration) {
272
                        configuration->dump();
273
274
                    else {
                        lwsl_notice("Error: configuration file %s not found\n", optarg);
275
                        return 1;
277
278
                    break;
279
                case 'h':
280
                   print_usage();
281
                    exit(1);
282
                case 'p':
283
                   print_registers();
284
                    return 0;
285
                case 's':
286
                    if (optarg != nullptr) {
287
                        int x = 0;
288
                        if (int_of(optarg, x)) {
                            start_motor_speed = std::unique_ptr<int>(new int(x));
289
290
291
292
                    start_motor = true;
293
                   break;
                case 't':
294
                    test_mode = true;
296
297
                case 'v':
                   printf("Version: %s\n", Version::FOCSERVER_DATE);
298
                return 0; case 'w':
299
300
301
                   op_result = write_register(write_device, optarg);
                    if (op_result != 0) {
```

```
return op_result;
304
305
                      break;
                  case 'W':
306
307
                      www_directory = optarg;
308
                      break:
309
310
             }
311
312
             if (!start_motor && !daemonize && (write_device || capture_device)) {
313
                  return op_result;
314
             }
315
316
317
              \star normally lock path would be /var/lock/lwsts or similar, to
318
              \star simplify getting started without having to take care about
319
              * permissions or running as root, set to /tmp/.lwsts-lockc
320
321
             if (daemonize && lws_daemonize("/tmp/.lwsts-lock")) {
                  fprintf(stderr, "Failed to daemonize\n");
322
323
324
325
             /* we will only try to log things according to our debug_level \star/ setlogmask(LOG_UPTO (LOG_DEBUG));
326
327
             openlog("lwsts", syslog_options, LOG_DAEMON);
328
329
             /* tell the library what debug level to emit and to send it to syslog \star/ lws_set_log_level(debug_level, lwsl_emit_syslog);
330
331
332
333
             lwsl notice("FOC webserver.\n");
334
335
             if (!configuration && File::exists(FocConfiguration::FILENAME)) {
336
                  configuration = FocConfiguration::fromFile(
      FocConfiguration::FILENAME);
337
338
             if (!configuration) {
                  lwsl_notice("Configuration file %s not found\n",
339
      FocConfiguration::FILENAME);
340
341
             FocServer
                                            server(configuration);
342
             FocDevice*
                                            dev = server.device();
             smart::hw::AxiDataCapture* devcap = server.deviceCapture();
343
344
345
             server.setTestMode(test_mode);
346
             if (www_directory != nullptr) {
347
                  server.setWwwDirectory(www_directory);
348
              lwsl\_notice("focserver version: \$s\n", Version::FOCSERVER\_DATE); \\ lwsl\_notice("FOC design: \$s\n", dev->designName); \\ 
349
350
             lwsl_notice("FOC IP core base address: 0x%08" PRIxPTR "\n", dev->
351
      getBaseAddress());
    lwsl_notice("WWW server directory: %s\n", server.getWwwDirectory().c_str());
    lwsl_notice("Test mode: %s\n", test_mode? "true": "false");
352
353
354
355
             if (start motor) {
356
                  if (start_motor_speed) {
357
                      dev->writeParameter(RPM_SP_REG, (uint32_t)*start_motor_speed);
358
                  lwsl_notice("Starting the motor at speed %d RPM.\n", (int32_t)dev->
359
      {\tt readParameter} (RPM_SP_REG));
                 dev->startMotor(MODE_SPEED, devcap);
360
361
             server.run();
363
364
             lwsl_notice("Exited cleanly\n");
365
        } catch (const std::exception& ex) {
            printf("Error: %s\n", ex.what());
366
367
             return 2:
368
369
         return 0;
370 F
```

#### 5.11 src/Version.h File Reference

Version information.

### **Variables**

constexpr const char \* Version::FOCSERVER DATE = "2017-09-27"

Build date of the focserver.

#### 5.11.1 Detailed Description

Version information.

Version

1.0

Date

2017

Copyright

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## 5.12 src/WebsocketBuffer.cpp File Reference

Implementation of the class WebsocketBuffer.

```
#include <stdarg.h>
#include <string.h>
#include "WebsocketBuffer.h"
```

#### **Macros**

#define ALIGN(x\_align) (ALIGNMENT\*(((x\_align) + ALIGNMENT - 1u)/ALIGNMENT))
 Align a value.

#### **Typedefs**

typedef uint64\_t buffer\_element\_t
 Buffer element.

#### **Variables**

constexpr unsigned int QUEUE LENGTH LIMIT = 500

Limit of the write queue length.

• constexpr unsigned int QUEUE\_BYTES\_LIMIT = 10 \* 1024 \* 1024

Limit of the total data size in a queue, in bytes.

constexpr unsigned int ALIGNMENT = (sizeof(buffer\_element\_t))

Alignment, in bytes.

constexpr unsigned int PRE\_PADDING = ALIGN(LWS\_SEND\_BUFFER\_PRE\_PADDING)

Size of pre-padding, in bytes, aligned.

constexpr unsigned int POST\_PADDING = ALIGN(LWS\_SEND\_BUFFER\_POST\_PADDING)

Size of post-paddding, in bytes, aligned.

constexpr unsigned int FRAGMENT\_SIZE = 32\*1024

Size above which messages will be fragmentized.

## 5.12.1 Detailed Description

Implementation of the class WebsocketBuffer.

Version

1.0

Date

2017

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## 5.13 src/WebsocketBuffer.h File Reference

Interface of the class WebsocketBuffer.

```
#include <stdint.h>
#include <string>
#include <vector>
#include <deque>
#include <libwebsockets.h>
```

#### Classes

• class WebsocketBuffer

Write buffer, consisting of a queue of the messages to be written and a write buffer for the libwebsockets.

#### 5.13.1 Detailed Description

Interface of the class WebsocketBuffer.

Version

1.0

Date

2017

Copyright

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