

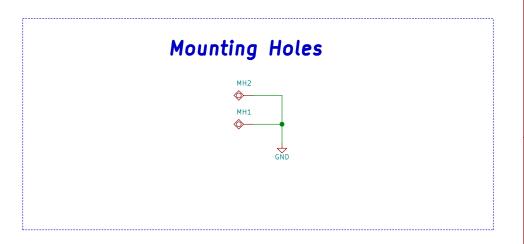
NA(NH1×20)

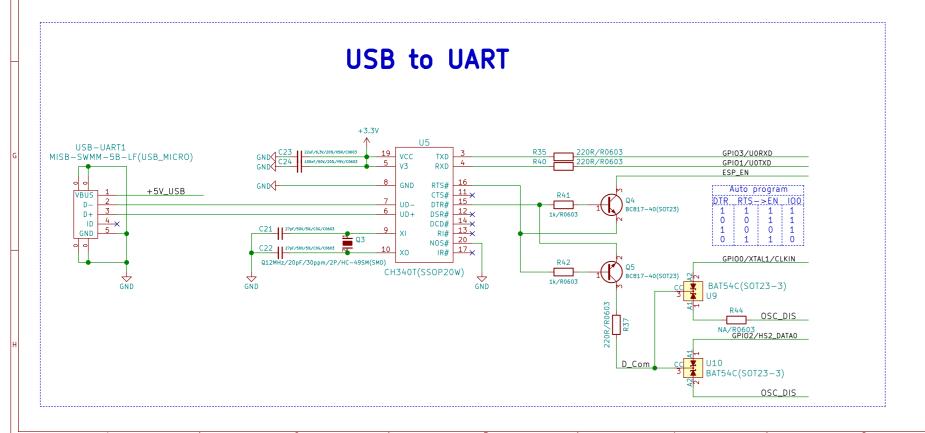
Extension

GP105

GND ↓ +5V\_EXT ← GPI07/SD\_DATA0

GPIO8/SD\_DATA1
GPIO9/SD\_DATA2





Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDL_in EMAC_MDO_out EMAC_CRS_out EMAC_COL_out	Any GPIO
I2C	I2CEXTO_SCL_in I2CEXTO_SDA_in I2CEXT1_SCL_in I2CEXT1_SDA_in I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out I2CEXT1_SCL_out	Any GPIO
General Purpose SPI	HSPIQ_in/_out HSPIQ_in/_out HSPIC_IN/_out HSPIC_CSO_in/_out HSPI_CSL_out HSPI_CS2_out VSPIQ_in/_out VSPIC_in/_out VSPIC_In/_out VSPIC_CSO_in/_out VSPIC_CSO_in/_out VSPIC_SS2_out	Any GPIO

		Voltage of Interr	nal LDO(VDD_SDIO)			
Pin	Default	3.3V	1.8V			
GPI012/MTDI	Pull-Down	0	1	Bootstrapping Pins		
		Bootin	g Mode			
Pin	Default	SPI Flash Boot	Download Boot	Sett	Settings	
GPI00	Pull-Up	1	0			
GPI02	Pull-Down	Don't-care	0			
		Debugging Log on l	JOTXD During Booting			
Pin	Default	UOTXD Toggling	UOTXD Silent			
GPI015/MTD0	Pull-Up	1	0			
		Timing of	SDIO Slave			
Pin	Default	Falling-edge:Output	Falling-edge:Input Rising-edge:Output	Rising—edge:Input Falling—edge:Output	Rising—edge:Input Rising—edge:Outpu	
GPI015/MTD0	Pull-Up	0	0	1	1	
GPI05	Pull-Up	0	1	0	1	

Internal Bootstra	pping Resistors
MTDI/GPI012:	Pull-Down
GP100:	Pull-Up
GP102:	Pull-Down
GP104:	Pull-Down
MTDO/GPI015:	Pull-Up
GP105:	Pull-Up

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File: ESP32-GATE	WAY_Rev_B.sch	
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