

















Interface	Signal	Pin
EMAC	EMAC_MDC_out EMAC_MDI_in EMAC_MDO_out EMAC_CRS_out EMAC_COL_out	Any GPIO
I2C	I2CEXTO_SCL_in I2CEXTO_SDA_in I2CEXT1_SCL_in I2CEXT1_SDA_in I2CEXT0_SCL_out I2CEXT0_SDA_out I2CEXT1_SCL_out I2CEXT1_SCL_out	Any GPIO
General Purpose SPI	HSPIQ_in/_out HSPID_in/_out HSPID_in/_out HSPICLK_in/_out HSPLCS0_in/_out HSPLCS1_out HSPLCS2_out VSPIQ_in/_out VSPID_in/_out VSPICS0_in/_out VSPLCS0_in/_out VSPLCS0_in/_out VSPLCS0_in/_out	Any GPIO

	·	Voltage of Intern	al LDO(VDD_SDIO)			
Pin	Default	3.3V	1.8V			
GPI012/MTDI	Pull-Down	0	1	Bootstrapping Pins		
		Bootin	g Mode			
Pin	Default	SPI Flash Boot	Download Boot	iii Seti	tings	
GPI00	Pull-Up	1	0			
GPI02	Pull-Down	Don't-care	0			
		Debugging Log on U	OTXD During Booting			
Pin	Default	UOTXD Toggling	UOTXD Silent			
GPI015/MTD0	Pull-Up	1	0			
		Timing of	SDIO Slave			
Pin	Default	Falling-edge:Output	Falling-edge:Input Rising-edge:Output	Rising—edge:Input Falling—edge:Output	Rising-edge:Input Rising-edge:Output	
GPI015/MTD0	Pull-Up	0	0	1	1	
GPI05/PHY_PW	/RPull-Up	0	1	0	1	

Internal Bootstra	pping Resistors
MTDI/GPI012:	Pull-Down
GPI00:	Pull-Up
GPI02:	Pull-Down
GP104:	Pull-Down
MTDO/GPI015:	Pull-Up
GPI05/PHY	_PWRP.ull-Up

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