Power Supply

LiPo Charger

External Power Sense

BL4054B-42TPRN(SOT23-5)
IBAT = 1000/Rprog = 455mA

CHARGING

Vout=0.6*(1+Ra/Rb)

ESP-WROOM-32 MODULE

ESP-WROOM-32 MODULE

Buttons

SD/MMC Card

MICRO_SD1

TFC-9P-1.7H(ATFFS150A01BR016)

Ethernet

LAN8710A-EZC(QFN32)

The PHY is setted up as follows:
MODE: All capable(10/100Base)
–Auto–negotiation enabled
–RMII Configuration
–SMI address: 0x00

Single 10/100 BASE-TX Filter Connector Module
RJP-003TC1(LPJ4112CNL)

PoE Power Supply

The input filter capacitor must be >5 µF for to MPS!

250mW(50mA@5V) minimal power consumption on the PD required for to Maintain Power Signature(MPS)!

Class=0 (0.44 to 12.95W)

VOUT=1.35*(1+Ra/Rb)

SI3402-8-GM(QFN-20,5x5mm)

Extensions

EXT1   EXT2
NS(HN1x10)

UEXT

B-V-10-LF(GBH254SMT-10)

USB to UART

USB-UART1
MISB-SWMM-5B-LF(USB_MICRO)

CH340T(SSOP20W)

Auto program

DTR	RTS	~EN	IO0
0	0	1	1
1	1	1	1
0	1	1	0
1	0	0	1

!!!WARNING!!!
When the board is powered via PoE, don't include the USB cable for to programming it!!!
In case this is required, USB-ISO must be used for to guarantee galvanic isolation!
See: https://www.olimex.com/Products/USB-Modules/USB-ISO/

Notes:

Software Selectable Pins

Interface	Signal	Pin
EMAC	EMAC_MDC_out	Any GPIO
	EMAC_MDI_in	
	EMAC_MDO_out	
	EMAC_CRS_out	
	EMAC_COL_out	
I2C	I2CEXT0_SCL_in	Any GPIO
	I2CEXT0_SDA_in	
	I2CEXT0_SCL_in	
	I2CEXT0_SDA_in	
	I2CEXT0_SCL_out	
	I2CEXT0_SDA_out	
	I2CEXT1_SCL_out	
	I2CEXT1_SDA_out	
General Purpose SPI	HSPIQ_in/_out	Any GPIO
	HSPID_in/_out	
	HSPICLK_in/_out	
	HSPICS0_in/_out	
	HSPICS1_in/_out	
	HSPICS2_in/_out	
	VSPIQ_in/_out	
	VSPID_in/_out	
	VSPICLK_in/_out	
	VSPICS0_in/_out	
	VSPICS1_in/_out	
	VSPICS2_in/_out	

For more information refer to esp_wroom_32_datasheet_en.pdf.

Internal Bootstrapping Resistors

MTDI/GPIO12:	Pull-Down
GPIO0:	Pull-Up
GPIO2:	Pull-Down
GPIO4:	Pull-Down
MTDO/GPIO15:	Pull-Up
GPIO5:	Pull-Up

Bootstrapping Pins Settings

Voltage of Internal LDO(VDD_SDIO)

Pin	Default	3.3V	1.8V
GPIO12/MTDI	Pull-Down		

Booting Mode

Pin	Default	SPI Flash Boot	Download Boot
GPIO0	Pull-Up		
GPIO2	Pull-Down	Don't-care	

Debugging Log on U0TXD During Booting

Pin	Default	U0TXD Toggling	U0TXD Silent
GPIO15/MTDO	Pull-Up		

Timing of SDIO Slave

Pin	Default	Falling-edge Input Falling-edge Output	Falling-edge Input Rising-edge Output	Rising-edge Input Falling-edge Output	Rising-edge Input Rising-edge Output
GPIO15/MTDO	Pull-Up	0	0	1	1
GPIO5	Pull-Up	0	1	0	1

Fiducials

FID1 Fiducial	FID2 Fiducial	FID3 Fiducial
FID4 Fiducial	FID5 Fiducial	FID6 Fiducial