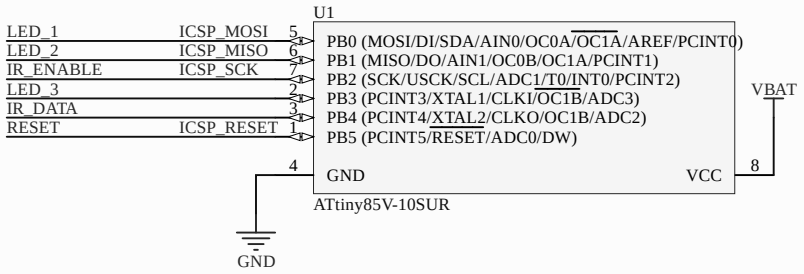
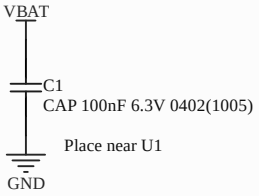
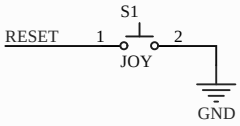
hope

MCU

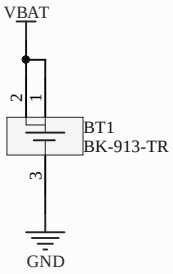




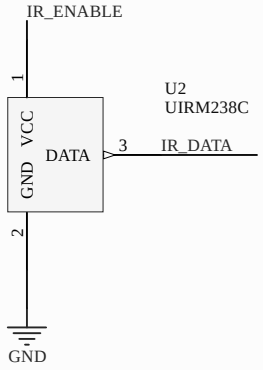


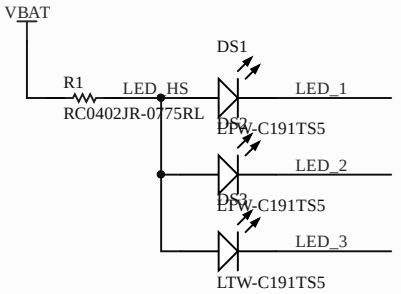
Note: After reset, processor flashes LEDs

Battery

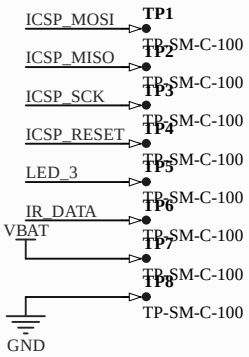


I/O





Test Points



Title: Hope Badge		
Project: Hope Badge.PrjPcb		Revision: C
Date: 2018-05-12	Time: 11:34:19	Sheet 1 of 1

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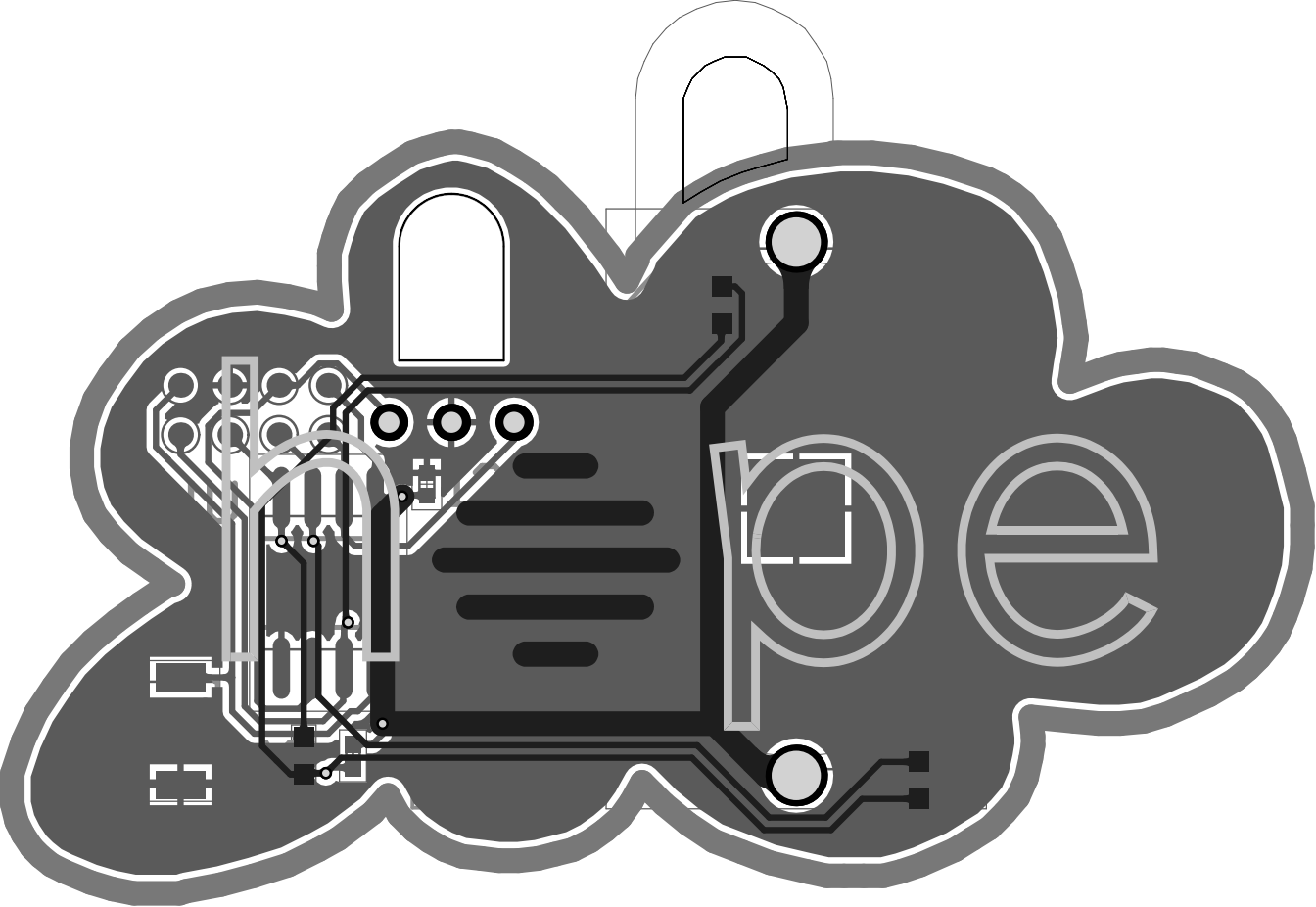


Design Rules Verification Report

Filename : C:\Users\matt\Blinkinlabs-Repos\HopeBadge\pcb\Hope Badge.PcbDoc

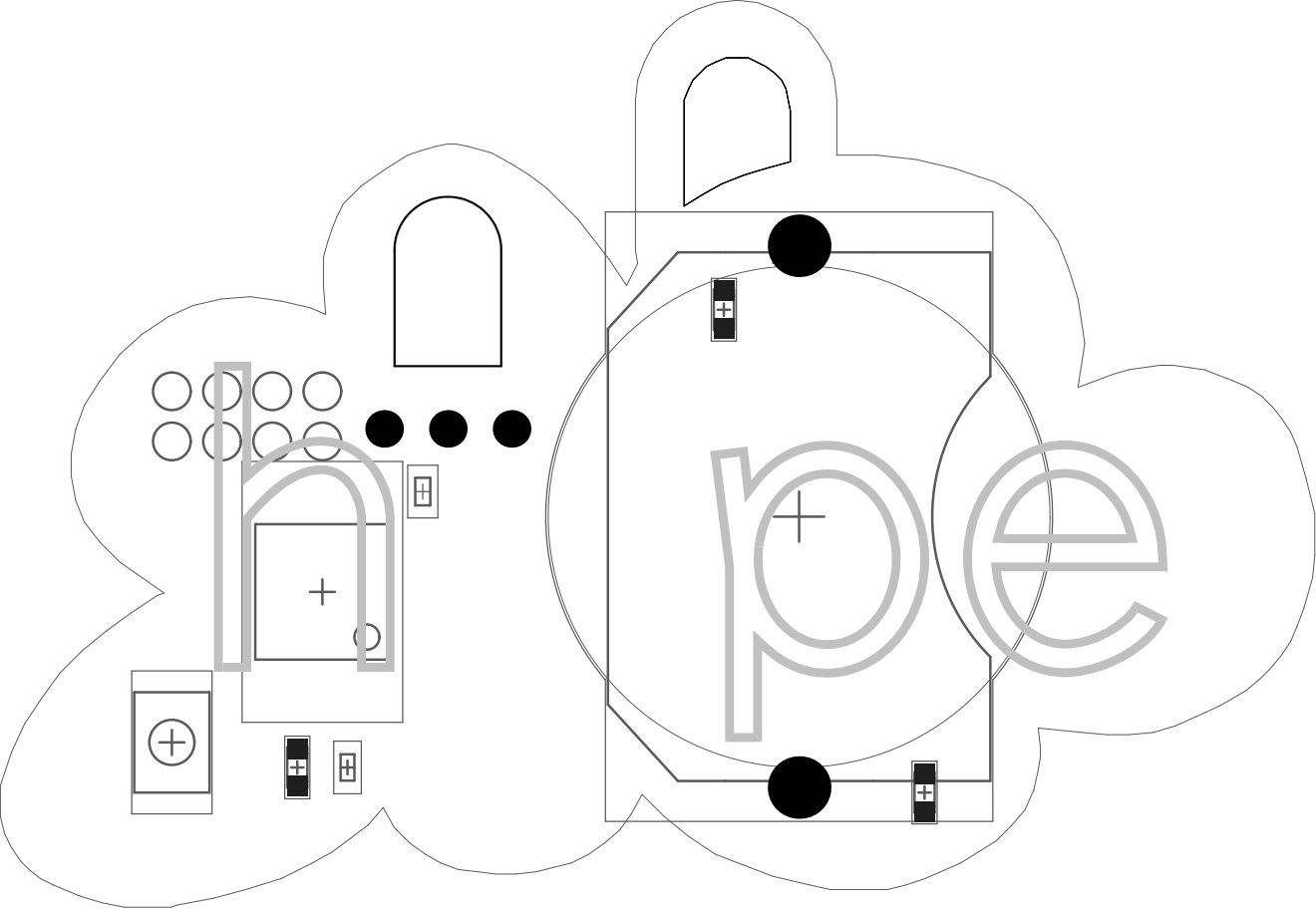
Warnings 0
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.25mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.254mm) (Max=1mm) (Preferred=0.254mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Hole Size Constraint (Min=0.025mm) (Max=2.54mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.25mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.254mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
Warning	Hope Badge.SchDoc	ICSP_SCK contains IO Pin and Power Pin objects (Pin U2-1,Pin U1-7)
Warning	Hope Badge.SchDoc	IR_DATA contains IO Pin and Output Pin objects (Pin U1-3,Pin U2-3)
Warning	Hope Badge.SchDoc	Nets Wire ICSP_MISO has multiple names (Net Label ICSP_MISO,Net Label ICSP_MISO,Net Label LED_2,Net Label LED_2)
Warning	Hope Badge.SchDoc	Nets Wire ICSP_MOSI has multiple names (Net Label ICSP_MOSI,Net Label ICSP_MOSI,Net Label LED_1,Net Label LED_1)
Warning	Hope Badge.SchDoc	Nets Wire ICSP_RESET has multiple names (Net Label ICSP_RESET,Net Label ICSP_RESET,Net Label RESET,Net Label RESET)
Warning	Hope Badge.SchDoc	Nets Wire ICSP_SCK has multiple names (Net Label ICSP_SCK,Net Label ICSP_SCK,Net Label IR_ENABLE,Net Label IR_ENABLE)



U2
BT1
U1
C1
CR2032
BLINKIN LABS
JOY
R1