





**Design Rules Verification Report**Filename: C:\Users\blinkinlabs\Blinkinlabs-Repos\LeoBlinky2018\pcb-ENT\LeoBlinky2018-

Warnings 0 Rule Violations 177

Warnings	
Total	0

Rule Violations			
Clearance Constraint (Gap=0.2mm) (All),(All)	44		
Clearance Constraint (Gap=0.2mm) (IsText),(All)			
Short-Circuit Constraint (Allowed=No) (All),(All)	0		
Un-Routed Net Constraint ( (All) )			
Modified Polygon (Allow modified: No), (Allow shelved: No)	0		
Width Constraint (Min=0.2mm) (Max=2mm) (Preferred=0.4mm) (All)	0		
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor	0		
Moder SQ2 5 to Method int (Min=0.025mm) (Max=3.6mm) (All)			
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0		
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	20		
Minimum Solder Mask Sliver (Gap=0mm) (OnLayer('Top Solder')),(All)	0		
Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)	113		
Silk to Silk (Clearance=0.254mm) (All),(All)	0		
Net Antennae (Tolerance=0mm) (All)	0		
Room LeoBlinky2018-ENT (Bounding Region = (187.45mm, 146.5mm, 378.85mm, 219.5mm)			
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0		
Total	177		

Clearance Constraint (Gap=0.2mm) (All),(All) Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-1(72.565mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-10(78.28mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-10(78.28mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-11(78.915mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-13(79.55mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-14(78.915mm.30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-15(78.28mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-16(77.645mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-17(77.01mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-18(76.375mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-19(75.74mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-2(73.2mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-20(75.105mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-21(74.47mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-22(73.835mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-23(73.2mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-3(73.835mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-4(74.47mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-5(75.105mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-6(75.74mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-7(76.375mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U1-8(77.01mm.36,225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-1(86.508mm, 36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-10(92.223mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-10(92.223mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-11(92.858mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-13(93.493mm,30.775mm) on Bottom Layer And Pad Clearance Constraint; (0.185mm < 0.2mm) Between Pad U7-14(92.858mm.30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-15(92.223mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-16(91.588mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-17(90.953mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-18(90.318mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-19(89.683mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-2(87.143mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-20(89.048mm.30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-21(88.413mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-22(87.778mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-23(87.143mm,30.775mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-3(87.778mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-4(88.413mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-5(89.048mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-6(89.683mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-7(90.318mm,36.225mm) on Bottom Layer And Pad Clearance Constraint: (0.185mm < 0.2mm) Between Pad U7-8(90.953mm,36.225mm) on Bottom Layer And Pad

#### Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All) Minimum Solder Mask Sliver Constraint: (0.077mm < 0.254mm) Between Pad C1-1(69.55mm,30.425mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.077mm < 0.254mm) Between Pad C3-1(50.895mm,28.45mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.077mm < 0.254mm) Between Pad C4-1(57.9mm,28.45mm) on Bottom Layer Mindmum Solder Mask Sliver Constraint: (0.077mm < 0.254mm) Between Pad C6-1(83.321mm,30.425mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad J3-D-(18.35mm,4.9mm) on Bottom Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad J3-D-(18.35mm,4.9mm) on Bottom Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad J3-D+(17.7mm,4.9mm) on Bottom Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad J3-GND(16.4mm,4.9mm) on Bottom Layer Mindmum Solder Mask Sliver Constraint: (0.122mm < 0.254mm) Between Pad J3-GND(16.4mm,4.9mm) on Bottom Layer MMMmum Solder Mask Sliver Constraint: (0.122mm < 0.254mm) Between Pad J3-S4(20.125mm,4.9mm) on Multi-Layer And Minimum Solder Mask Sliver Constraint: (0.247mm < 0.254mm) Between Pad R1-1(69.5mm,31.95mm) on Bottom Layer Mindmum Solder Mask Sliver Constraint: (0.247mm < 0.254mm) Between Pad R2-1(44.675mm,34.25mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.247mm < 0.254mm) Between Pad R3-1(44.675mm,32.225mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.247mm < 0.254mm) Between Pad R4-1(83.271mm,31.95mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U2-1(43.061mm,33.125mm) on Bottom Laver Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U2-2(42.111mm,33.125mm) on Bottom Layer Minimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U5-1(16.85mm,6.825mm) on Bottom Layer Mindimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U5-2(15.9mm,6.825mm) on Bottom Layer Mindimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U5-4(14.95mm,9.575mm) on Bottom Layer Mindimum Solder Mask Sliver Constraint: (0.147mm < 0.254mm) Between Pad U5-5(15.9mm,9.575mm) on Bottom Layer And

## Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All) Silk To Solder Mask Clearance Constraint: (0.239mm < 0.254mm) Between Arc (43.911mm,33.125mm) on Bottom Overlay Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad B1-1(159.8mm,59.5mm) on Bottom Layer Still To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad B1-2(159.8mm,7.5mm) on Bottom Layer Still To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad B2-1(25mm,59.5mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (Collision < 0.254mm) Between Pad B2-2(25mm,7.5mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad C2-1(41.995mm,37.925mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad C2-1(41.995mm,37.925mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad C2-2(43.795mm,37.925mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.193mm < 0.254mm) Between Pad C2-2(43.795mm,37.925mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad C5-1(38.5mm,27.7mm) on Bottom Layer Still To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad C5-1(38.5mm,27.7mm) on Bottom Layer জীমি To Solder Mask Clearance Constraint: (0.175mm < 0.254mm) Between Pad C5-2(38.5mm.29.5mm) on Bottom Layer জীমি To Solder Mask Clearance Constraint: (0.193mm < 0.254mm) Between Pad C5-2(38.5mm,29.5mm) on Bottom Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS10-1(91.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS10-1(91.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS10-2(88.4mm, 2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS10-2(88.4mm, 2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS1-1(8.4mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS1-1(8.4mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS11-1(71.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS11-1(71.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS11-2(68.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS11-2(68.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS1-2(11.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS1-2(11.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS12-1(51.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS12-1(51.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS12-2(48.4mm, 2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS12-2(48.4mm, 2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS13-1(31.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS13-1(31.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS13-2(28.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS13-2(28.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS14-1(11.6mm,2.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS14-1(11.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS14-2(8.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS14-2(8.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.248mm < 0.254mm) Between Pad DS15-1(148.15mm,64.5mm) on Top Layer Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS15-1(148.15mm,64.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS15-2(151.85mm,64.5mm) on Top Layer Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS15-2(151.85mm,64.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.248mm < 0.254mm) Between Pad DS16-1(168.15mm,64.5mm) on Top Layer Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS16-1(168.15mm,64.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS16-2(171.85mm,64.5mm) on Top Layer Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS16-2(171.85mm,64.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS17-1(151.85mm,2.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS17-1(151.85mm,2.5mm) on Top Layer জীমি To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS17-2(148.15mm,2.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS17-2(148.15mm,2.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS18-1(171.85mm,2.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS18-1(171.85mm,2.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS18-2(168.15mm, 2.5mm) on Top Layer

## Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All) Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS18-2(168.15mm,2.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS2-1(28.4mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS2-1(28.4mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS2-2(31.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS2-2(31.6mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS3-1(48.4mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS3-1(48.4mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS3-2(51.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS3-2(51.6mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS4-1(68.4mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS4-1(68.4mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS4-2(71.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS4-2(71.6mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS5-1(88.4mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS5-1(88.4mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS5-2(91.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS5-2(91.6mm,64.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS6-1(108.4mm,64.5mm) on Top Layer জামি To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS6-1(108.4mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS6-2(111.6mm,64.5mm) on Top Layer জামি To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS6-2(111.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.254mm < 0.254mm) Between Pad DS7-1(128.4mm,64.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS7-1(128.4mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.192mm < 0.254mm) Between Pad DS7-2(131.6mm,64.5mm) on Top Layer Still To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS7-2(131.6mm,64.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS8-1(131.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS8-1(131.6mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS8-2(128.4mm, 2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS8-2(128.4mm, 2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS9-1(111.6mm,2.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS9-1(111.6mm,2.5mm) on Top Layer And Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS9-2(108.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad DS9-2(108.4mm,2.5mm) on Top Layer Anc Silk To Solder Mask Clearance Constraint: (0.179mm < 0.254mm) Between Pad L1-1(38.611mm,34.5mm) on Bottom Laye Silk To Solder Mask Clearance Constraint: (0.182mm < 0.254mm) Between Pad L1-1(38.611mm,34.5mm) on Bottom Laye Silk To Solder Mask Clearance Constraint: (0.177mm < 0.254mm) Between Pad L1-2(38.611mm,35.9mm) on Bottom Laye Silk To Solder Mask Clearance Constraint: (0.185mm < 0.254mm) Between Pad L1-2(38.611mm,35.9mm) on Bottom Laye Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW1-1(35.25mm,16.25mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW1-1(35.25mm,16.25mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW1-2(42.75mm,16.25mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW1-2(42.75mm,16.25mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW1-3(35.25mm,11.75mm) on Bottom Layer Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW1-3(35.25mm,11.75mm) on Bottom Layer Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW1-4(42.75mm,11.75mm) on Bottom Laver Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW1-4(42.75mm,11.75mm) on Bottom Laye Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad SW1-5(35.5mm,14mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad SW1-5(35.5mm,14mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW2-1(48.25mm,16.25mm) on Bottom SALETO Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW2-1(48.25mm,16.25mm) on Bottom SAKETO Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW2-2(55.75mm,16.25mm) on Bottom SAKETO Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW2-2(55.75mm,16.25mm) on Bottom SAKETO Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW2-3(48.25mm,11.75mm) on Bottom Layer

#### Silk To Solder Mask (Clearance=0.254mm) (IsPad),(All)

Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW2-3(48.25mm,11.75mm) on Bottom Layer Silk To Solder Mask Clearance Constraint: (0.15mm < 0.254mm) Between Pad SW2-4(55.75mm,11.75mm) on Bottom Layer Silk To Solder Mask Clearance Constraint: (0.25mm < 0.254mm) Between Pad SW2-4(55.75mm,11.75mm) on Bottom Layer Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad SW2-5(48.5mm,14mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad SW2-5(48.5mm,14mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad SW3-1(40.62mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.2mm < 0.254mm) Between Pad SW3-3(40.62mm,44mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-4(35.38mm,44mm) on Bottom Layer Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-4(35.38mm,44mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-4(35.38mm,44mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder Mask Clearance Constraint: (0.249mm < 0.254mm) Between Pad SW3-6(35.38mm,48mm) on Bottom Layer And Silk To Solder

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# **Electrical Rules Check Report**

Class	Document	Message
Warning	LeoBlinky2018-ENT.SchDoc	Net FLASH_CS has no driving source (Pin U3-2,Pin U4-1)
Warning	LeoBlinky2018-ENT.SchDoc	Net FLASH_SCK has no driving source (Pin U3-5,Pin U4-6)
Error	LeoBlinky2018-ENT.SchDoc	Net LED18 has only one pin (Pin U7-7)
Error	LeoBlinky2018-ENT.SchDoc	Net LED19 has only one pin (Pin U7-8)
Error	LeoBlinky2018-ENT.SchDoc	Net LED20 has only one pin (Pin U7-9)
Error	LeoBlinky2018-ENT.SchDoc	Net LED21 has only one pin (Pin U7-10)
Error	LeoBlinky2018-ENT.SchDoc	Net LED22 has only one pin (Pin U7-11)
Error	LeoBlinky2018-ENT.SchDoc	Net LED23 has only one pin (Pin U7-12)
Error	LeoBlinky2018-ENT.SchDoc	Net LED24 has only one pin (Pin U7-13)
Error	LeoBlinky2018-ENT.SchDoc	Net LED25 has only one pin (Pin U7-14)
Error	LeoBlinky2018-ENT.SchDoc	Net LED26 has only one pin (Pin U7-15)
Error	LeoBlinky2018-ENT.SchDoc	Net LED27 has only one pin (Pin U7-16)
Error	LeoBlinky2018-ENT.SchDoc	Net LED28 has only one pin (Pin U7-17)
Error	LeoBlinky2018-ENT.SchDoc	Net LED29 has only one pin (Pin U7-18)
Error	LeoBlinky2018-ENT.SchDoc	Net LED30 has only one pin (Pin U7-19)
Error	LeoBlinky2018-ENT.SchDoc	Net LED31 has only one pin (Pin U7-20)
Warning	LeoBlinky2018-ENT.SchDoc	Net LED_CLK has no driving source (Pin U1-3,Pin U3-16,Pin U7-3)
Warning	LeoBlinky2018-ENT.SchDoc	Net LED_GCLK has no driving source (Pin U1-21,Pin U3-7,Pin U7-21)
Warning	LeoBlinky2018-ENT.SchDoc	Net LED_LE has no driving source (Pin U1-4,Pin U3-17,Pin U7-4)
Warning	LeoBlinky2018-ENT.SchDoc	Net LED_MOSI has no driving source (Pin U1-2,Pin U3-8)
Error	LeoBlinky2018-ENT.SchDoc	Net RESET has only one pin (Pin U3-6)



