

A

A

B

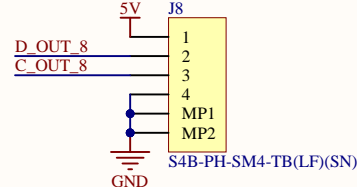
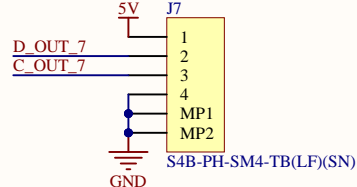
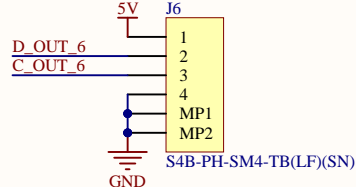
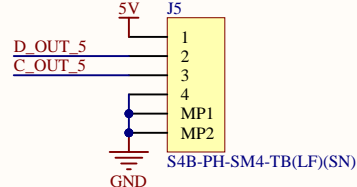
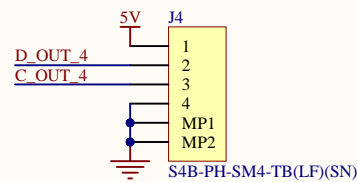
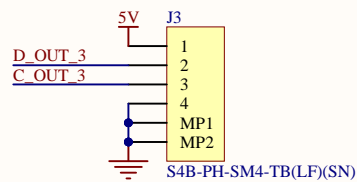
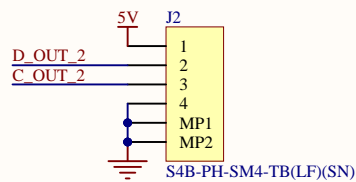
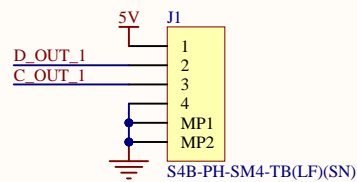
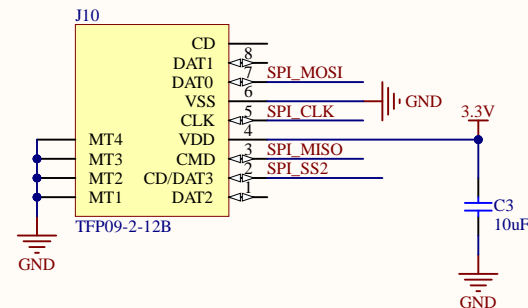
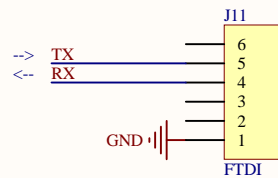
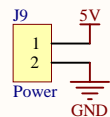
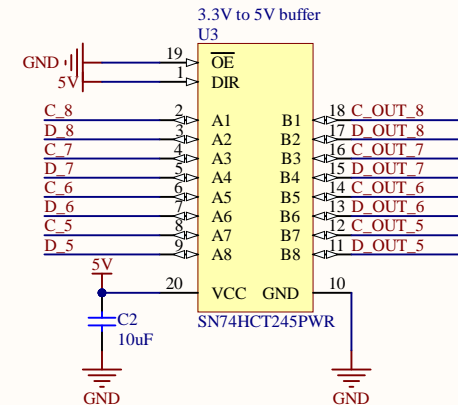
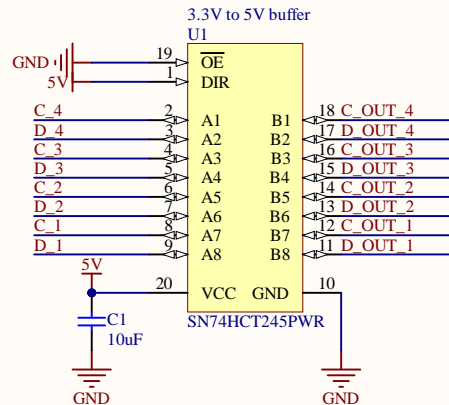
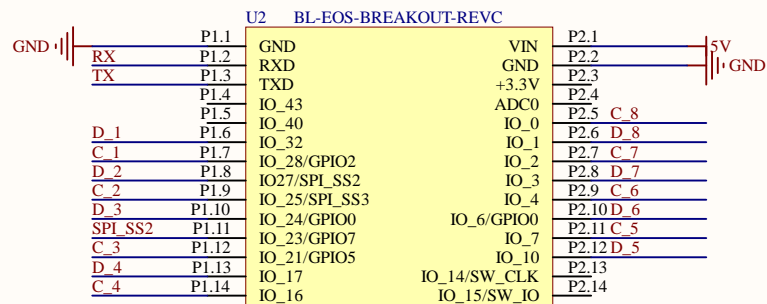
B


C

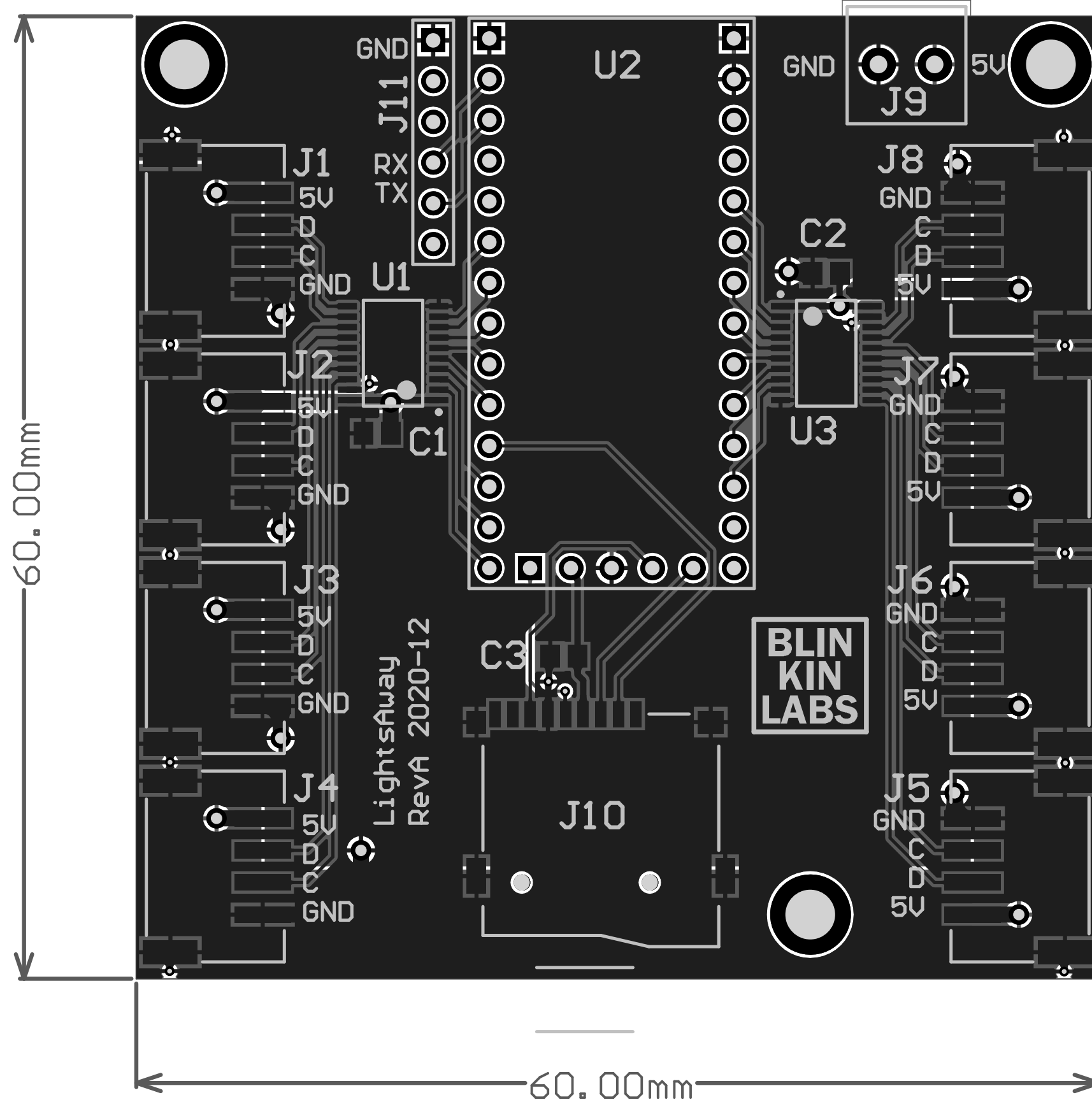
C

D

D

BLIN
KIN
LABS

Title: LightsAway		
Project: LightsAway	Revision: RevA	
Date: 12/1/2020 Time: 5:21:18 PM	Sheet 1 of 1	



Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.013mm	3.8
1	Top Layer	Copper	0.036mm	
	Dielectric 1	FR-4	1.102mm	4.8
2	Bottom Layer	Copper	0.036mm	
	Bottom Solder	Solder Resist	0.013mm	3.8
	Bottom Overlay			

Total board thickness: 1.199mm

Design Rules Verification Report

Filename : C:\Users\blinkinlabs\Blinkinlabs-Repos\QL-EOS-S3-breakout\LightMod\LightMod.

Warnings 0
Rule Violations 45

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.2mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint (All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.2mm) (Max=1mm) (Preferred=0.4mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.45mm)	0
Minimum Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	45
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	45

Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)

[illegible]

Electrical Rules Check Report

Class	Document	Message
		Successful Compile for LightMod.PrjPcb

