





Layer	Name	Material	Thickness	Constant
	Top Overlay			
	Top Solder	Solder Resist	0.013mm	3.8
1	Top Layer	Copper	0.036mm	
	Dielectric 1	FR-4	1.102mm	4.8
2	Bottom Layer	Copper	0.036mm	
	Bottom Solder	Solder Resist	0.013mm	3.8
	Bottom Overlay			

Total board thickness:

1.199mm

 $\label{lem:condition} \textbf{Design Rules Verification Report} \\ \textbf{Filename : C:\Users\blinkinlabs\Blinkinlabs-Repos\QL-EOS-S3-breakout\LightMod\LightMod}.}$ 

Warnings 0 Rule Violations 45

## Warnings Total 0

Rule Violations	
Clearance Constraint (Gap=0.2mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( (All) )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.2mm) (Max=1mm) (Preferred=0.4mm) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor	0
Working A5 ferrore arance (Gap=0.254mm) (AII),(AII)	0
Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All)	45
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Total	45

## Minimum Solder Mask Sliver (Gap=0.254mm) (All),(All) Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-1(31.15mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-2(30.05mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-3(28.95mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-4(27.85mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-5(26.75mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-6(25.65mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.197mm < 0.254mm) Between Pad J10-7(24.55mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.248mm < 0.254mm) Between Pad J10-8(23.45mm,16.5mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.1mm < 0.254mm) Between Pad J10-CD(22.35mm,16.5mm) on Top Layer And Maymum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-1(18.85mm, 36.075mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-10(18.85mm,41.925mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-11(13.15mm,41.925mm) on Top Layer Mindmum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-12(13.15mm,41.275mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-13(13.15mm,40.625mm) on Top Layer MM mum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-14(13.15mm,39.975mm) on Top Layer Mindmum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-15(13.15mm,39.325mm) on Top Layer Mindmum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-16(13.15mm,38.675mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-17(13.15mm,38.025mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-18(13.15mm,37.375mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-19(13.15mm,36.725mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-2(18.85mm,36.725mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-3(18.85mm.37.375mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-4(18.85mm, 38.025mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-5(18.85mm, 38.675mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-6(18.85mm,39.325mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-7(18.85mm,39.975mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U1-8(18.85mm,40.625mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-1(40.15mm.41.925mm) on Top Layer And Minimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-10(40.15mm, 36.075mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-11(45.85mm, 36.075mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-12(45.85mm,36.725mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-13(45.85mm,37.375mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-14(45.85mm, 38.025mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-15(45.85mm, 38.675mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad U3-16(45.85mm,39.325mm) on Top Layer Mindimum Solder Mask Sliver Constraint: (0.047mm < 0.254mm) Between Pad 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U3-8(40.15mm,37.375mm) on Top Layer And

## **Electrical Rules Check Report**

Class	Document	Message
		Successful Compile for LightMod.PrjPcb





