

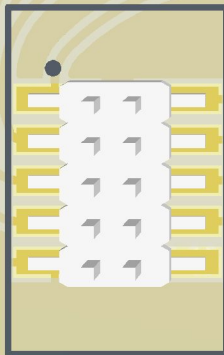


MicroMatrix Driver
2020-06 RevA

GND 5V

+5V
23
25
26
27
32
35
GND

P4



OUT3

P1

← Upduino
V2
goes
here →

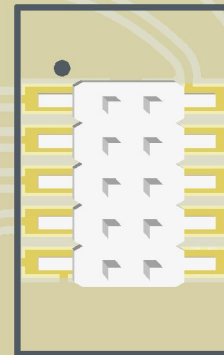
P2

P3



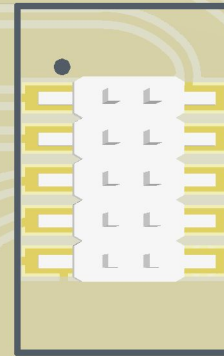
5V

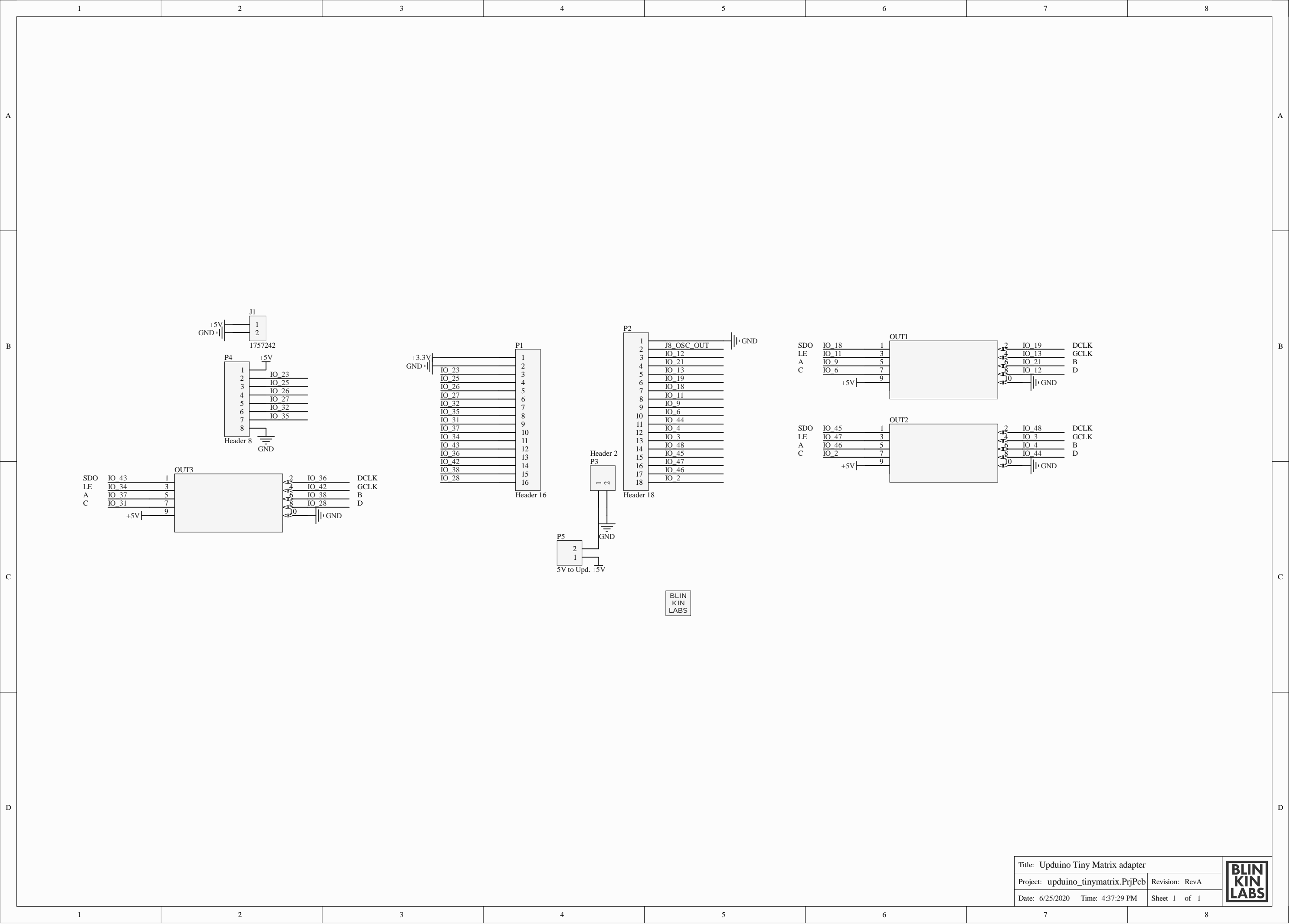
OUT2



to Upd.

OUT1





J1



GND

5V

+5V

23

25

26

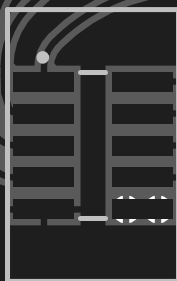
27

32

35

GND

P4



OUT3



P1

Upduino
V2
goes
here



**BLINK
LABS**

MicroMatrix Driver
2020-06 RevA



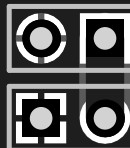
OUT1

OUT2



P2

P3



5V

to Upd.

Design Rules Verification Report

Filename : C:\Users\matt\Desktop\upduino_tinymatrix\upduino_tinymatrix.PcbDoc

Warnings 0
Rule Violations 42

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=10mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=10mil) (Max=60mil) (Preferred=10mil) (All)	0
Hole Size Constraint (Min=1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Minimum Solder Mask Sliver (Gap=10mil) (All),(All)	0
Silk To Solder Mask (Clearance=10mil) (IsPad),(All)	42
Silk to Silk (Clearance=10mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Room upduino_tinymatrix (Bounding Region = (4675mil, 2325mil, 6975mil, 4800mil)	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	42

Silk To Solder Mask (Clearance=10mil) (IsPad),(All)

[illegible]

Electrical Rules Check Report

Class	Document	Message
Error	upduino_tinymatrix.SchDoc	Net +3.3V has only one pin (Pin P1-1)
Error	upduino_tinymatrix.SchDoc	Net J8_OSC_OUT has only one pin (Pin P2-2)
Warning	upduino_tinymatrix.SchDoc	Off grid L? at 8007.223mil,3995.733mil
Warning	upduino_tinymatrix.SchDoc	Un-Designated Part L?

