

INDIAN INSTITUTE OF INFORMATION TECHNOLOGY KOTTAYAM Department of Computer Science and Engineering

FIRST MID SEMESTER EXAMINATION - DEC, 2022

COURSE TITLE: ICS111 IT Workshop I

Date & Time:17-12-2022 & 2:30 PM to 4 PM

Course Instructor: Dr P Victer Paul, Dr Goutam Mali, Dr Nandini

Max. Marks: 50 Batch: 1, 2 and 3

Answer all Questions

SECTION - A

[10x1=10]

- Which one of the following is not an internal component of the CPU?
 - a. Arithmetic unit b. Logic unit . Interface unit
- d. Control unit
- 2. Cache memory is used to transfer data between:
 - a. Main memory and secondary memory
 - b. Processor and an input device
 - Main memory and processor
 - d. Processor and an output device
- Which one of the following hardware components is normally used to accommodate secondary cache? a. Motherboard & Processor c. RAM
- d. Any secondary storage device 4. What is the purpose of the memory address register?
 - a. Stores the address of the next location in the main memory
 - b. Stores the address of the next location in the secondary memory
 - . Stores the address of the next location in the cache memory
 - d. Stores the address of an output device to which the data is to be sent
- Which of the following memory is the fastest type of memory?
 - a. Secondary memory b. Primary memory
 - Cache memory
- d. ROM
- 6. A device driver acts as an interface between:
 - a. End-user and I/O device
 - b. Application software and I/O device
 - c. Application software and operating system
 - A. Operating system and I/O device

| 7 | 7. A network that is restricted to use by a single organisation is referred to as | :: |
|----|--|---------|
| | A. LAN b. WAN c. Internet d. Intranet | |
| 8 | 3. WWW follow architecture | |
| | a. client server b. peer to peer c. 3 - tier d. Wireless | |
| _ | During the booting process, the CPU looks to for the first instructed start-up program. | tion in |
| | A. ROM BIOS b. RAM BIOS c. SRAM d. Cache | |
| 1 | 0. The process of restarting a computer | |
| | Marm boot b. Cold boot c. Hard Boot d. None of the ab | ove |
| | SECTION - B [4x5 | =20] |
| | Demonstrate the sequence of operations performed by the PC before the user i | s given |
| | Demonstrate at least ten directory and file manipulation MSDOS/Linux comth examples. | mands |
| 3. | Propose different factors to classify the computer networks and discuss various them using each identified factor. | s types |
| | How a computer can be identified uniquely over the internet and discuss its di presentations with limitations. | fferent |
| | SECTION - C [2x10 | =20] |
| 1. | | |
| | a. Describe the memory hierarchy in computers. | |
| | b. Compare the memory hierarchy based on | |
| | i. What they are made of ii. Access latency | |
| | iii. Cost | |
| | iv. Size. | (5+5) |
| 2. | | |
| 2. | a. List and describe the different ways in which the computer can be wired in network. | the |
| | b. Compare them based on the hop count analysis/ number of steps taken to | reach |
| | from any one node to any other node in the network. | (6+4) |
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