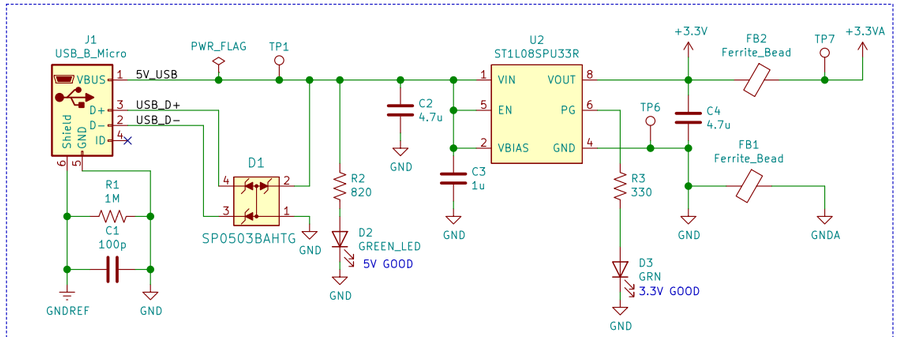
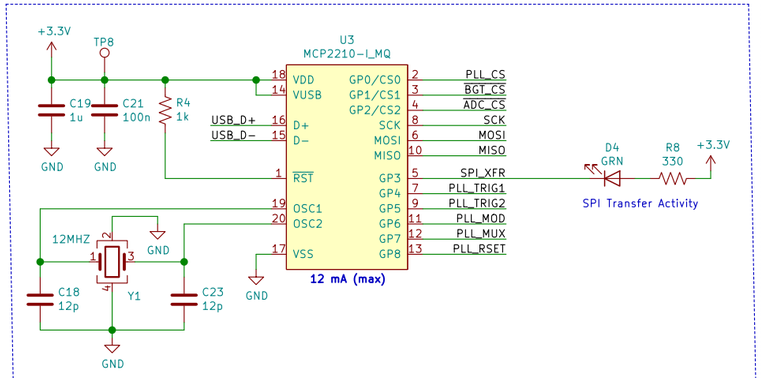
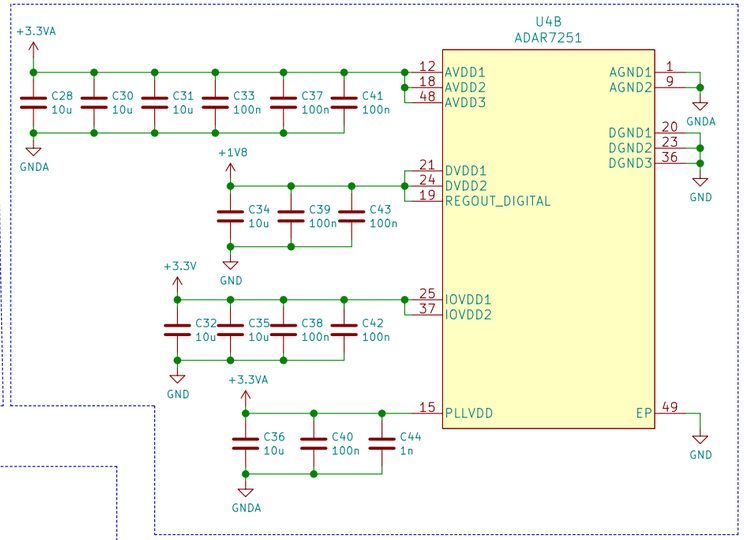
USB and +3.3V LDO



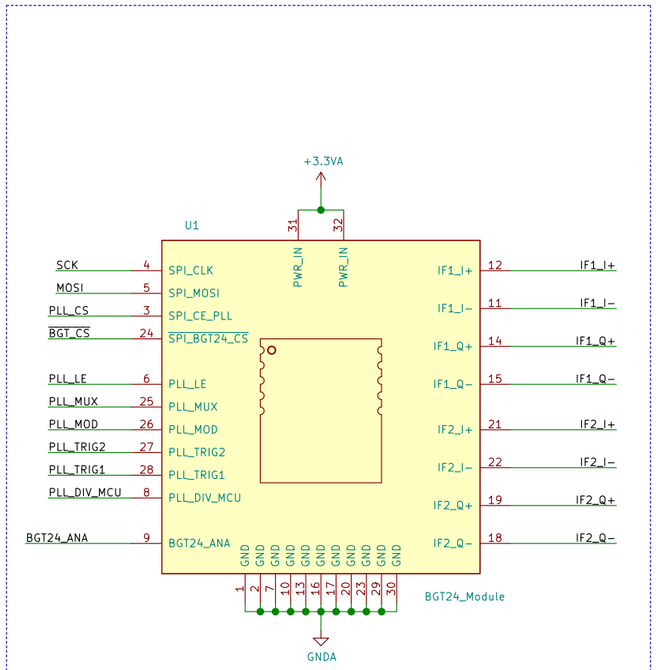
SPI Controller



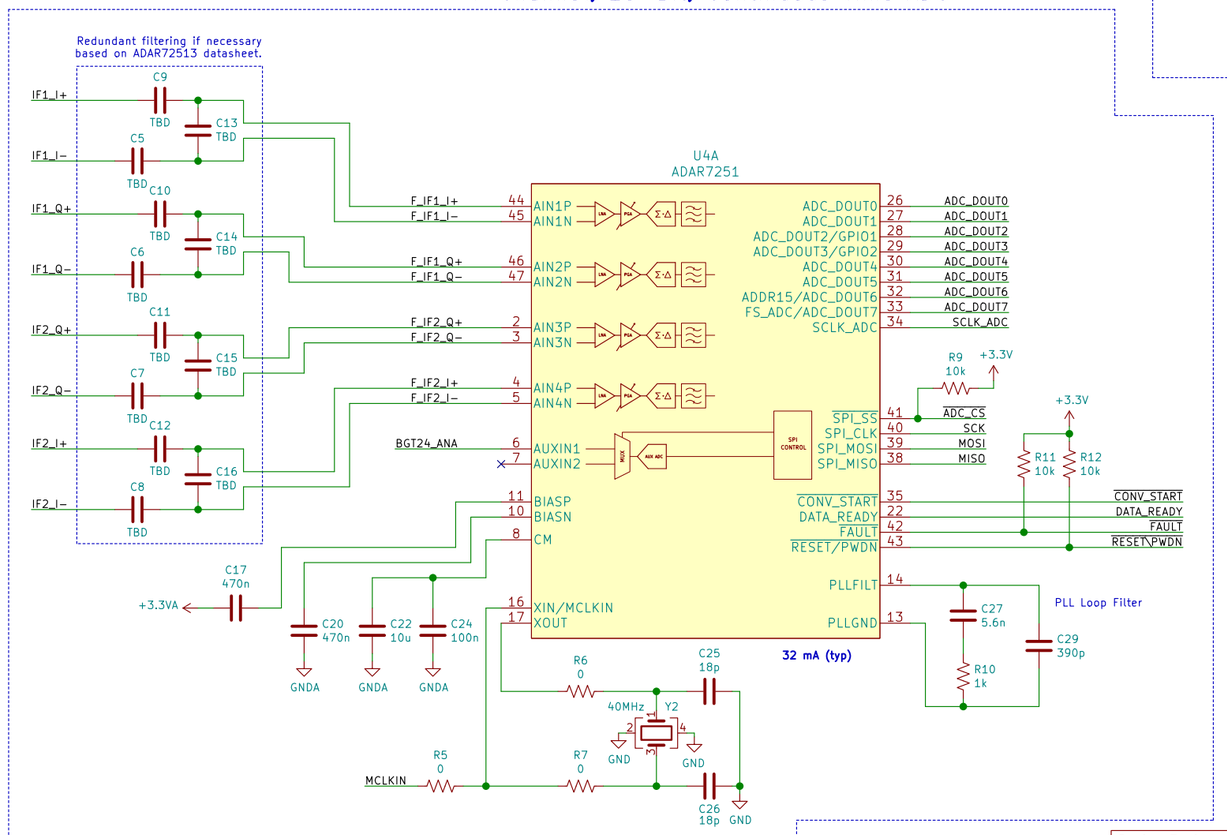
ADC Decoupling Bank



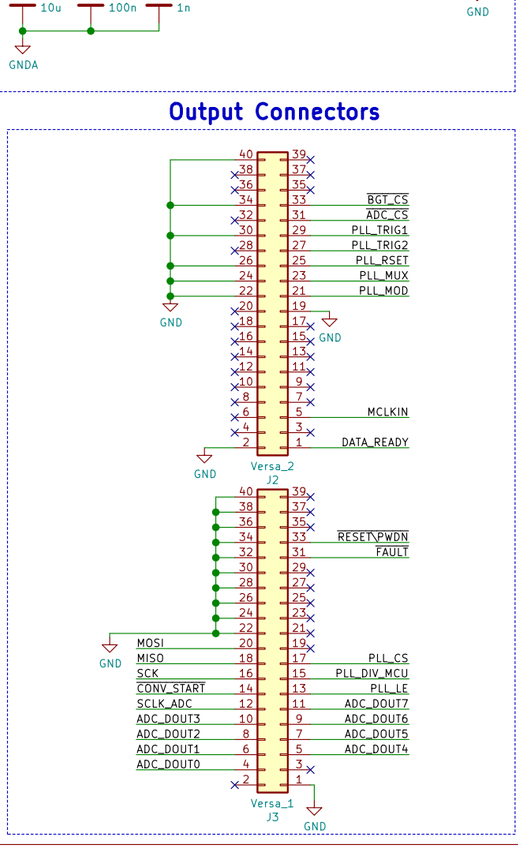
Radar MMIC – BGT24 Module



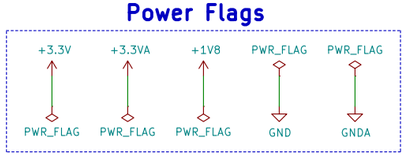
4−Channel, 16−Bit, Continuous Time ADC



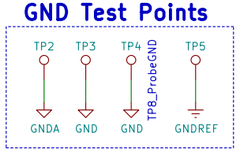
Output Connectors



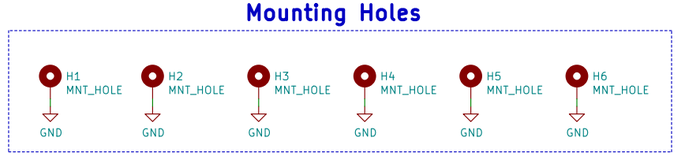
Power Flags



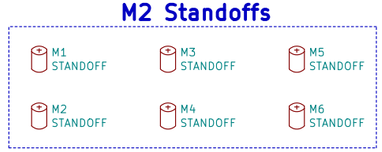
GND Test Points



Mounting Holes



M2 Standoffs



Design Engineers:
Austin S.
Leo L.
Prabodh J.

bloop

Sheet: /
File: spi_control.sch

Title: SPI Control

Size: B	Date: 2020−01−04	**Rev: A**
KiCad E.D.A. kicad (5.1.5)−3		Id: 1/1