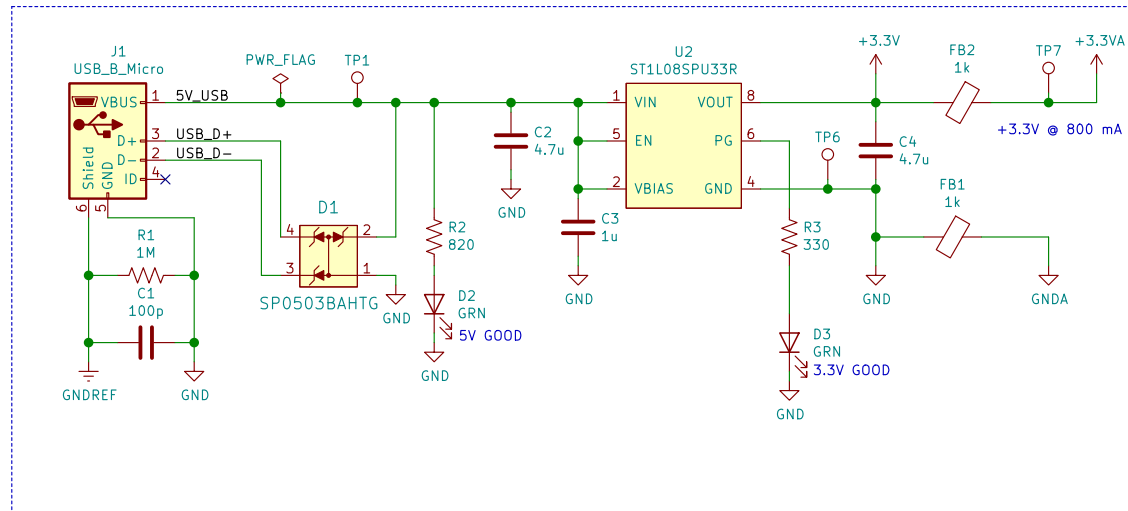
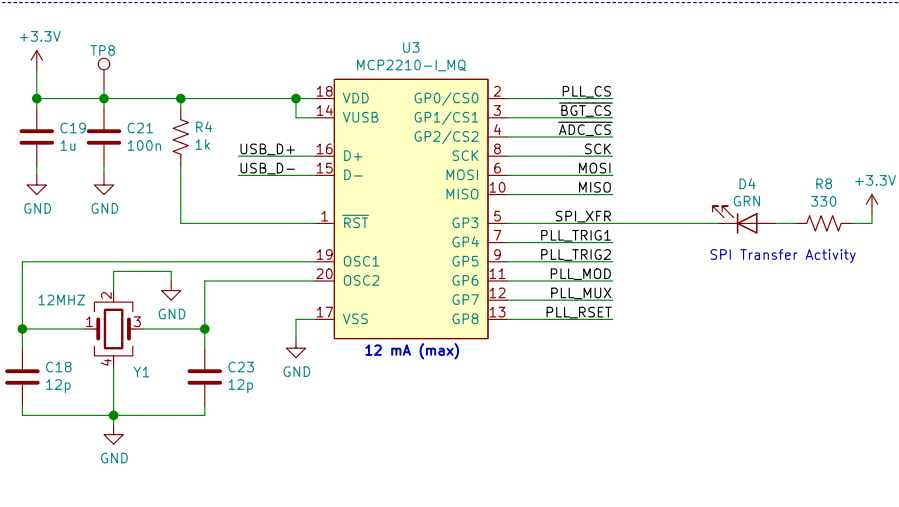


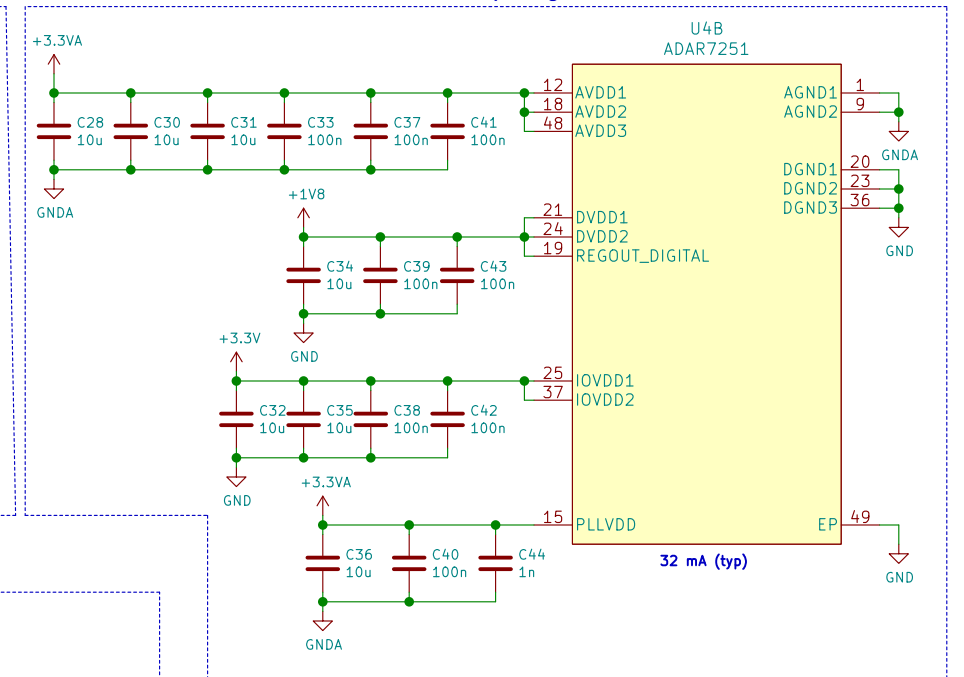
USB and +3.3V LDO



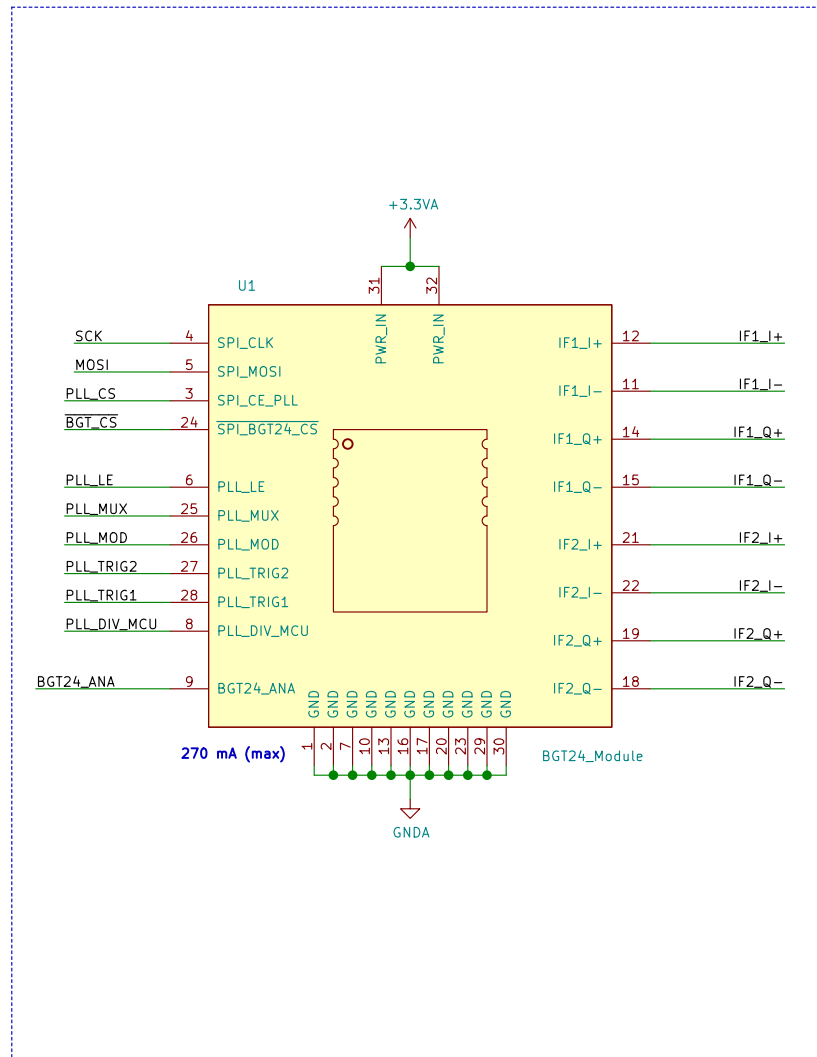
SPI Controller



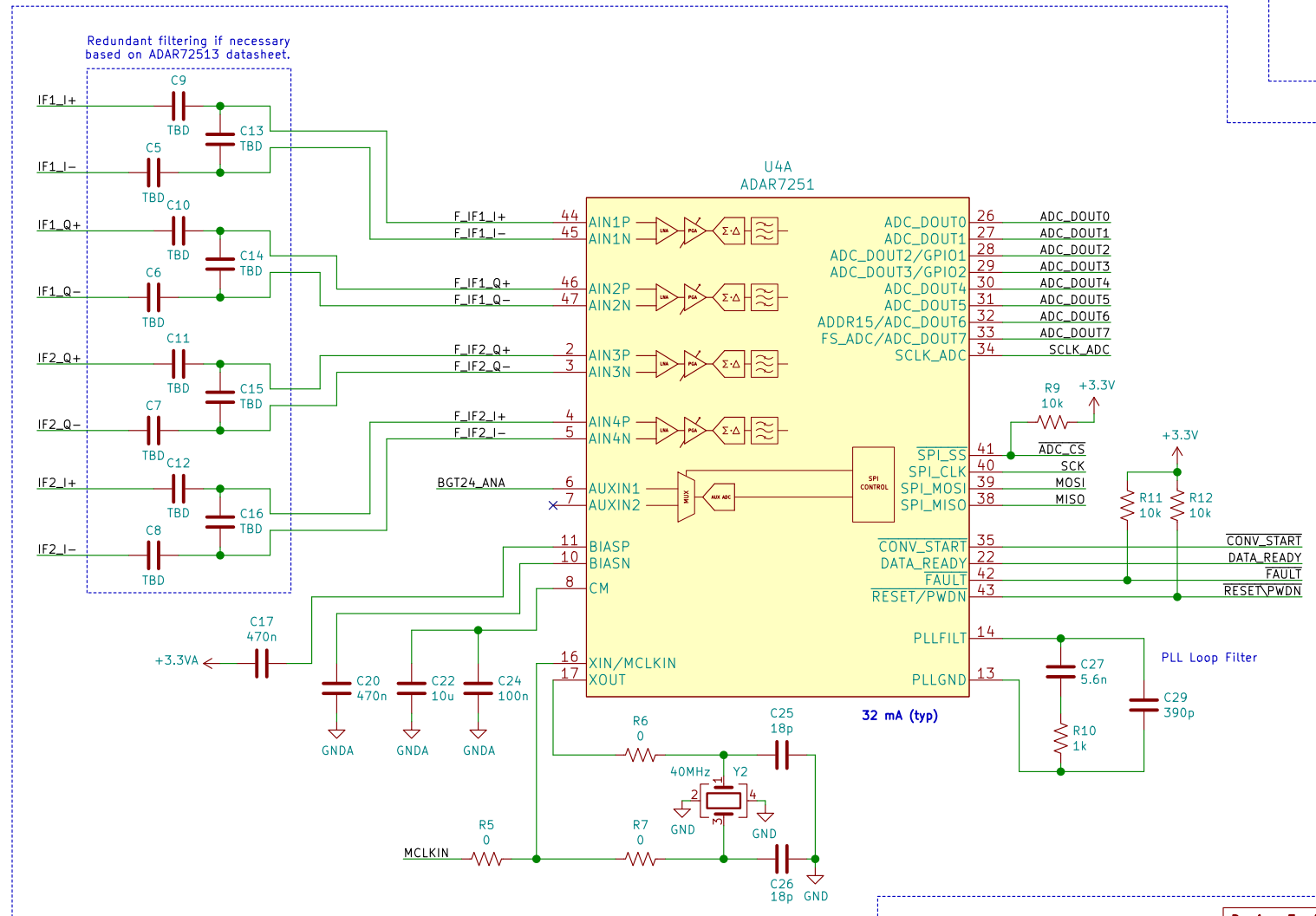
ADC Decoupling Bank



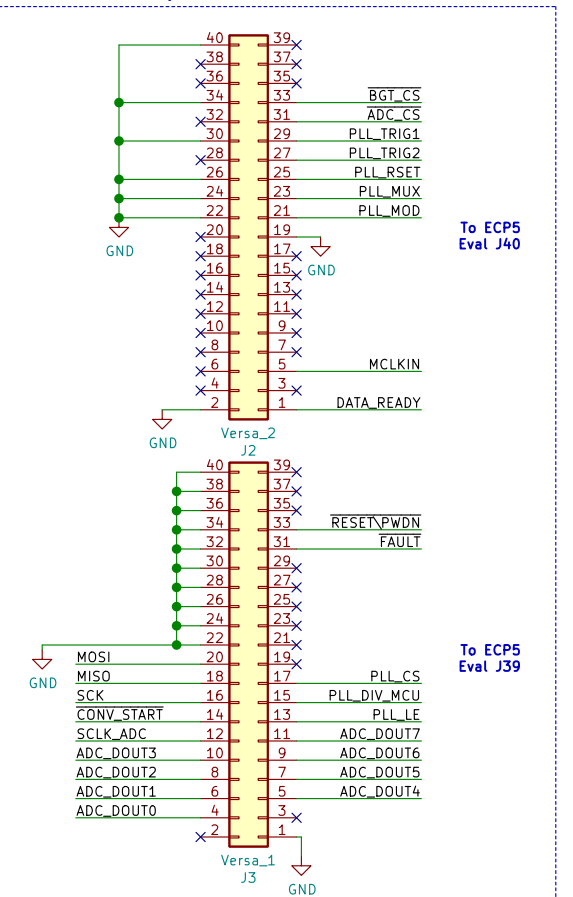
Radar MMIC – BGT24 Module



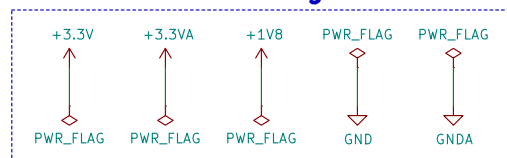
4-Channel, 16-Bit, Continuous Time ADC



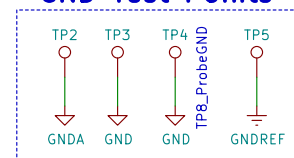
Output Connectors



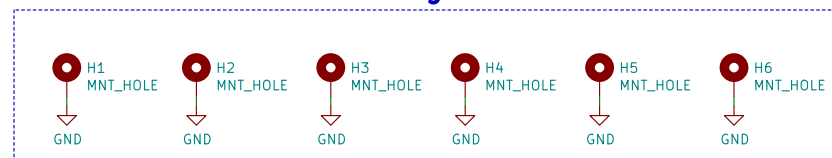
Power Flags



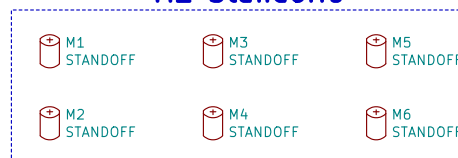
GND Test Points



Mounting Holes



M2 Standoffs



Design Engineers:

Austin S.
Leo L.
Prabodh J.

Sheet: /
File: spi_control.sch

Title: SPI Control

Size: B Date: 2020-01-05

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