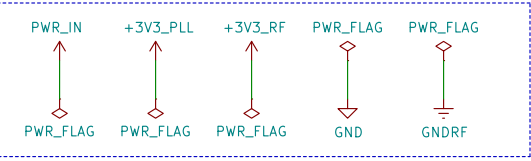


Power Flags



Castellated Pinout
- VIN: +3.3 VDC (typ)
- VIN: +3.6 VDC (max)
- I_{TOTAL} = 265 mA (typ)
- I_{TOTAL} = 325 mA (max)

RF and Analog
ground isolated

Design Engineers: Austin S. Leo L. Prabodh J.		
Sheet: / File: rf.sch		
Title: Radar MMIC and RF Front-End		
Size: B	Date: 2019-10-12	Rev: A
KiCad E.D.A. kicad (5.1.4)-1		Id: 1/1

