# **Vulkan Syncronization Notes**

[https://github.com/KhronosGroup/Vulkan-Docs/wiki/Synchronization-Examples]

### General

A 'vkCmdPipelineBarrier' covers all resources globally.

```
const VkMemoryBarrier mem_barrier = {
                  = VK_STRUCTURE_TYPE_MEMORY_BARRIER,
    .sType
                  = NULL,
    .pNext
    .srcAccessMask = VkAccessFlagBits,
    .dstAccessMask = VkAccessFlagBits,
};
const VkImageMemoryBarrier img_barrier = {
                        = VK_STRUCTURE_TYPE_IMAGE_MEMORY_BARRIER,
    .sType
    .pNext
                        = NULL.
    .srcAccessMask
                        = VkAccessFlagBits,
                        = VkAccessFlagBits,
    .dstAccessMask
                        = VkImageLayout,
    .oldLayout
    .newLayout
                        = VkImageLayout,
    .srcQueueFamilyIndex = (uint32_t)VK_QUEUE_FAMILY_IGNORED or a queue index,
    .dstQueueFamilyIndex = (uint32_t)VK_QUEUE_FAMILY_IGNORED or a queue index,
                        = VkImage,
    .image
    .subresourceRange = {
                       = VkImageAspectFlagBits,
        .aspectMask
        .baseMipLevel = uint32_t,
                    = uint32_t,
        .levelCount
        .baseArrayLayer = uint32_t,
        .layerCount
                       = uint32_t,
    }
};
const VkSubpassDependency sub_pass_dep = {
    .srcSubpass
                    = uint32_t,
    .dstSubpass
                    = uint32_t
    .srcStageMask
                    = VkPipelineStageFlagBits,
    .dstStageMask
                    = VkPipelineStageFlagBits,
    .srcAccessMask = VkAccessFlagBits,
    .dstAccessMask = VkAccessFlagBits,
    .dependencyFlags = VK_DEPENDENCY_BY_REGION_BIT,
};
'VK_DEPENDENCY_BY_REGION_BIT' means that you only read / write pixels you own in a single sa
```

This would not work if you had a blur shader that read outside of that area.

```
const VkBufferMemoryBarrier buf_mem_barrier = {
                          = VK_STRUCTURE_TYPE_BUFFER_MEMORY_BARRIER,
    .sType
    .pNext
                          = NULL,
                          = VkAccessFlagBits,
    .srcAccessMask
                          = VkAccessFlagBits,
    .dstAccessMask
    .srcQueueFamilyIndex = uint32_t,
    .dstQueueFamilyIndex = uint32_t,
    .buffer
                          = VkBuffer,
    .offset
                          = VkDeviceSize,
                          = VkDeviceSize,
    .size
};
```

## Compute -> Compute

### Write -> Read

Using a "vkCmdPipelineBarrier" inbetween two 'vkCmdDispatch' protect against read after write.

'VK\_PIPELINE\_STAGE\_COMPUTE\_SHADER\_BIT' for both src and dst stage. 'VK\_ACCESS\_SHADER\_WRITE\_BIT' for srcAccessMask 'VK\_ACCESS\_SHADER\_READ\_BIT' for dstAccessMask

### Read -> Write

A simpler execution dependency solves the write after read issue. 'vkCmd-PipelineBarrier' with no memory barrier.

### Compute -> Graphics

```
Write (C) -> Read (G)
```

Same as Compute -> Compute for this situation.

Write 
$$(C) \rightarrow Read(C) \rightarrow Read(C)$$

Nearly the same but adds more flags thus batching rather than using two barriers. Or the 'dstStageMask' and 'dstAccessMask' so that it includes both of the following ops.

## Image based writes and reads

Use 'VkImageMemoryBarrier' with a correct layout transition. This is only if the Draw wants to view it as an image other wise mem\_bariier is fine.

## Graphics -> Compute

Use a 'VkImageMemoryBarrier' with the correct stages and access masks. Super simple.

## Graphics -> Graphics

Use a 'VkSubpassDependency' if you can. "VkAttachmentDescription" does implicit layout transitions.

Otherwise use a 'VkImageMemoryBarrier'.

#### WAR Hazard

You would normally only need an execution dep but since you need a layout transition then you need an 'VkImageMemoryBarrier' with an empty 'srcAccess-Mask'.

### WAW Hazard (RP -> RP reusing the same depth buffer)

Always needs a memory dep. Needs use of 'VK\_SUBPASS\_EXTERNAL' because of the automatic transition. Use pipeline stages with the fragments tests as those use the depth buffer. .dependencyFlags should be 0.

## **Memory Transfer**

### Staging buffer -> Device Local Memory

Start with a 'vkCmdCopyBuffer'. Then:

If the queue is unified a normal 'vkCmdPipelineBarrier' is fine.

Otherwise: A 'VkBufferMemoryBarrier' is needed. End and submit to the queue. (Must have a semaphore the gfx queue waints on) Begin commands Another 'VkBufferMemoryBarrier' with the correct dst Access. End cmd and submit to gfx queue.

Images have the same story except they can use 'VkImageMemory-Barrier' as they also have the family queue indices field. (unified uses: VK\_QUEUE\_FAMILY\_IGNORED)

## External Deps

Variables: \$first\_subpass\_that\_uses\_attachments: 0 \$last\_subpass\_that\_uses\_attachments : #num\_subpasses - 1 \$first\_need\_for\_layout: VK\_PIPELINE\_STAGE\_COLOR\_ATTACHMENT\_OUTPU \$last\_need\_for\_layout: VK\_PIPELINE\_STAGE\_COLOR\_ATTACHMENT\_OUTPUT\_BIT;

// When to do layout transitions: dependencies[0].srcSubpass = VK\_SUBPASS\_EXTERNAL; dependencies[0].dstSubpass =  $first_subpass_that_uses_attachments$ ; dependencies[0].srcStageMask = VK\_PIPELINE\_STAGE\_ALL\_COMMANDS\_BIT;

```
dependencies[0].dstStageMask =
                            $first need for layout;
                                                 dependen-
cies[0].srcAccessMask = VK\_ACCESS\_MEMORY\_READ\_BIT; dependen-
cies[0].dstAccessMask = VK ACCESS COLOR ATTACHMENT READ BIT
   VK ACCESS COLOR ATTACHMENT WRITE BIT;
                                                 dependen-
cies[0].dependencyFlags = VK DEPENDENCY BY REGION BIT;
dependencies[1].srcSubpass
                      =
                           $last subpass that uses attachments;
dependencies[1].dstSubpass = VK SUBPASS EXTERNAL; dependen-
cies[1].srcStageMask = $last need for layout; dependencies[1].dstStageMask =
VK PIPELINE STAGE ALL COMMANDS BIT; dependencies[1].srcAccessMask
= VK ACCESS COLOR ATTACHMENT READ BIT | VK ACCESS COLOR ATTACHMENT WRIT
dependencies[1].dstAccessMask = VK_ACCESS_MEMORY_READ_BIT;
dependencies[1].dependencyFlags = VK_DEPENDENCY_BY_REGION_BIT;
                         —-// // Required Layout Table for Access
from Shaders // // -
                                         --// // (1) Storage
Image // VK_IMAGE_LAYOUT_GENERAL // // (2) Sampled Image: //
VK_IMAGE_LAYOUT_DEPTH_READ_ONLY_STENCIL_ATTACHMENT_OPTIMAL
// VK IMAGE LAYOUT DEPTH ATTACHMENT STENCIL READ ONLY OPTIMAL
// VK_IMAGE_LAYOUT_DEPTH_STENCIL_READ_ONLY_OPTIMAL
    VK IMAGE LAYOUT SHADER READ ONLY OPTIMAL
VK_IMAGE_LAYOUT_GENERAL // // (3) Combined Image Sampler: //
VK IMAGE LAYOUT DEPTH READ ONLY STENCIL ATTACHMENT OPTIMAL
// VK IMAGE LAYOUT DEPTH ATTACHMENT STENCIL READ ONLY OPTIMAL
// VK_IMAGE_LAYOUT_DEPTH_STENCIL_READ_ONLY_OPTIMAL
    VK_IMAGE_LAYOUT_SHADER_READ_ONLY_OPTIMAL
VK_IMAGE_LAYOUT_GENERAL // // (4) Input Attachment:
VK_IMAGE_LAYOUT_DEPTH_READ_ONLY_STENCIL_ATTACHMENT_OPTIMAL
// VK_IMAGE_LAYOUT_DEPTH_ATTACHMENT_STENCIL_READ_ONLY_OPTIMAL
// VK_IMAGE_LAYOUT_DEPTH_STENCIL_READ_ONLY_OPTIMAL
    VK_IMAGE_LAYOUT_SHADER_READ_ONLY_OPTIMAL
VK_IMAGE_LAYOUT_GENERAL
```