

GPIO	SYS	TIM1	TIM2	TIM3	TIM4	TIM5	TIM8	I2C1	I2C2	I2S2	I2S3	SPI1	SPI2	SPI3	USART1	USART2	USART3	CAN	DAC	ADC	引出	配置
PA	0		CH1			CH1										CTS				ADC123_IN0	X	KEY_WU
	1		CH2			CH2										RTS				ADC123_IN1	O	
	2		CH3			CH3										TX				ADC123_IN2	O	
	3		CH4			CH4										RX				ADC123_IN3	O	
	4											NSS				CK			DAC1	ADC12_IN4	O	
	5											SCK							DAC2	ADC12_IN5	O	
	6											MISO								ADC12_IN6	O	
	7			CH1		CH2						MOSI								ADC12_IN7	O	
	8		CH1												CK						O	
	9		CH2												TX						O	UART_TX
	10		CH3												RX						O	UART_RX
	11		CH4												CTS			RX			O	
	12														RTS			TX			O	
	13	JTMS																			O	JTMS
	14	JTCK																			O	JTCK
	15	JTDI								WS	NSS			NSS							O	
PB	0			CH3																ADC12_IN8	O	
	1			CH4																ADC12_IN9	O	
	2	BOOT																			X	KEY_BO
	3	JTDO	CH2							CK		SCK		SCK							O	
	4	JTRST		CH1						SD		MISO		MISO							O	
	5			CH2								MOSI		MOSI							O	
	6																				O	
	7					CH1		SCL							TX						O	
	8					CH2		SDA							RX						O	
	9					CH3		SCL						NSS					RX		O	
	10		CH3														TX		TX		O	
	11		CH4					SDA									RX				O	
	12									WS							CK				O	
	13									CK							SCK				O	
	14																MISO				O	
	15									SD							MOSI				O	
PC	13																				X	LED_R
	14																				X	LED_G
	15																				X	LED_B

\*粗體 + 斜體為 Remap 功能