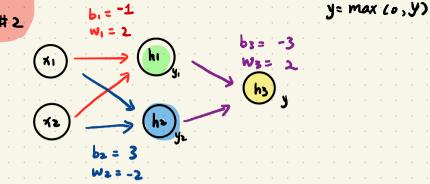
# 1



From the above table, we know that he is an OR gate

he is a NAND gate